

Model Name: T260XW06 V4

Issue Date: 2010/09/07

(*) Preliminary Specifications
() Final Specifications

Customer Signature	Date	AUO	Date
Approved By _____		Approval By PM Director Yen Ting Chiu _____	
Note		Reviewed By RD Director Eugene CC Chen _____	
		Reviewed By Project Leader Gump LIn _____	
		Prepared By PM Hubert Liu _____	

Contents

No		
		CONTENTS
		RECORD OF REVISIONS
1		GENERAL DESCRIPTION
2		ABSOLUTE MAXIMUM RATINGS
3		ELECTRICAL SPECIFICATION
	3-1	ELECTRICAL CHARACTERISTICS
	3-2	INTERFACE CONNECTIONS
	3-3	SIGNAL TIMING SPECIFICATION
	3-4	SIGNAL TIMING WAVEFORM
	3-5	COLOR INPUT DATA REFERENCE
	3-6	POWER SEQUENCE
	3-7	BACKLIGHT SPECIFICATION
4		OPTICAL SPECIFICATION
5		MECHANICAL CHARACTERISTICS
6		RELIABILITY TEST ITEMS
7		INTERNATIONAL STANDARD
	7-1	SAFETY
	7-2	EMC
8		PACKING
	8-1	DEFINITION OF LABEL
	8-2	PACKING METHODS
	8-3	PALLET AND SHIPMENT INFORMATION
9		PRECAUTION
	9-1	MOUNTING PRECAUTIONS
	9-2	OPERATING PRECAUTIONS
	9-3	ELECTROSTATIC DISCHARGE CONTROL
	9-4	PRECAUTIONS FOR STRONG LIGHT EXPOSURE
	9-5	STORAGE
	9-6	HANDLING PRECAUTIONS FOR PROTECT FILM

Record of Revision

Version	Date	Page	Description
0.1	09/07		First release

1. General Description

This specification applies to the 26.0 inch Color TFT-LCD Module T260XW06 V4. This LCD module has a TFT active matrix type liquid crystal panel 1,366x768 pixels, and diagonal size of 26.0 inch. This module supports 1,366x768 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T260XW06 V4 has been designed to apply the 8-bit 1 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important. The LED Driver is combined into whole module.

* General Information

Items	Specification	Unit	Note
Active Screen Size	26.00	inch	
Display Area	575.769(H) x 323.712(V)	mm	
Outline Dimension	613.0(H) x 361.0 (V) x 14.9(D)	mm	D: Front Bezel to T-Con Cover
Driver Element	a-Si TFT active matrix		
Display Colors	8 bit, 16.7M	Colors	
Number of Pixels	1,366x768	Pixel	
Pixel Pitch	0.4215 (H) x 0.4215 (W)	mm	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment	Anti-Glare, 3H		Haze=2%

2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit

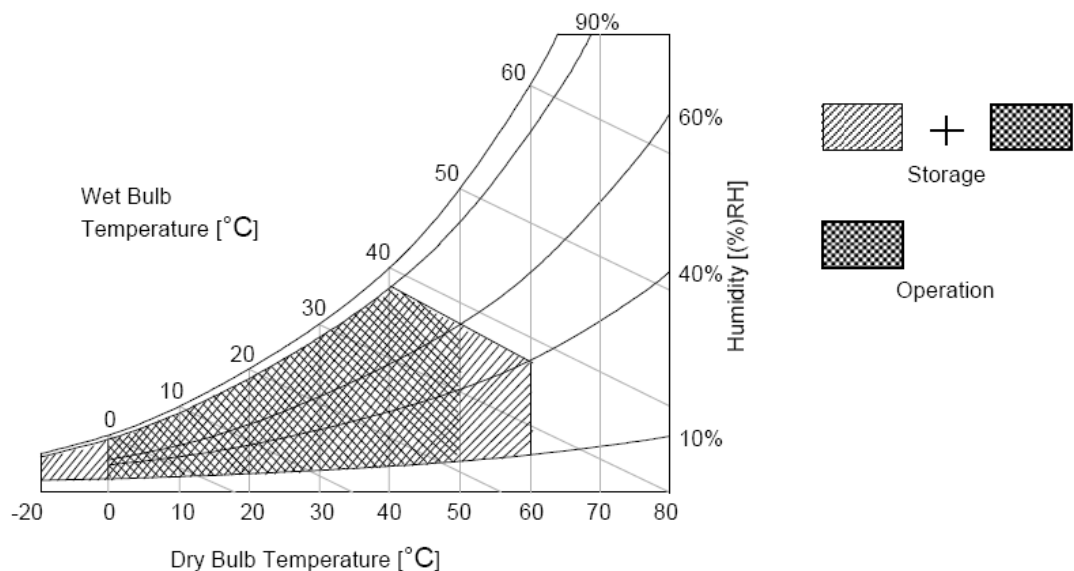
Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	V_{DD}	-0.3	14	V_{DC}	Note 1
Input Voltage of Signal	V_{in}	-0.3	4	V_{DC}	Note 1
BLU Input Voltage	V_{DDB}	-0.3	28	V_{DC}	Note 1
BLU Brightness Control Voltage	V_{dim}	-0.3	7	V_{DC}	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be 39°C and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C.

Note 3: Surface temperature is measured at 50°C Dry condition



3. Electrical Specification

The T260XW06 V4 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input for BLU is to power inverter.

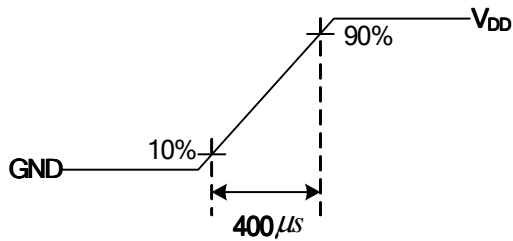
3.1 Electrical Characteristics

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max		
LCD							
Power Supply Input Voltage (12V model)		V_{DD}	10.8	12	13.2	V_{DC}	
Power Supply Input Current		I_{DD}	--	TBD	TBD	A	1
Power Consumption		P_C	--	TBD	TBD	Watt	1
Inrush Current		I_{RUSH}	--	--	TBD	A	2
LVDS Interface	Input Differential Voltage	$ V_{ID} $	200	400	600	mV_{DC}	3
	Differential Input High Threshold Voltage	V_{TH}	+100	--	+300	mV_{DC}	3
	Differential Input Low Threshold Voltage	V_{TL}	-300	--	-100	mV_{DC}	3
	Input Common Mode Voltage	V_{ICM}	1.1	1.25	1.4	V_{DC}	3
CMOS Interface	Input High Threshold Voltage	V_{IH} (High)	2.7	--	3.3	V_{DC}	4
	Input Low Threshold Voltage	V_{IL} (Low)	0	--	0.6	V_{DC}	4
Backlight Power Consumption		P_{BL}	TBD	TBD	TBD	Watt	
Life Time (MTTF)			30000		--	Hours	6

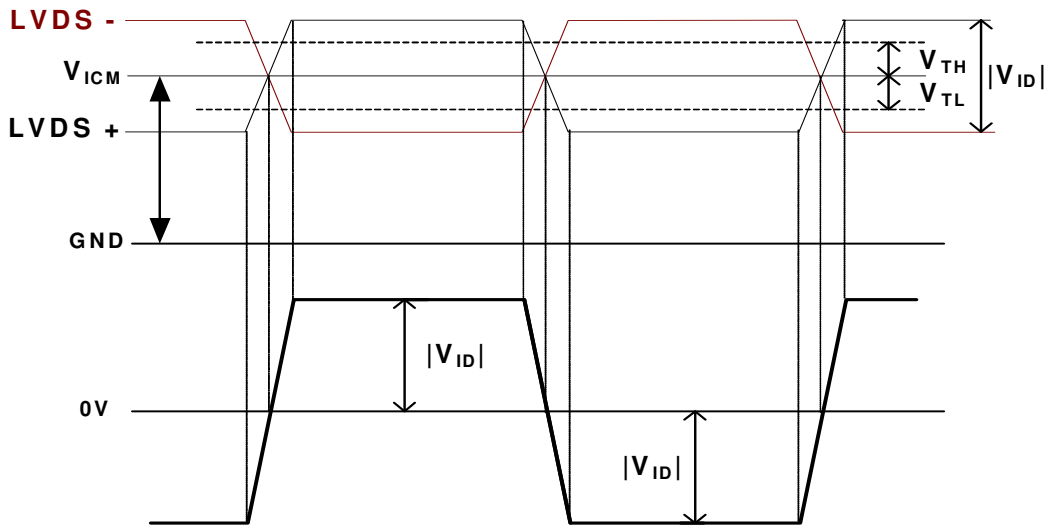
Note :

1. Test Condition:
 - (1) $V_{DD} = 12V$
 - (2) $F_v = 60Hz$
 - (3) $F_{CLK} = 82MHz$
 - (4) Temperature = 25 °C
 - (5) Test Pattern : White Pattern

2. Measurement condition : Rising time = 400us

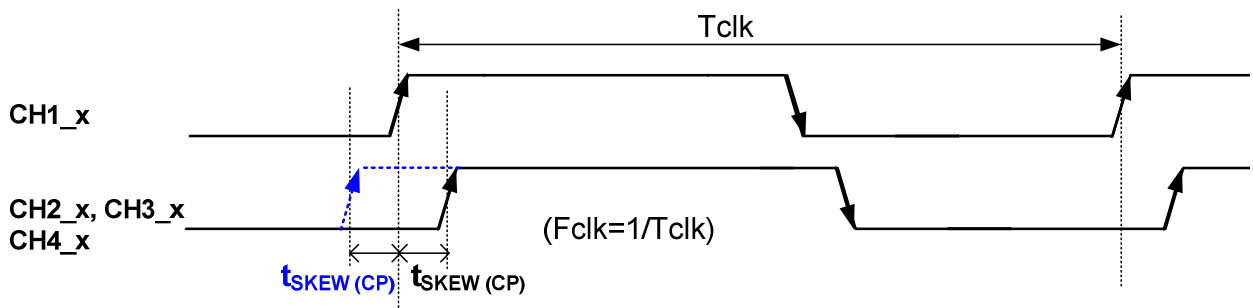


3. $V_{ICM} = 1.25V$



4. The measure points of V_{IH} and V_{IL} are in LCM side after connecting the System Board and LCM.

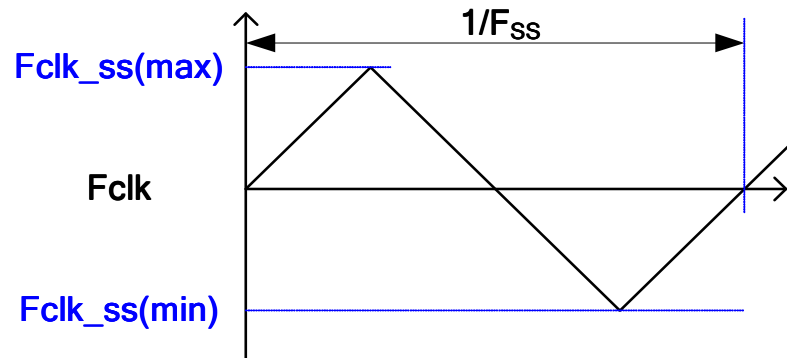
5. Input Channel Pair Skew Margin



Note: $x = 0, 1, 2, 3, 4$

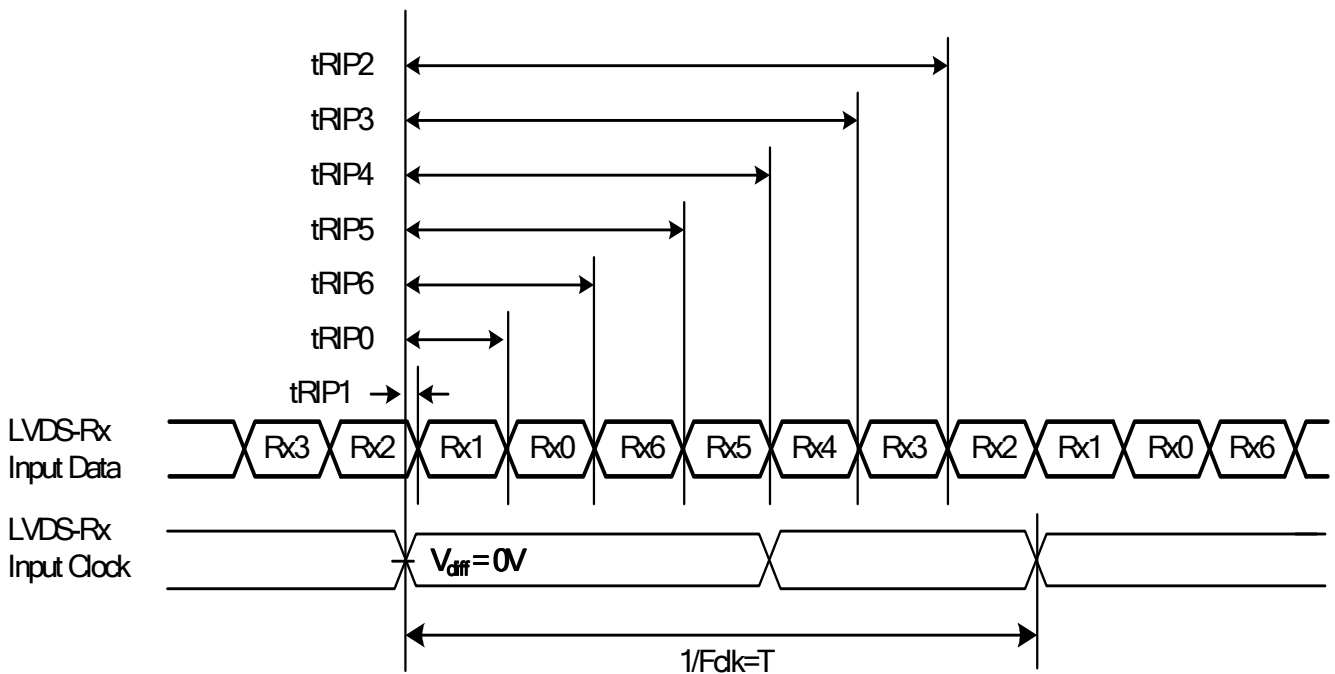
6. The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value.
[Operating condition: Continuous operating at $T_a = 25 \pm 2^\circ C$]

7. LVDS Receiver Clock SSCG (Spread spectrum clock generator) is defined as below figures



8. Receiver Data Input Margin

Parameter	Symbol	Rating			Unit	Note
		Min	Type	Max		
Input Clock Frequency	Fclk	Fclk (min)	--	Fclk (max)	MHz	$T=1/Fclk$
Input Data Position0	tRIP1	- tRMG	0	tRMG	ns	
Input Data Position1	tRIP0	$T/7- tRMG $	$T/7$	$T/7+ tRMG $	ns	
Input Data Position2	tRIP6	$2T/7- tRMG $	$2T/7$	$2T/7+ tRMG $	ns	
Input Data Position3	tRIP5	$3T/7- tRMG $	$3T/7$	$3T/7+ tRMG $	ns	
Input Data Position4	tRIP4	$4T/7- tRMG $	$4T/7$	$4T/7+ tRMG $	ns	
Input Data Position5	tRIP3	$5T/7- tRMG $	$5T/7$	$5T/7+ tRMG $	ns	
Input Data Position6	tRIP2	$6T/7- tRMG $	$6T/7$	$6T/7+ tRMG $	ns	

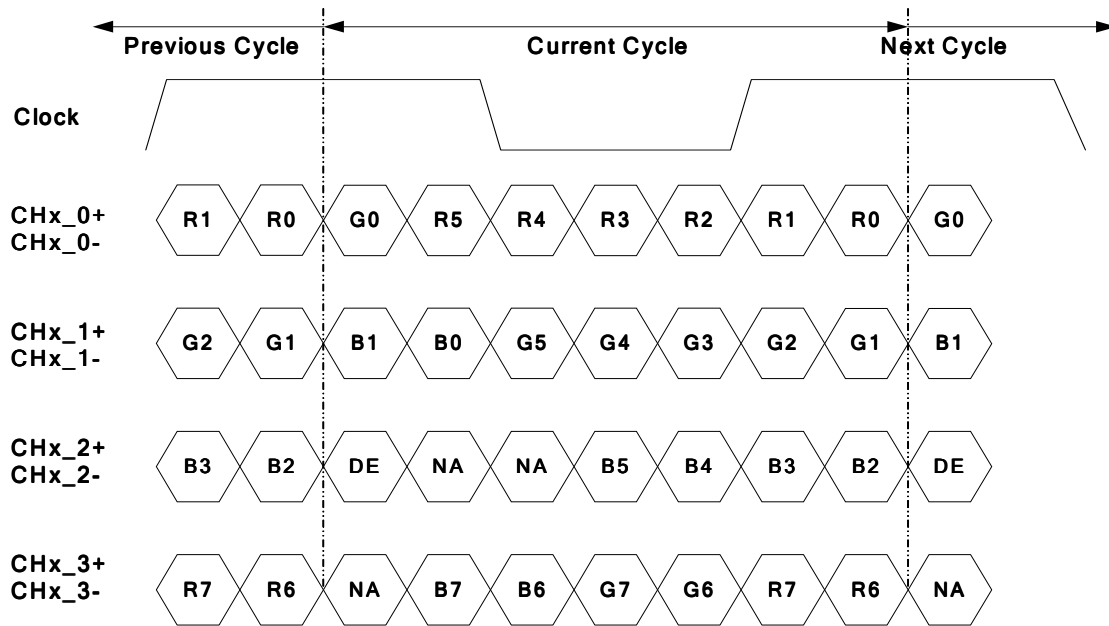


Interface Connections

- LCD connector: 093G30-B0001A-1 (Starconn, LVDS connector)

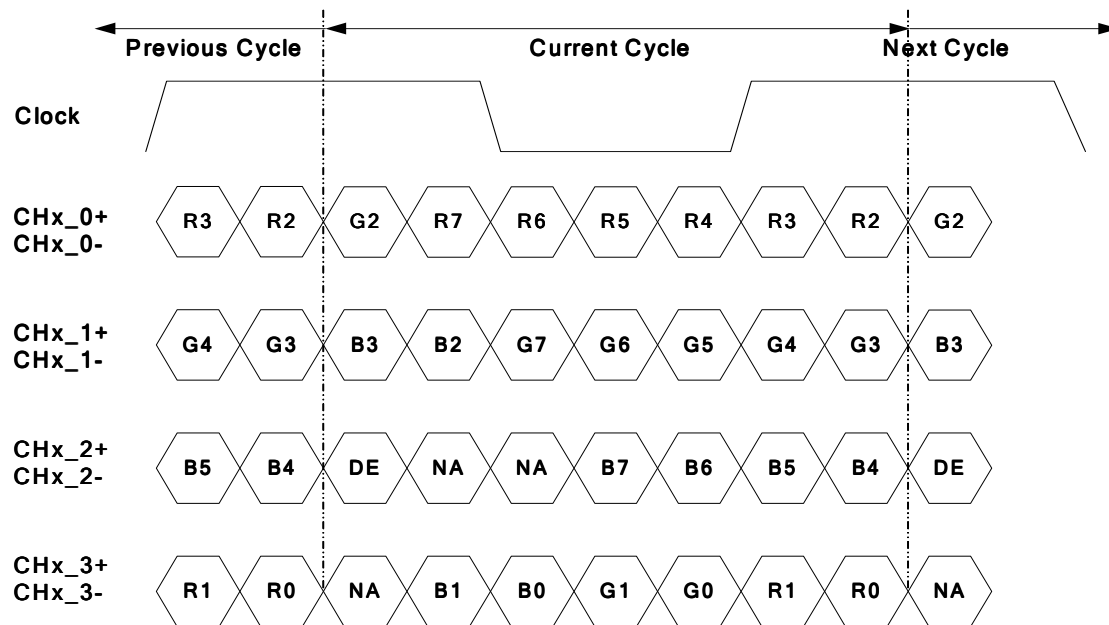
PIN	Symbol	Description
1	V _{DD}	Power Supply, +12V DC Regulated
2	V _{DD}	Power Supply, +12V DC Regulated
3	V _{DD}	Power Supply, +12V DC Regulated
4	V _{DD}	Power Supply, +12V DC Regulated
5	GND	Ground
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	LVDS_SEL	Open/High(3.3V) for NS, Low(GND) for JEIDA
10	N.C.	AUO Internal Use Only
11	GND	Ground
12	CH1_0-	LVDS Channel 1, Signal 0-
13	CH1_0+	LVDS Channel 1, Signal 0+
14	GND	Ground
15	CH1_1-	LVDS Channel 1, Signal 1-
16	CH1_1+	LVDS Channel 1, Signal 1+
17	GND	Ground
18	CH1_2-	LVDS Channel 1, Signal 2-
19	CH1_2+	LVDS Channel 1, Signal 2+
20	GND	Ground
21	CH1_CLK-	LVDS Channel 1, Clock -
22	CH1_CLK+	LVDS Channel 1, Clock +
23	GND	Ground
24	CH1_3-	LVDS Channel 1, Signal 3-
25	CH1_3+	LVDS Channel 1, Signal 3+
26	GND	Ground
27	N.C.	AUO Internal Use Only
28	N.C.	AUO Internal Use Only
29	N.C.	AUO Internal Use Only
30	GND	Ground

LVDS Option = High/Open→NS



Note: x = 1, 2, 3, 4...

LVDS Option = Low→JEIDA



Note: x = 1, 2, 3, 4...

3.2 Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Timing Table

Signal	Item	Symbol	Min.	Typ.	Max	Unit
Vertical Section	Period	Tv	784	810	1015	Th
	Active	Tdisp (v)	768			Th
	Blanking	Tblk (v)	16	42	247	Th
Horizontal Section	Period	Th	1460	1648	2000	Tclk
	Active	Tdisp (h)	1366			Tclk
	Blanking	Tblk (h)	94	282	634	Tclk
Clock	Frequency	Fclk=1/Tclk	50	80	86	MHz
Vertical Frequency	Frequency	Fv	47	60	63	Hz
Horizontal Frequency	Frequency	Fh	43	48	53	KHz

Notes:

(1) Display position is specific by the rise of DE signal only.

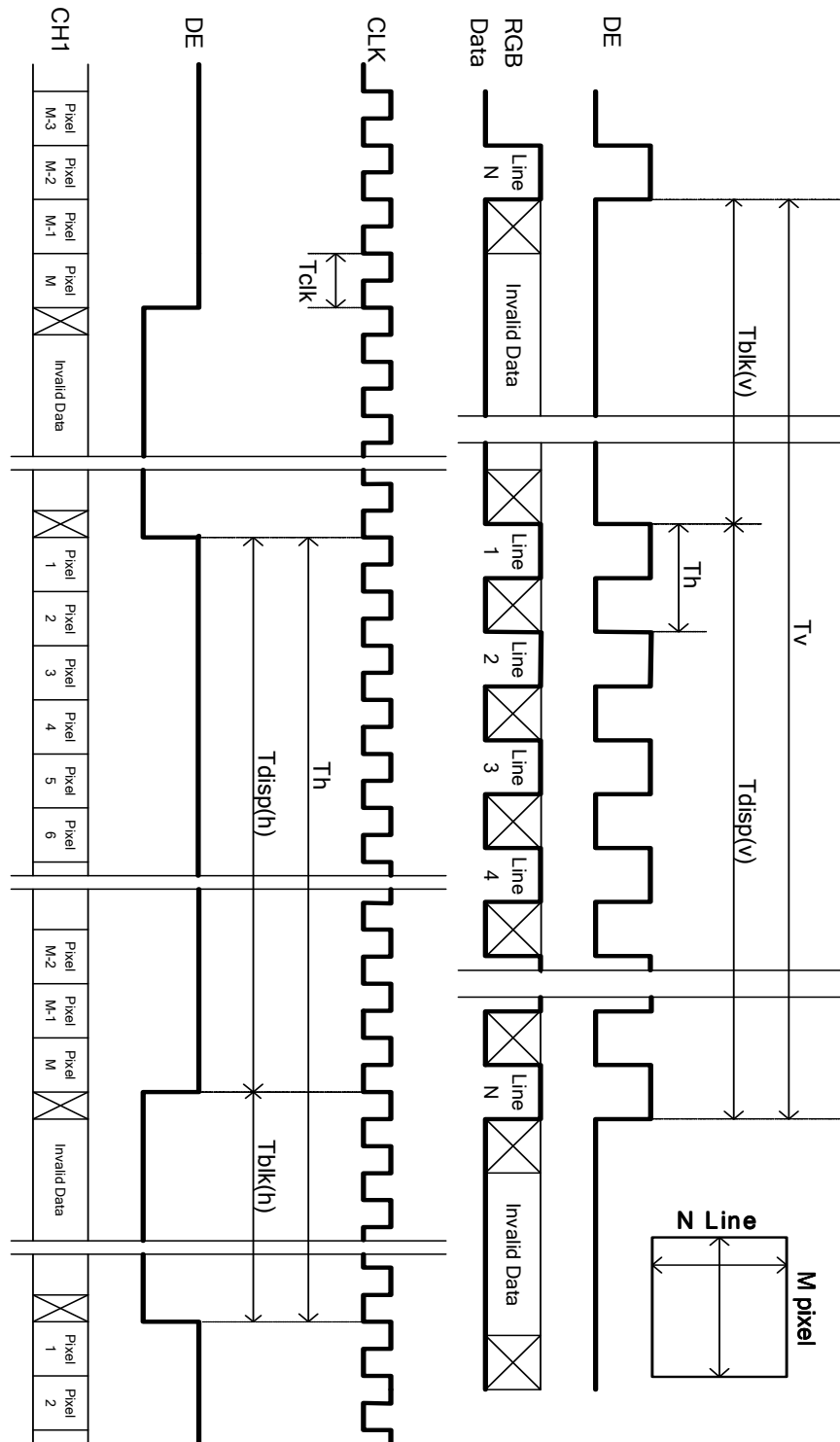
Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.

(2) Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen.

(3) If a period of DE "High" is less than 1366 DCLK or less than 768 lines, the rest of the screen displays black.

(4) The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.

Signal Timing Waveforms



3.3 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

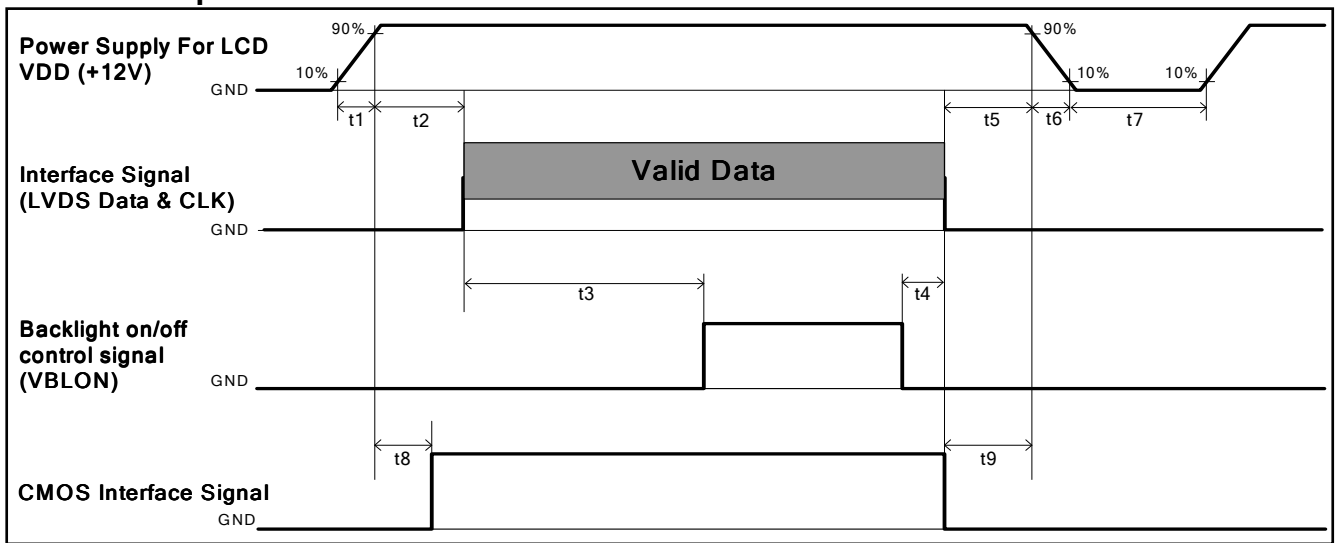
Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB				LSB				MSB				LSB				MSB				LSB			
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
R	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0

	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
B	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1

	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3.4 Power Sequence for LCD



Parameter	Values			Unit
	Min.	Type.	Max.	
t1	0.4	---	30	ms
t2	0.1	---	150	ms
t3	450	---	---	ms
t4	0 ^{*1}	---	---	ms
t5	0	---	---	ms
t6	---	---	--- ^{*2}	ms
t7	500	---	---	ms
t8	10	---	50	ms
t9	0	---	---	ms

Note:

- (1) t4=0 : concern for residual pattern before BLU turn off.
- (2) t6 : voltage of VDD must decay smoothly after power-off. (customer system decide this value)

3.7 Backlight Specification

The backlight unit contains 1pcs light bar.

3.7.1 Electrical specification

Item	Symbol	Condition	Spec			Unit	Note	
			Min	Typ	Max			
Input Voltage	V_{DDB}	-	21.6	24	26.4	VDC	-	
Input Current	I_{DDB}	VDDB=24V	3.375	3.75	4.125	ADC	1	
Input Power	P_{DDB}	VDDB=24V	81	90	99	W	1	
Inrush Current	I_{RUSH}	VDDB=24V	-	-	6	ADC	2	
Operating Frequency	FBL	VDDB=24V	42	44	46	KHz		
On/Off control voltage	V_{BLON}	ON	VDDB=24V	2	-	5.5	VDC	-
		OFF		0	-	0.8		-
On/Off control current	I_{BLON}	VDDB=24V	-	-	1.5	mA	-	
Internal PWM Dimming Control Voltage	V_{IPWM}	MAX	VDDB=24V	3.0	-	3.3	VDC	-
		MIN		-	0	-	VDC	-
Internal PWM Dimming Control Current	I_{IPWM}	VDDB=24V	-	-	2	mADC	-	
Internal PWM Dimming Ratio	R_{IPWM}	VDDB=24V	10	-	100	%		
External PWM Control Voltage	V_{EPWM}	MAX	VDDB=24V	2	-	3.3	VDC	-
		MIN	VDDB=24V	0	-	0.8		-
External PWM Control Current	I_{EPWM}	VDDB=24V	-	-	2	mADC	-	
External PWM Duty ratio	D_{EPWM}	VDDB=24V	10	-	100	%	3	
External PWM Frequency	F_{EPWM}	VDDB=24V	140	180	240	Hz	-	

Note 1 : Dimming ratio= 100% (MAX) ($T_a=25\pm 5^\circ\text{C}$, Turn on for 45minutes)

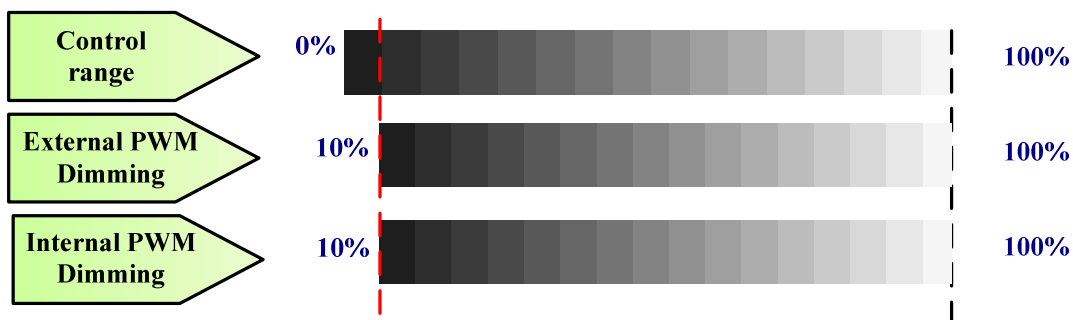
Note 2 : Measurement condition Rising time = 20ms (VDDB : 10%~90%);

Note 3 :For External PWM application, $\geq 5\%$ dimming is function well and no backlight shutdown.

3.7.2 Input Pin Assignment

LED Driver Connector: CI0114M1HRL-NH (Cvilux)

Pin	Symbol	Description
1	VDDB	Operating Voltage Supply, +24V DC regulated
2	VDDB	Operating Voltage Supply, +24V DC regulated
3	VDDB	Operating Voltage Supply, +24V DC regulated
4	VDDB	Operating Voltage Supply, +24V DC regulated
5	VDDB	Operating Voltage Supply, +24V DC regulated
6	BLGND	Ground and Current Return
7	BLGND	Ground and Current Return
8	BLGND	Ground and Current Return
9	BLGND	Ground and Current Return
10	BLGND	Ground and Current Return
11	DET	BLU status detection: Normal : 0~0.8V ; Abnormal : Open collector
12	VBLOn	BLU On-Off control: BL On : High/Open (2V~5.5V); BL off : Low (0~0.8V/GND)
13	VDIM	Internal PWM (0~3.3V for 10~100% Duty, open for 100%) < NC ; at External PWM mode >
14	PDIM	External PWM (10%~100% Duty, open for 100%) < NC ; at Internal PWM mode >

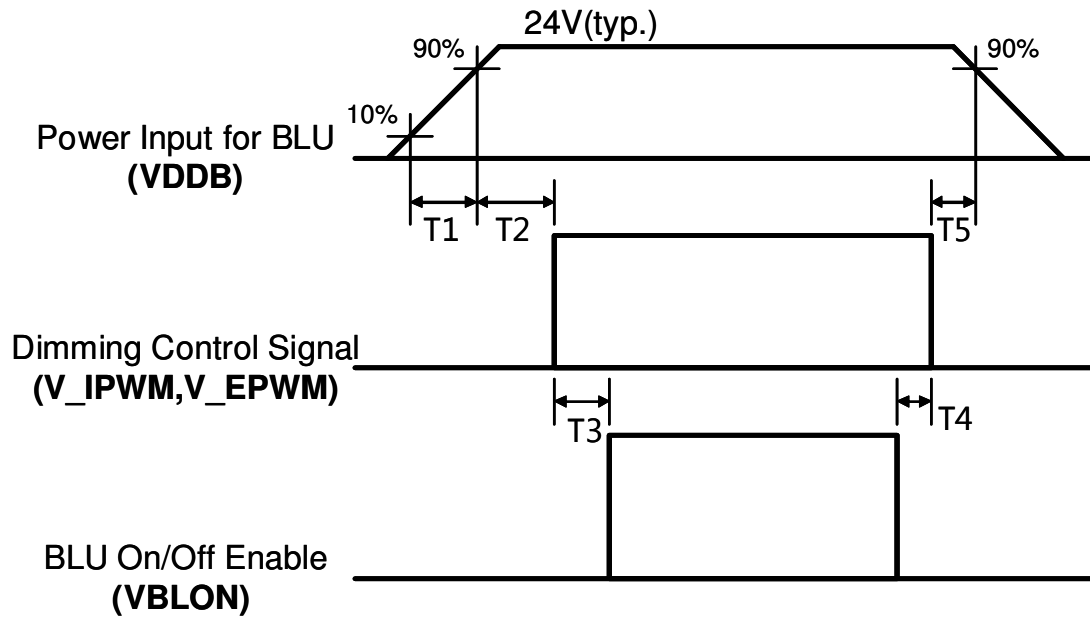


PWM Dimming : include Internal and External PWM Dimming

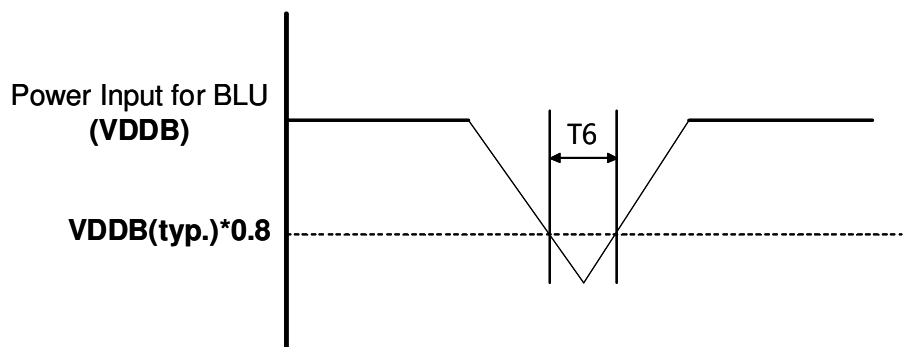
(Note*) IF External PWM function includes 10% dimming ratio. Judge condition as below:

- (1) Backlight module must be lighted ON normally.
- (2) All protection function must work normally.
- (3) Uniformity and flicker could NOT be guaranteed

3.7.3 Power Sequence for LED Driver



Dip condition for LED Driver

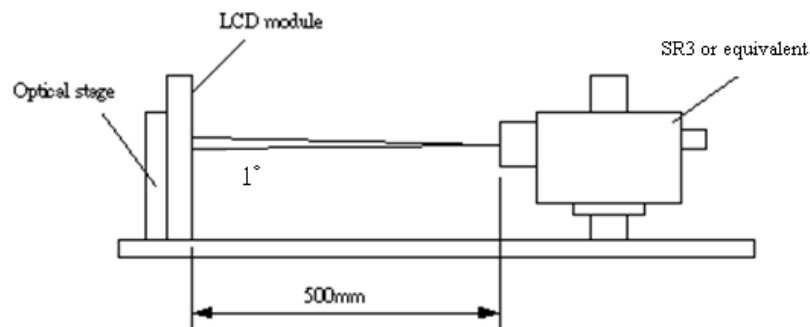


Parameter	Value			Units
	Min	Typ	Max	
T1	20	-	-	ms
T2	500	-	-	ms
T3	250	-	-	ms
T4	0	-	-	ms
T5	1	-	-	ms
T6	-	-	10	ms

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of φ and θ equal to 0°.

Fig.1 presents additional information concerning the measurement equipment and method.



Parameter	Symbol	Values			Unit	Notes
		Min.	Typ.	Max		
Contrast Ratio	CR	2400	3000	--		1
Surface Luminance (White)	L_{WH}	280	350	--	cd/m ²	2
Luminance Variation	$\delta_{WHITE(\theta P)}$	--	--	1.33		3
Response Time (G to G)	T_Y	--	6.5	--	Ms	4
Color Gamut	NTSC		68		%	
Color Coordinates						
<div style="border-left: 1px dashed black; border-right: 1px dashed black; padding: 0 5px;"> Red Green Blue White </div>	R_X	Typ.-0.03	0.627	Typ.+0.03		
	R_Y		0.336			
	G_X		0.338			
	G_Y		0.617			
	B_X		0.155			
	B_Y		0.040			
	W_X		0.280			
W_Y	0.290					
Viewing Angle						5
<div style="border-left: 1px dashed black; border-right: 1px dashed black; padding: 0 5px;"> x axis, right($\varphi=0^\circ$) x axis, left($\varphi=180^\circ$) y axis, up($\varphi=90^\circ$) y axis, down ($\varphi=270^\circ$) </div>	θ_r	--	89	--	degree	
	θ_l	--	89	--	degree	
	θ_u	--	89	--	degree	
	θ_d	--	89	--	degree	

Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance of } L_{on5}}{\text{Surface Luminance of } L_{off5}}$$

2. Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When LED current = 120mA, $L_{WH} = L_{on5}$ where L_{on5} is the luminance with all pixels displaying white at center 5 location.

3. The variation in surface luminance, δ_{WHITE} is defined (center of Screen) as:

$$\delta_{WHITE(9P)} = \text{Maximum}(L_{on1}, L_{on2}, \dots, L_{on9}) / \text{Minimum}(L_{on1}, L_{on2}, \dots, L_{on9})$$

4. Response time T_{γ} is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on $F_v=60\text{Hz}$ to optimize.

Measured Response Time		Target				
		0%	25%	50%	75%	100%
Start	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%
	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%	

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG4.

FIG. 2 Luminance

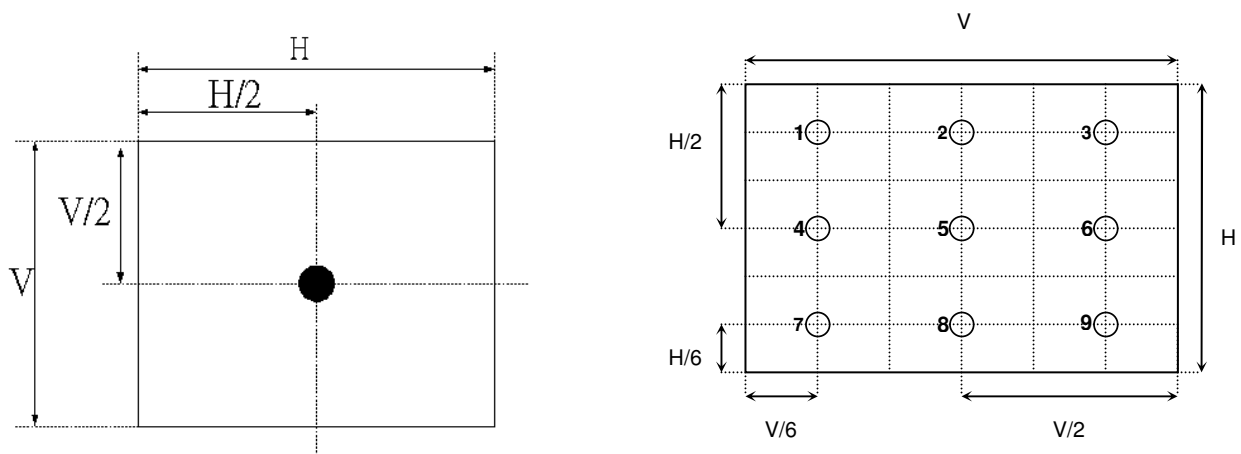


FIG.3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for “any level of gray(bright) “ and “any level of gray(dark)”.

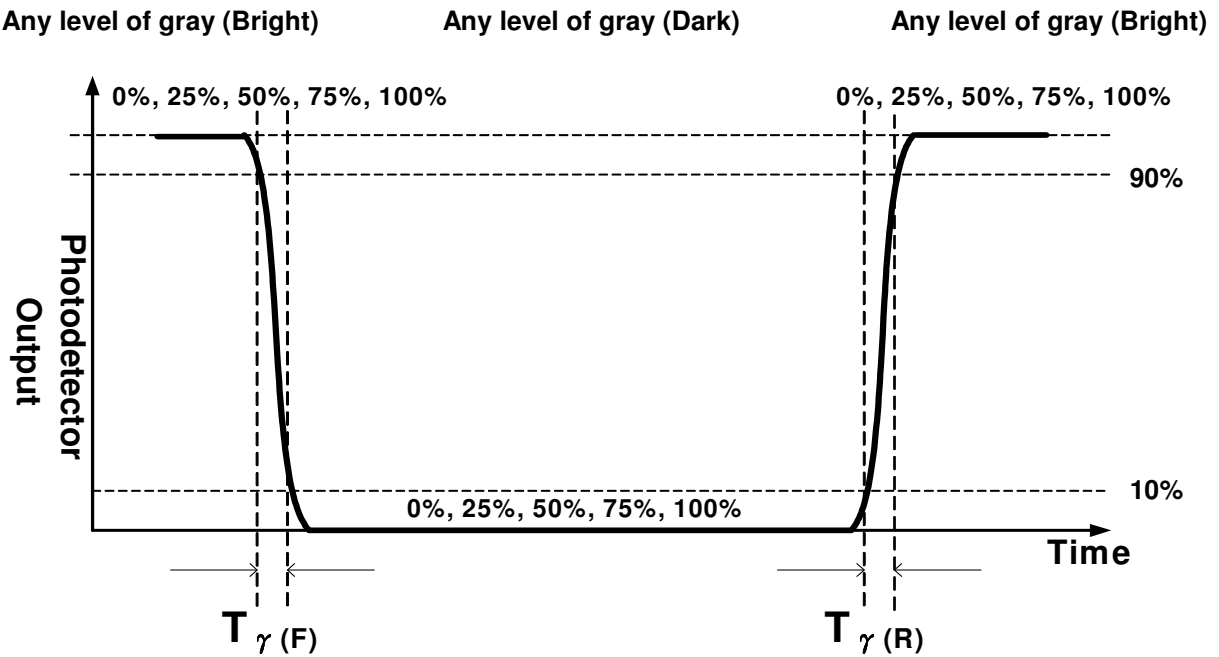
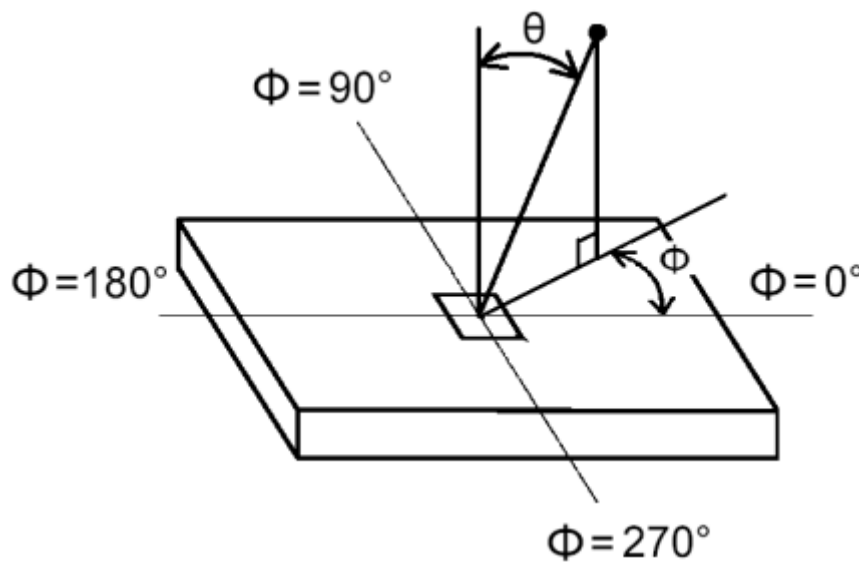


FIG.4 Viewing Angle

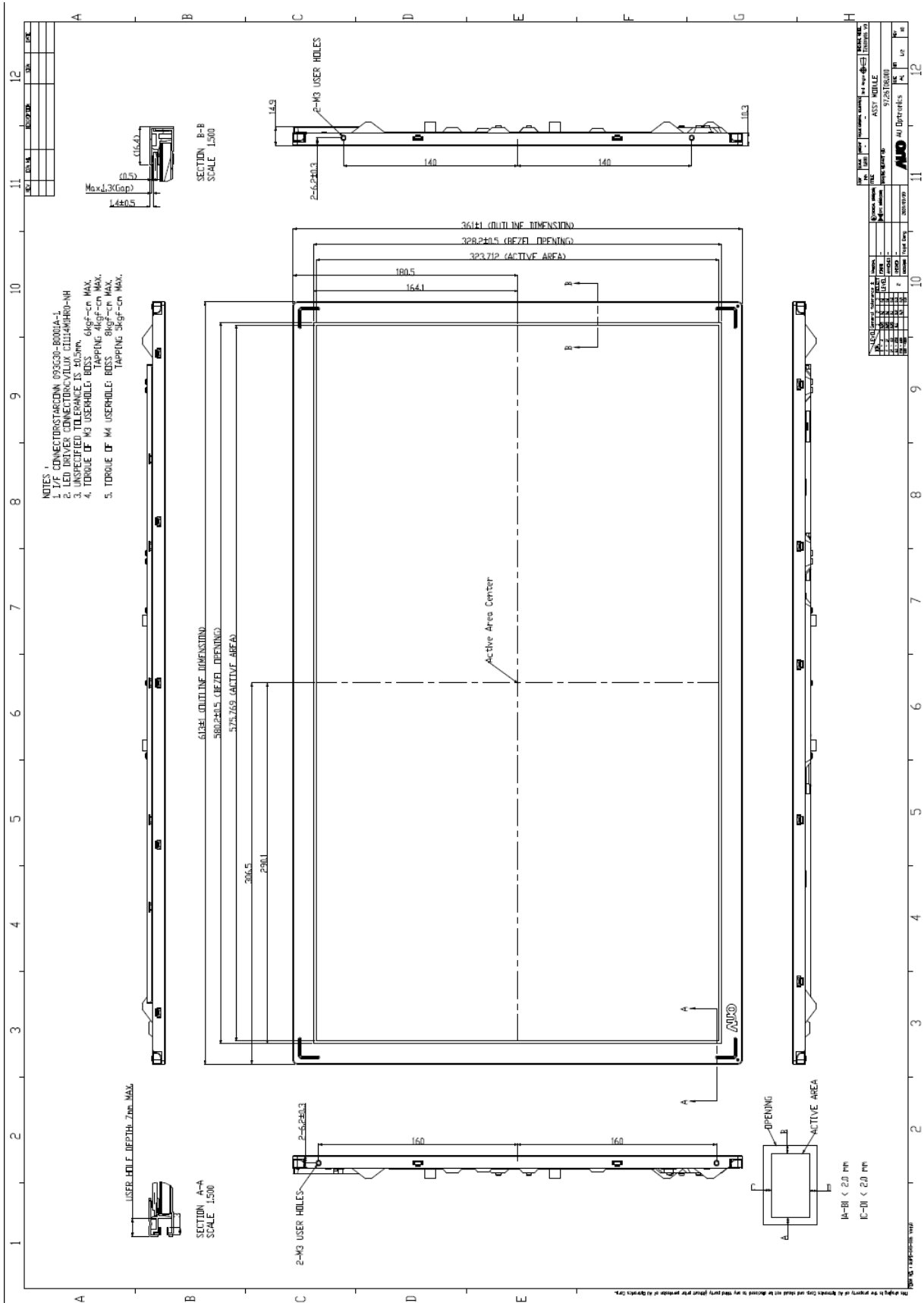


5. Mechanical Characteristics

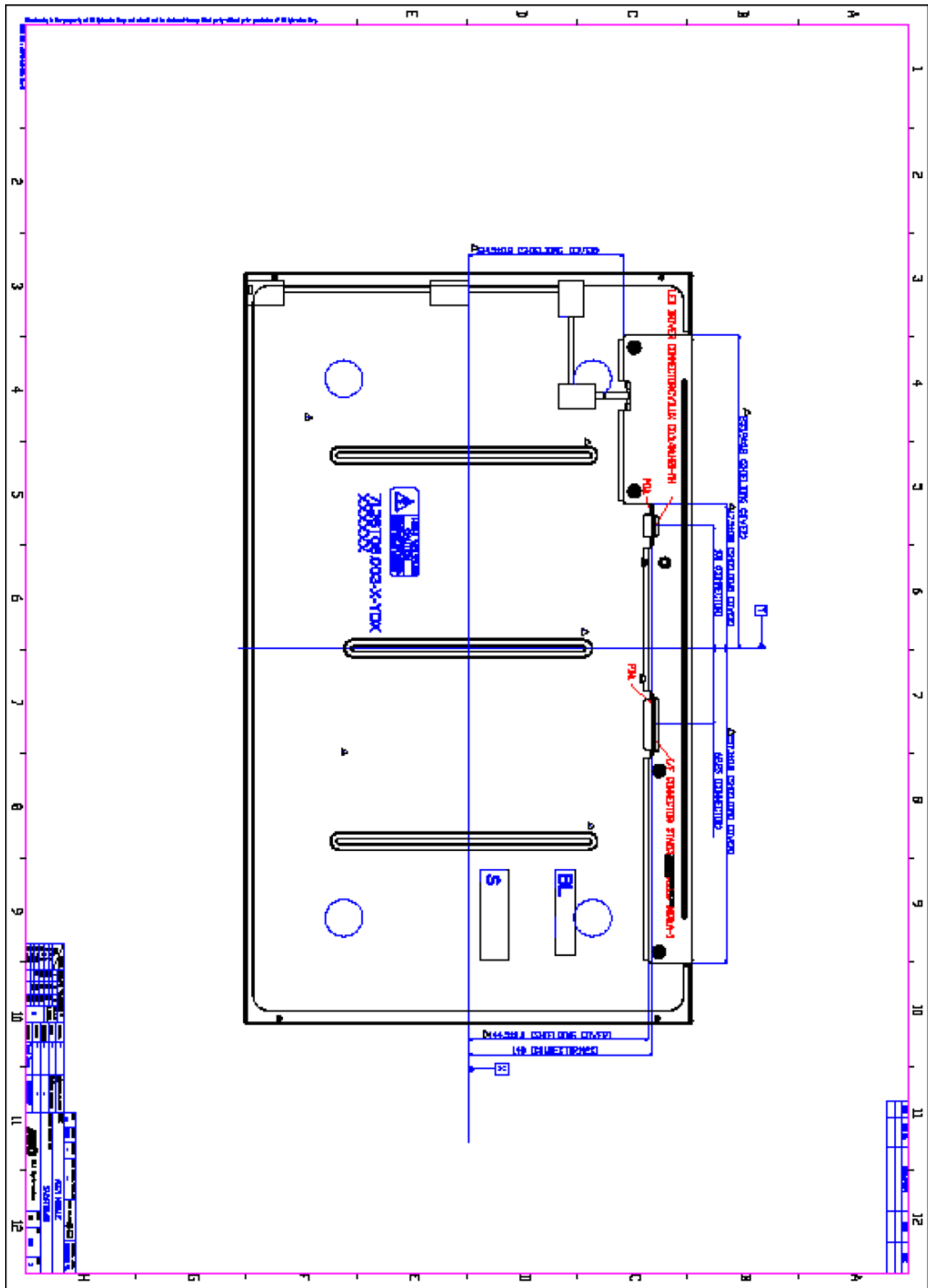
The contents provide general mechanical characteristics for the model T260XW06 V0. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal	613.0 mm
	Vertical	361.0 mm
	Depth	14.9 mm (Bezel to T-con cover)
Bezel Opening	Horizontal	580.2 mm
	Vertical	328.2 mm
Active Display Area	Horizontal	575.769mm
	Vertical	323.712 mm
Weight	3400 g (Typ.)	
Surface Treatment	Anti-Glare, 3H	

Front View



Back View



6. Reliability Test Items

	Test Item	Q'ty	Condition
1	High temperature storage test	3	60°C , 300hrs
2	Low temperature storage test	3	-20°C , 300hrs
3	High temperature operation test	3	50°C , 300hrs
4	Low temperature operation test	3	-5°C , 300hrs
5	Vibration test (non-operation)	3	wave form : random Overall average energy level : 1.0Grms Level: 10~300Hz Duration:X,Y,Z 10min one time each direction
6	Shock test (non-operation)	3	shock level : 50G wave form :half sine wave 20ms in ±X, ±Y, ±Z axis one time each direction
7	Vibration test (With carton)	3	1.0Grms 10~200 Hz ,10 min/axis (Random)
8	Drop test (With carton)	3	Height: 457 mm 1 corner, 3 edges , 6 flats (refer ASTM D 5276)

7. International Standard

7.1 Safety

- (1) UL 60950-1, UL 60065; Standard for Safety of Information Technology Equipment Including electrical Business Equipment.
- (2) IEC 60950-1 : 2001, IEC 60065:2001 ; Standard for Safety of International Electrotechnical Commission
- (3) EN 60950 : 2001+A11, EN 60065:2002+A1:2006; European Committee for Electrotechnical Standardization (CENELEC), EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

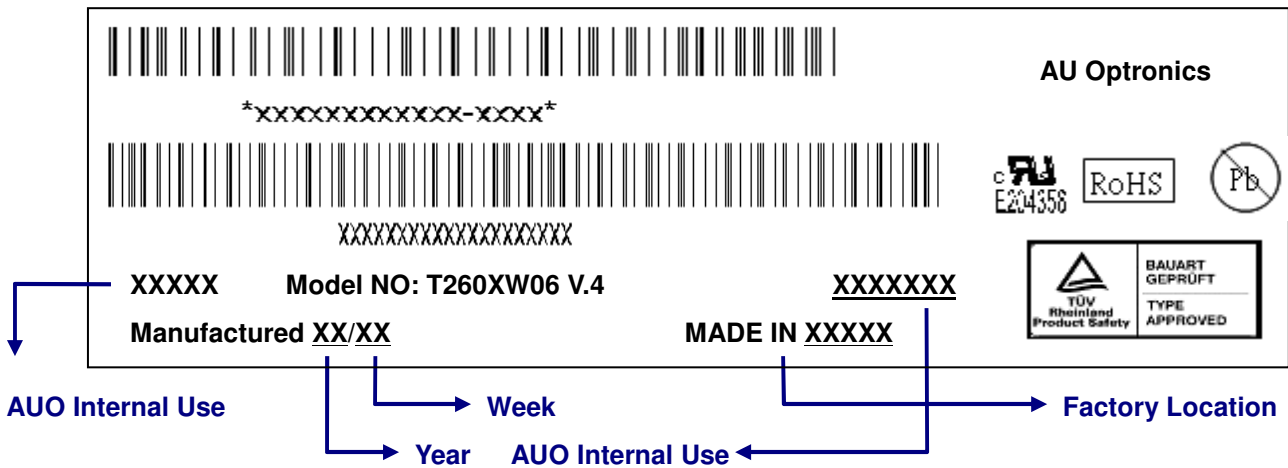
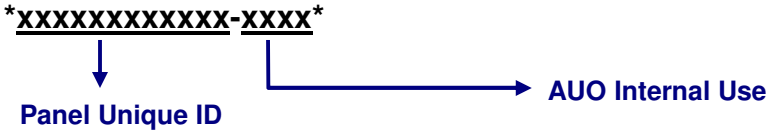
7.2 EMC

- (1) ANSI C63.4 “Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. “American National standards Institute(ANSI), 1992
- (2) C.I.S.P.R “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.” International Special committee on Radio Interference.
- (3) EN 55022 “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.” European Committee for Electrotechnical Standardization. (CENELEC), 1998


8. Packing


8-1 DEFINITION OF LABEL:

A. Panel Label:



Green mark description

(1) For Pb Free Product, AUO will add  for identification.

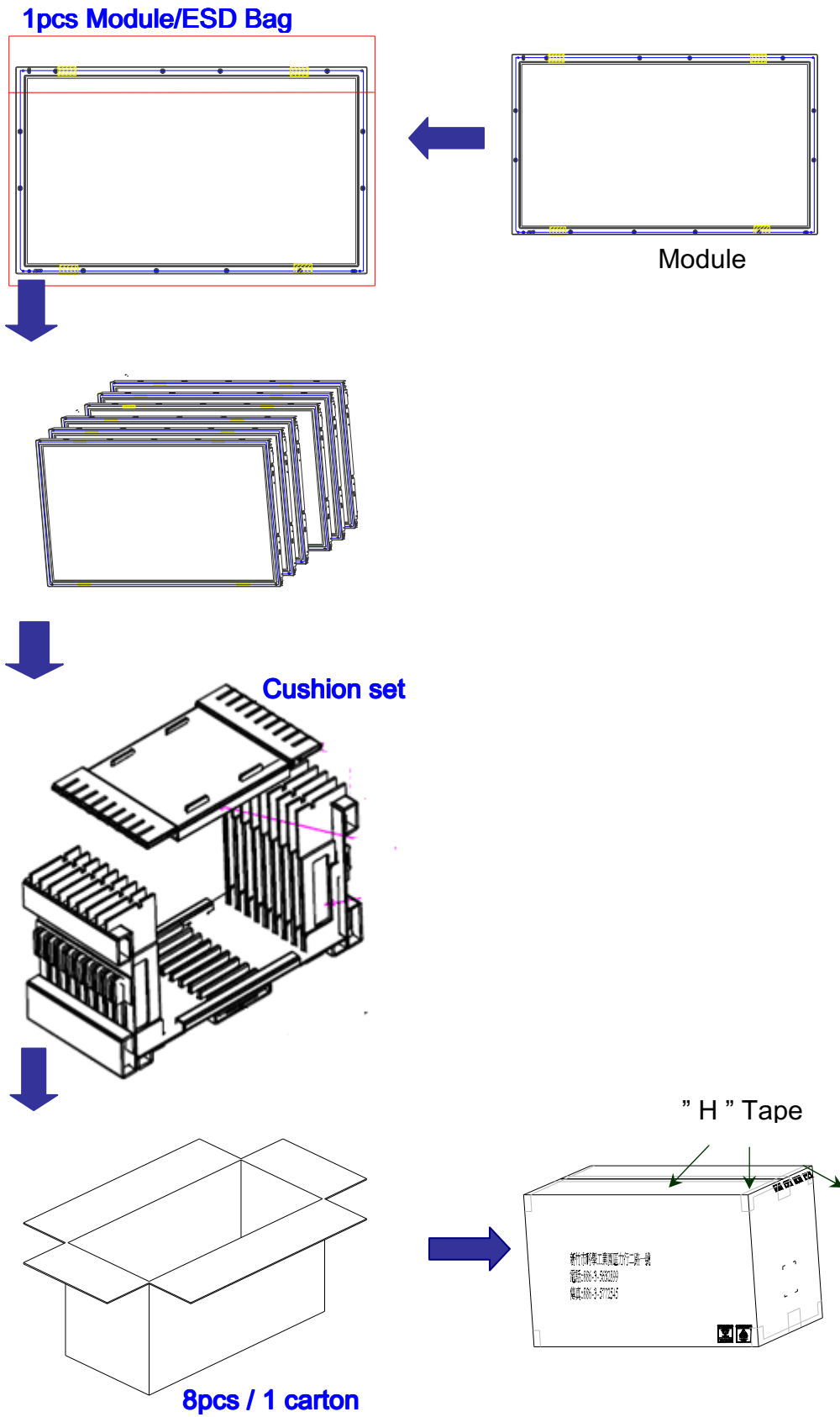
(2) For RoHs compatible products, AUO will add  for identification.

Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

B. Carton Label:

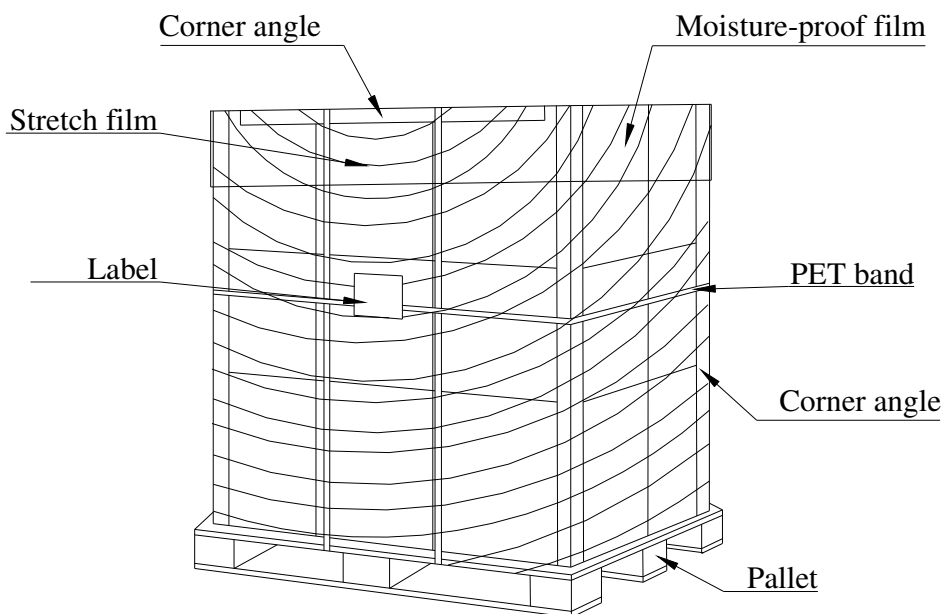


8-2 PACKING METHODS:



8-3 Pallet and Shipment Information

	Item	Specification			Packing Remark
		Qty.	Dimension	Weight (kg)	
1	Packing BOX	8pcs/box	722(L)*325(W)*438(H)	29	
2	Pallet	1	980(L)*740(W)*135(H)	16	
3	Boxes per Pallet	6 boxes/pallet			
4	Panels per Pallet	48pcs/pallet			
	Pallet after packing	72	980(L)*740(W)*1011(H)	190	



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit broken by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- (1) The device listed in the product specification sheets was designed and manufactured for TV application
- (2) The spike noise causes the mis-operation of circuits. It should be lower than following voltage:
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (3) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (4) Brightness of CCFL depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (5) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (6) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (7) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall

be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.