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T315HW07 V3 Product Specification Rev. 0.1

Model Name: T315HW07 V3

Issue Date : 2010/12/21

()Preliminary Specifications(*)Final Specifications

Customer Signature	Date	AUO	Date						
Approved By		Approval By PM Director YenTing Chiu YenTing dun 2010, 1.4							
Note		Reviewed By RD Director Eugene CC Chen Magene Chen Reviewed By Project Leader Shinli Chen Shinli Chen Shinli Chen. 2010.12. Prepared By PM Joanne Hsu Joanne Hsu Joanne Msu	23.						



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Record of Revision

Version	Date	Page	Description
0.0	2010/05/17		First release
0.1	2010/08/23	8	Update Pin 6 as ROTATE pin
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1. General Description

This specification applies to the 31.5 inch Color TFT-LCD Module T315HW07 V3. This LCD module has a TFT active matrix type liquid crystal panel 1,920x1,080 pixels, and diagonal size of 31.5 inch. This module supports 1,920x1080 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 10-bit gray scale signal for each dot.

The T315HW07 V3 has been designed to apply the 10-bit 4 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

* General Information

Items	Specification	Unit	Note
Active Screen Size	31.55	inch	
Display Area	698.40(H) x 392.85(V)	mm	
Outline Dimension	741.4(H) x 435.8 (V) x 22.4(D)	mm	D : Front bezel to T-CON cover
Driver Element	a-Si TFT active matrix		
Display Colors	10 bit, 1073.7M	Colors	
Number of Pixels	1,920x1080	Pixel	
Pixel Pitch	0.36375 (H) x 0. 36375 (W)	mm	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment	Anti-Glare, 3H		Haze=11%



2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit

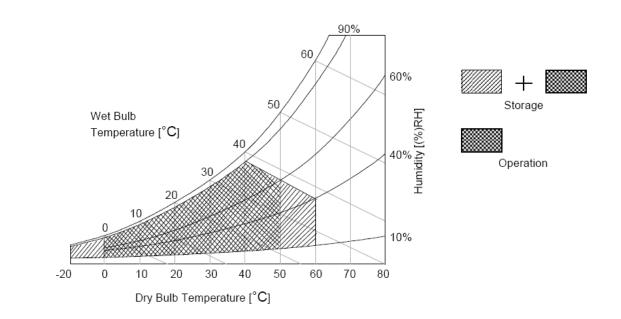
Item	Symbol	Min	Мах	Unit	Conditions
Logic/LCD Drive Voltage	Vcc	-0.3	14	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	4	[Volt]	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be $39^\circ\!\mathrm{C}$ and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40° C or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C.

Note 3: Surface temperature is measured at 50 $^\circ\!\!\mathbb{C}$ Dry condition







3. Electrical Specification

The T315HW07 V3 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second is employed for LED lightbar.

3.1 Electrical Characteristics

3.1.1: DC Characteristics

	Parameter	Symbol		Value		Unit	Note
	Falameter	Symbol	Min.	Тур.	Max	Offic	Note
LCD							
Power Su	pply Input Voltage	V _{DD}	10.8	12	13.2	V _{DC}	
Power Su	pply Input Current	I _{DD}	-	0.36	0.79	Α	1
Power Co	nsumption	Pc		4.32	10.428	Watt	1
Inrush Cu	rrent	I _{RUSH}			4	А	2
	Input Differential Voltage	V _{ID}	200	400	600	mV_{DC}	3
LVDS	Differential Input High Threshold Voltage	V _{TH}	+100		+300	mV_{DC}	3
Interface	Differential Input Low Threshold Voltage	V _{TL}	-300		-100	mV_{DC}	3
	Input Common Mode Voltage	VICM	1.1	1.25	1.4	V _{DC}	3
CMOS	Input High Threshold Voltage	V _{IH} (High)	2.7		3.3	V _{DC}	4
Interface	Input Low Threshold Voltage	V _{IL} (Low)	0		0.6	V_{DC}	4
Backlight	Power Consumption	P _{BL}		37.3	40.7	Watt	
Life time (MTTF)		30000			Hour	8, 9

3.1.2: AC Characteristics

	Parameter	Symbol		Value	Unit	Note		
	i alametei	Symbol	Min.	Тур.	Max	Onit	Note	
	Input Channel Pair Skew Margin	t _{SKEW (CP)}	-500		+500	ps	5	
LVDS	Receiver Clock : Spread Spectrum Modulation range	Fclk_ss	Fclk -3%		Fclk +3%	MHz	6	
Interface	Receiver Clock : Spread Spectrum Modulation frequency	Fss	30		200	KHz	6	
	Receiver Data Input Margin Fclk = 85 MHz Fclk = 65 MHz	tRMG	-0.4 -0.5		0.4 0.5	ns	7	

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VDD

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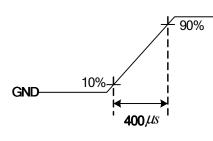
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Note :

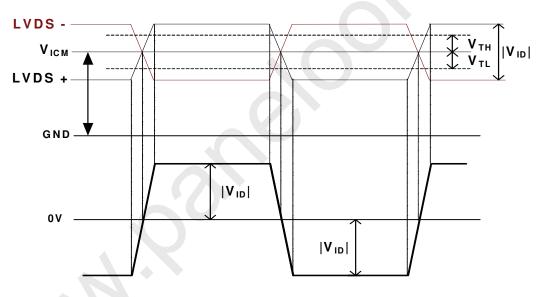
- 1. Test Condition:
 - (1) $V_{DD} = 12.0V$
 - (2) Fv = Type Timing, 120Hz
 - (3) Fclk= Max freq.
 - (4) Temperature = 25 $^{\circ}C$
 - (5) Typ. Input current : White Pattern

Max. Input current: Heavy loading pattern defined by AUO

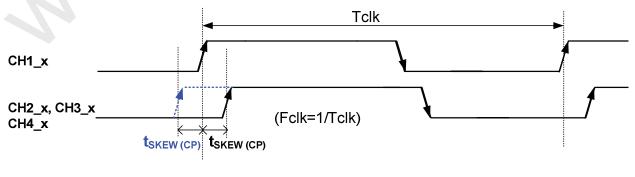
2. Measurement condition : Rising time = 400us



3. $V_{ICM} = 1.25V$



- **4.** The measure points of V_{IH} and V_{IL} are in LCM side after connecting the System Board and LCM.
- 5. Input Channel Pair Skew Margin



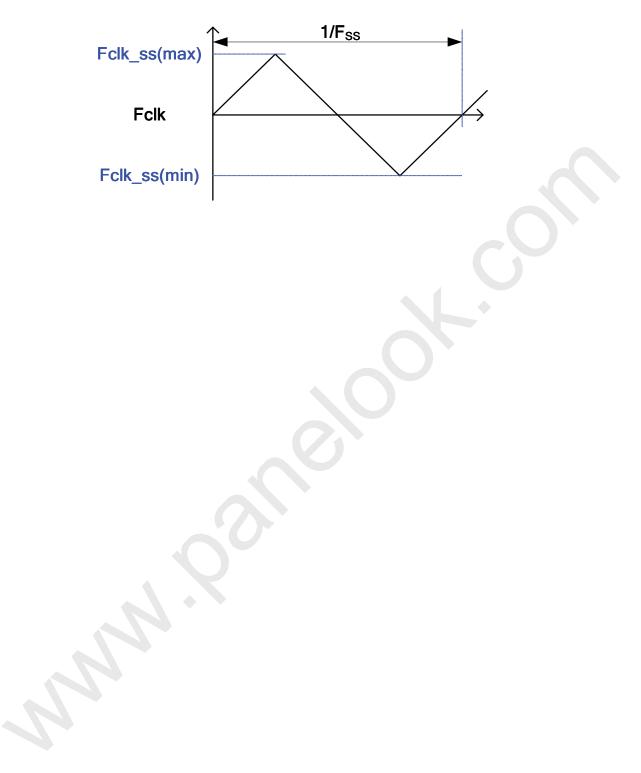
Note: x = 0, 1, 2, 3, 4

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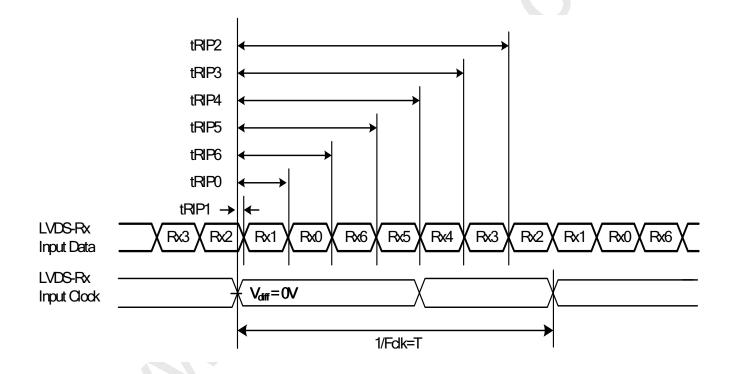
6. LVDS Receiver Clock SSCG (Spread spectrum clock generator) is defined as below figures





7. Receiver Data Input Margin

Parameter	Symbol		Rating								
Farameter	Symbol	Min	Туре	Мах	Unit	Note					
Input Clock Frequency	Fclk	Fclk (min)		Fclk (max)	MHz	T=1/Fclk					
Input Data Position0	tRIP1	- tRMG	0	tRMG	ns						
Input Data Position1	tRIP0	T/7- tRMG	T/7	T/7+ tRMG	ns						
Input Data Position2	tRIP6	2T/7- tRMG	2T/7	2T/7+ tRMG	ns						
Input Data Position3	tRIP5	3T/7- tRMG	3T/7	3T/7+ tRMG	ns						
Input Data Position4	tRIP4	4T/7- tRMG	4T/7	4T/7+ tRMG	ns						
Input Data Position5	tRIP3	5T/7- tRMG	5T/7	5T/7+ tRMG	ns						
Input Data Position6	tRIP2	6T/7- tRMG	6T/7	6T/7+ tRMG	ns						



- 8. The relative humidity must not exceed 80% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C. When operate at low temperatures, the brightness of LED will drop and the life time of LED will be reduced.
- **9.** The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value. [Operating condition: Continuous operating at $Ta = 25\pm2^{\circ}C$]

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3.2 Interface Connections

• LCD connector: JAE FI-RE51S-HF

PIN	Symbol	Description	PIN	Symbol	Description
1	Reserved	AUO Internal Use Only	26	GND	Ground
2	Reserved	AUO Internal Use Only	27	GND	Ground
3	NC	No connection	28	CH2_0-	LVDS Channel 2, Signal 0-
4	Reserved	AUO Internal Use Only	29	CH2_0+	LVDS Channel 2, Signal 0+
5	NC	No connection	30	CH2_1-	LVDS Channel 2, Signal 1-
6	ROTATE	Panel Rotation Display Control High(3.3V) : Rotate Enable Open/Low(GND) : Rotate Disable	31	CH2_1+	LVDS Channel 2, Signal 1+
7	LVDS_SEL	Open/High(3.3V) for NS, Low(GND) for JEIDA	32	CH2_2-	LVDS Channel 2, Signal 2-
8	Reserved	AUO Internal Use Only	33	CH2_2+	LVDS Channel 2, Signal 2+
9	Reserved	AUO Internal Use Only	34	GND	Ground
10	Reserved	AUO Internal Use Only	35	CH2_CLK-	LVDS Channel 2, Clock -
11	GND	Ground	36	CH2_CLK+	LVDS Channel 2, Clock +
12	CH1_0-	LVDS Channel 1, Signal 0-	37	GND	Ground
13	CH1_0+	LVDS Channel 1, Signal 0+	38	CH2_3-	LVDS Channel 2, Signal 3-
14	CH1_1-	LVDS Channel 1, Signal 1-	39	CH2_3+	LVDS Channel 2, Signal 3+
15	CH1_1+	LVDS Channel 1, Signal 1+	40	CH2_4-	LVDS Channel 2, Signal 4-
16	CH1_2-	LVDS Channel 1, Signal 2-	41	CH2_4+	LVDS Channel 2, Signal 4+
17	CH1_2+	LVDS Channel 1, Signal 2+	42	GND	Ground
18	GND	Ground	43	GND	Ground
19	CH1_CLK-	LVDS Channel 1, Clock -	44	GND	Ground
20	CH1_CLK+	LVDS Channel 1, Clock +	45	GND	Ground
21	GND	Ground	46	GND	Ground
22	CH1_3-	LVDS Channel 1, Signal 3-	47	NC	No connection
23	CH1_3+	LVDS Channel 1, Signal 3+	48	V_{DD}	Power Supply, +12V DC Regulated
24	CH1_4-	LVDS Channel 1, Signal 4-	49	V_{DD}	Power Supply, +12V DC Regulated
25	CH1_4+	LVDS Channel 1, Signal 4+	50	V_{DD}	Power Supply, +12V DC Regulated
			51	V_{DD}	Power Supply, +12V DC Regulated



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LCD connector: JAE FI-RE41S-HF

PIN	Symbol	Description	PIN	Symbol	Description
1	NC	No connection	21	CH3_3+	LVDS Channel 3, Signal 3+
2	NC	No connection	22	CH3_4-	LVDS Channel 3, Signal 4-
3	NC	No connection	23	CH3_4+	LVDS Channel 3, Signal 4+
4	NC	No connection	24	GND	Ground
5	NC	No connection	25	GND	Ground
6	NC	No connection	26	CH4_0-	LVDS Channel 4, Signal 0-
7	NC	No connection	27	CH4_0+	LVDS Channel 4, Signal 0+
8	NC	No connection	28	CH4_1-	LVDS Channel 4, Signal 1-
9	GND	Ground	29	CH4_1+	LVDS Channel 4, Signal 1+
10	CH3_0-	LVDS Channel 3, Signal 0-	30	CH4_2-	LVDS Channel 4, Signal 2-
11	CH3_0+	LVDS Channel 3, Signal 0+	31	CH4_2+	LVDS Channel 4, Signal 2+
12	CH3_1-	LVDS Channel 3, Signal 1-	32	GND	Ground
13	CH3_1+	LVDS Channel 3, Signal 1+	33	CH4_CLK-	LVDS Channel 4, Clock -
14	CH3_2-	LVDS Channel 3, Signal 2-	34	CH4_CLK+	LVDS Channel 4, Clock +
15	CH3_2+	LVDS Channel 3, Signal 2+	35	GND	Ground
16	GND	Ground	36	CH4_3-	LVDS Channel 4, Signal 3-
17	CH3_CLK-	LVDS Channel 3, Clock -	37	CH4_3+	LVDS Channel 4, Signal 3+
18	CH3_CLK+	LVDS Channel 3, Clock +	38	CH4_4-	LVDS Channel 4, Signal 4-
19	GND	Ground	39	CH4_4+	LVDS Channel 4, Signal 4+
20	CH3_3-	LVDS Channel 3, Signal 3-	40	GND	Ground
			41	GND	Ground

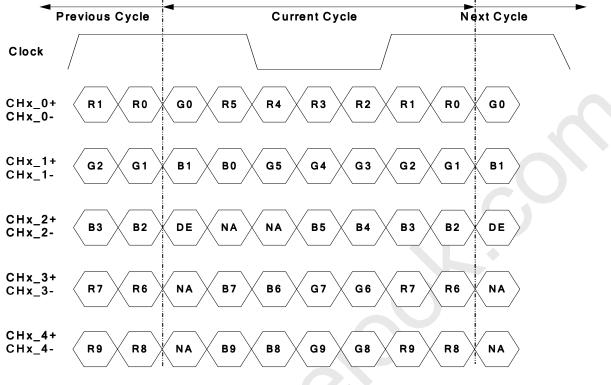
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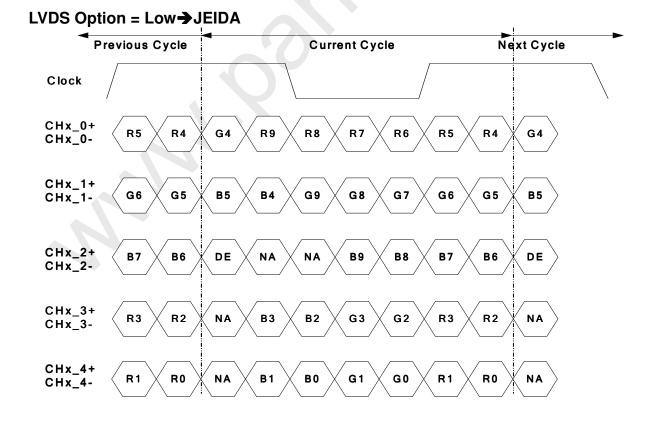


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LVDS Option = High/Open→NS



Note: x = 1, 2, 3, 4...



Note: x = 1, 2, 3, 4...

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3.3 Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Timing Table

Signal	Item	Symbol	Min.	Тур.	Max	Unit				
	Period	Τv	1090	1130	1392	Th				
Vertical Section	Active	Tdisp (v)		1080						
	Blanking	Tblk (v)	10	50	312	Th				
	Period	Th	540	570	580	Tclk				
Horizontal Section	Active	Tdisp (h)		480		Tclk				
	Blanking	Tblk (h)	60	90	100	Tclk				
Clock	Frequency	Fclk=1/Tclk	64.8	77.29	80.74	MHz				
Vertical Frequency	Frequency	Fv	94	120	122	Hz				
Horizontal Frequency	Frequency	Fh	120	135.6	139.2	KHz				

Notes:

(1) Display position is specific by the rise of DE signal only.

Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.

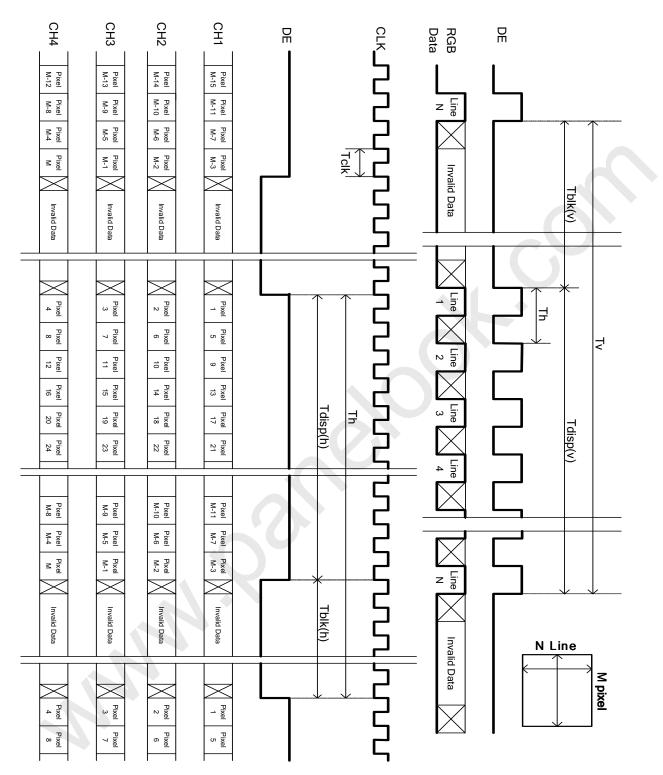
- (2)Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen.
- (3) If a period of DE "High" is less than 1920 DCLK or less than 1080 lines, the rest of the screen displays black.
- (4)The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.



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3.5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 10 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

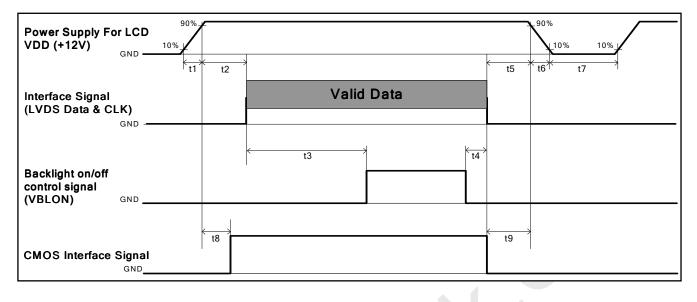
			COLOR DATA REFERENCE																												
														In	put	Col	or E	Data													
	Oslar	RED									GREEN								BLUE												
	Color	MS	в							L	SB	MSB LSB							SB	MS	SВ							L	SB		
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	B9	B8	B7	B6	В5	B4	B3	B2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
Basic	Blue(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R																															
	RED(1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
G																															
	GREEN(1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
В																															
	BLUE(1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

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3.6 Power Sequence for LCD



Deremeter		11			
Parameter	Min.	Туре.	Max.	Unit	
t1	0.4		30	ms	
t2	0.1		50	ms	
t3	450	<u> </u>		ms	
t4	0*1			ms	
t5	0			ms	
t6			*2	ms	
t7	500			ms	
t8	10		50	ms	
t9	0			ms	

Note:

(1) t4=0 : concern for residual pattern before BLU turn off.

(2) t6 : voltage of VDD must decay smoothly after power-off. (customer system decide this value)

(3) When CMOS Interface is N.C. (no connection), opened in Transmitted end, T8 timing spec can be negligible



3.7 Backlight Specification

The backlight unit contains 2pcs LED lightbar.

3.7.1 Electrical specification

	ltem	Symbol		Condition	Spec			Unit	Note
	item			Condition	Min	Тур	Мах	Onit	Note
1	Input Voltage	VD	DB	-	21.6	24	26.4	VDC	-
2	Input Current	I _{DI}	DB	VDDB=24V		1.55	1.69	ADC	1
3	Input Power	PD	DB	VDDB=24V		37.3	40.7	W	1
4	Inrush Current	I _{RU}	ISH	VDDB=24V	-	-	7	ADC	2
F		N/	ON		2	-	5.5		-
5	5 On/Off control voltage	V _{BLON}	OFF	VDDB=24V	0	-	0.8	VDC	-
6	On/Off control current	I _{BLON}		VDDB=24V			1.5	mA	-
7		V_DIM	MAX	VDDB=24V	3.0	-	5.5	VDC	-
7	Dimming Control Voltage		MIN			0	-	VDC	-
8	Dimming Control Current	I_C	MIM	VDDB=24V	-	-	2	mADC	-
9	Internal Dimming Ratio	DIM	1_R	VDDB=24V	5	-	100	%	3
10	External PWM		MAX	VDDB=24V	2	-	5.5		-
10	10 Control Voltage	V_EPWM	MIN	VDDB=24V	0	-	0.8	VDC	-
11	External PWM Control Current	I_EPWM		VDDB=24V	-	-	2	mADC	-
12	External PWM Duty ratio	D_EPWM		VDDB=24V	5	-	100	%	3
13	External PWM Frequency	F_EF	PWM	VDDB=24V	140	180	240	Hz	-

Note 1 : Dimming ratio= 100% (MAX) (Ta=25 \pm 5 $^{\circ}$ C , Turn on for 45minutes)

Note 2: Measurement condition Rising time = 20ms (VDDB : 10%~90%);

Note 3: Less than10% dimming control is functional well and no backlight shutdown happened

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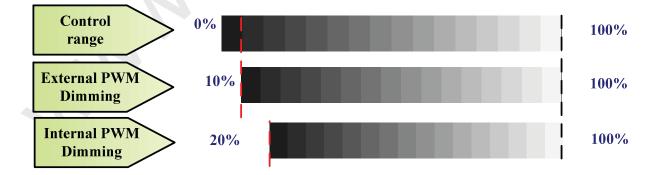


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3.7.2 Input Pin Assignment

LED Driver Connector: :CI0114M1HR0-NH (Cvilux)

Pin No	Symbol	Description			
1	VDDB	Operating Voltage Supply, +24V DC regulated			
2	VDDB	Operating Voltage Supply, +24V DC regulated			
3	VDDB	perating Voltage Supply, +24V DC regulated			
4	VDDB	Operating Voltage Supply, +24V DC regulated			
5	VDDB	Operating Voltage Supply, +24V DC regulated			
6	BLGND	Ground and Current Return			
7	BLGND	Ground and Current Return			
8	BLGND	Ground and Current Return			
9	BLGND	Ground and Current Return			
10	BLGND	Ground and Current Return			
11	DET	BLU status detection Normal : 0~0.8V L Abnormal : Open collector			
12	VBLON	BL On-Off control : BL On : High/Open (2.0V~5.5V); BL off : Low (0~0.8V/GND)			
13	VDIM	Internal PWM (20%~100%, 0V~3.3V)			
14	PDIM	External PWM (10%~100% Duty, open for 100%)			

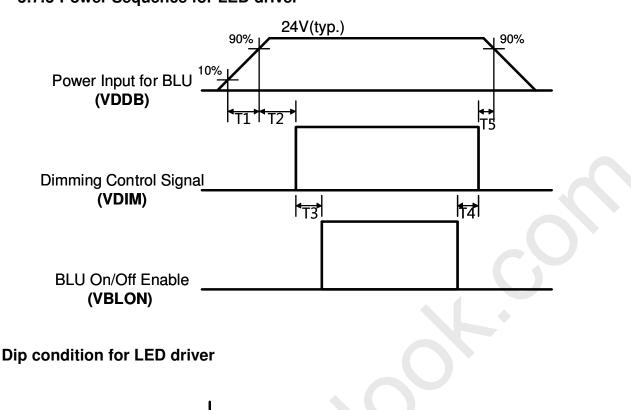


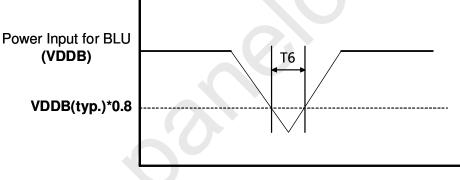
(Note*) IF External PWM function includes 10% dimming ratio. Judge condition as below:

- (1) Backlight module must be lighted ON normally.
- (2) All protection function must work normally.
- (3) Uniformity and flicker could NOT be guaranteed









Parameter		Unito		
	Min	Тур	Max	Units
T1	20	-	-	ms
T2	500	-	-	ms
ТЗ	250	-	-	ms
T4	0	-	-	ms
Т5	1	-	-	ms
Т6	-	-	10	ms

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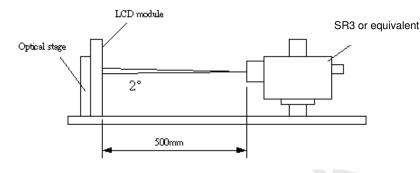


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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of ϕ and θ equal to 0°.

Fig.1 presents additional information concerning the measurement equipment and method.



	-					
Parameter	Symbol		Values		Unit	Notes
Farameter	Symbol	Min.	Тур.	Max	Onit	NOLES
Contrast Ratio	CR	3200	4000			1
Surface Luminance (White)	L _{WH}	320	400		cd/m ²	2
Luminance Variation	δ _{WHITE(9P)}	-		1.33		3
Response Time (G to G)	Тү		6.5		ms	4
Color Gamut	NTSC		72		%	
Color Coordinates		r				
Red	R _x		0.640			
	R _Y		0.330			
Green	G _X	1	0.300			
	G _Y	T.m. 0.00	0.620	Turn . 0.00		
Blue	B _X	Тур0.03	0.150	- Typ.+0.03		
	B _Y]	0.050			
White	W _X		0.280			
	W _Y]	0.290			
Viewing Angle						5
x axis, right(φ=0 °)	θ _r		89		degree	
x axis, left(φ=180°)	θι		89		degree	
y axis, up(φ=90°)	θ _u		89		degree	
y axis, down (φ=270°)	θ _d		89		degree	

Note:

1. Contrast Ratio (CR) is defined mathematically as:



Surface Luminance of Lon5

Contrast Ratio= Surface Luminance of Loff5

- Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When LED current I_F = typical value (without driver board), LED input VDDB =24V, I_{DDB}. = Typical value (with driver board), L_{WH}=Lon5 where Lon5 is the luminance with all pixels displaying white at center 5 location.
- 3. The variation in surface luminance, $\delta WHITE$ is defined (center of Screen) as:

 $\delta_{\text{WHITE(9P)}} = Maximum(L_{\text{on1}}, L_{\text{on2}}, \dots, L_{\text{on9}}) / Minimum(L_{\text{on1}}, L_{\text{on2}}, \dots, L_{\text{on9}})$

4. Response time T_{γ} is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on F_v=120Hz to optimize.

Measured				Target		
Response Time		0%	25%	50%	75%	100%
	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%
Start	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%	

The response time is defined as the following figure and shall be measured by switching the input signal for "any level of grey(bright) " and "any level of gray(dark)".

Any level of gray (Bright)

Any level of gray (Dark)

Any level of gray (Bright)

Any level of gray (Bright)

Any level of gray (Dark)

Any level of gray (Bright)

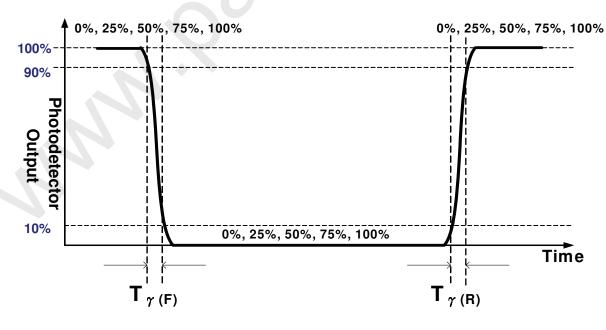
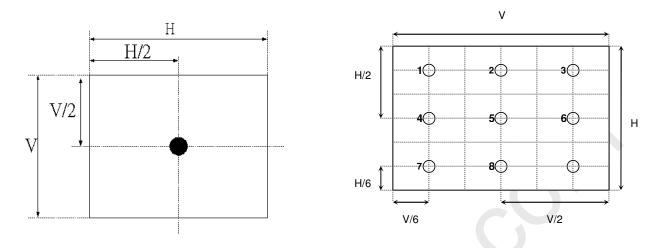


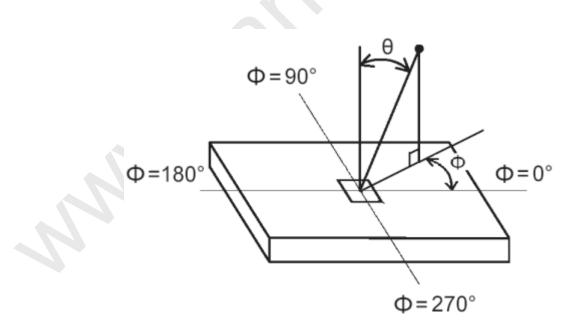


FIG. 2 Luminance



5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG3.

FIG.3 Viewing Angle







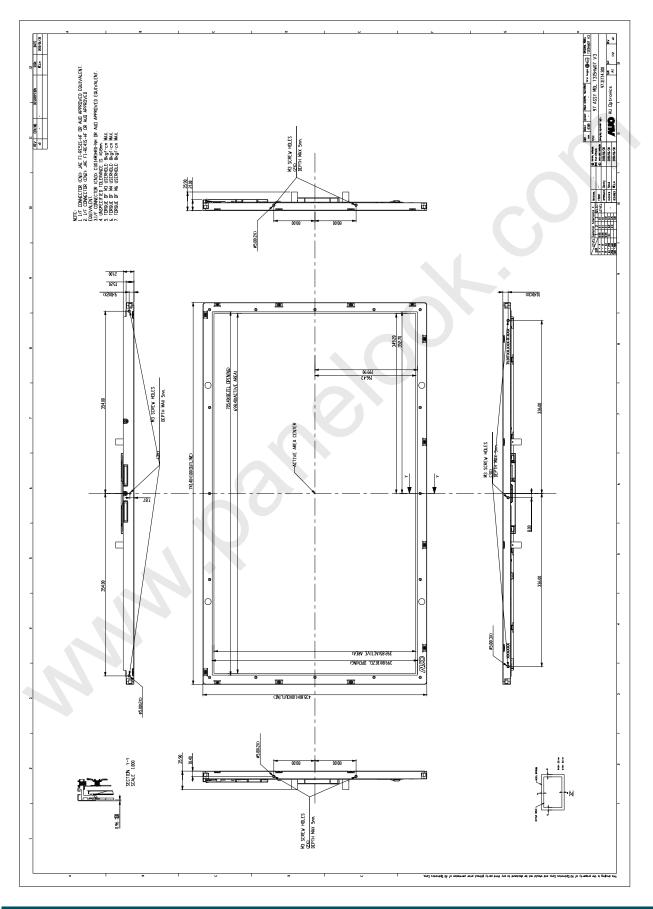
5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T315HW07 V3. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	741.40 mm		
Outline Dimension	Vertical	435.80 mm		
	Depth	22.4 mm (Front bezel to T-CON cover)		
	Horizontal	705.40 mm		
Bezel Opening	Vertical	399.80 mm		
Active Diaplay Area	Horizontal	698.40 mm		
Active Display Area	Vertical	392.85 mm		
Weight 6500 g		0 g (Тур.)		
Surface Treatment	Anti-Glare, 3H			



Front View



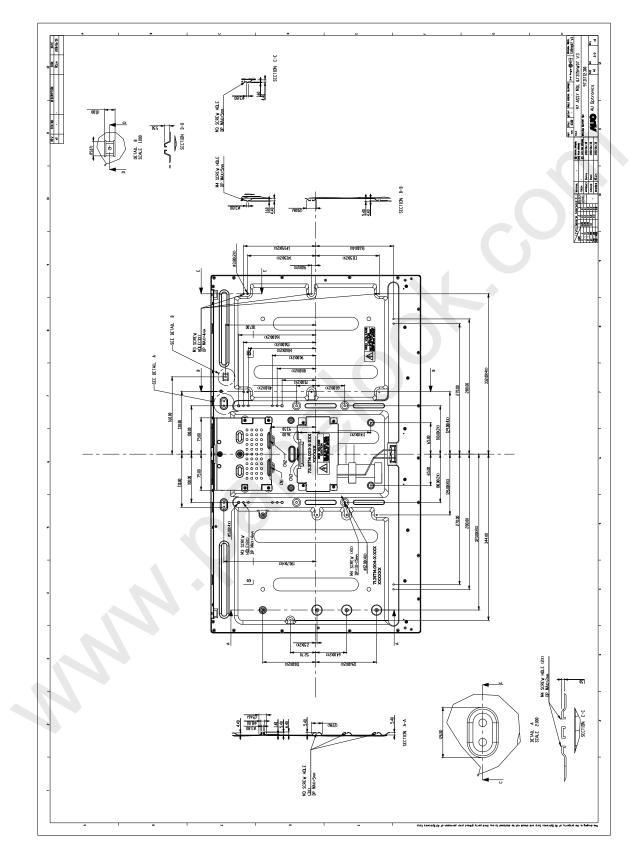
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Back View



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6. Reliability Test Items

	Test Item	Q'ty	Condition
1	High temperature storage test	3	60℃, 300hrs
2	Low temperature storage test	3	-20℃, 300hrs
3	High temperature operation test	3	50℃, 300hrs
4	Low temperature operation test	3	-5℃, 300hrs
5	Vibration test (non-operation)	3	Wave form: random Vibration level: 1.0G RMS Bandwidth: 10-300Hz, Duration: X, Y, Z 10min One time each direction
6	Shock test (non-operation)	3	Shock level: 50G Waveform: half since wave, 11ms Direction: ±X, ±Y, ±Z, One time each direction
7	Vibration test (With carton)	5	Random wave (1.0G RMS, 10-200Hz) 10mins/ Per each X,Y,Z axes
8	Drop test (With carton)	5	Height: 305mm 1 corner, 3 edges, 6 surfaces (ASTMD5276)

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7. International Standard

7.1 Safety

- (1) UL 60950-1, UL 60065; Standard for Safety of Information Technology Equipment Including electrical Business Equipment.
- (2) IEC 60950-1 : 2001, IEC 60065:2001 ; Standard for Safety of International Electrotechnical Commission
- (3) EN 60950 : 2001+A11, EN 60065:2002+A1:2006; European Committee for Electrotechnical Standardization (CENELEC), EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7.2 EMC

- ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- (2) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- (3) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998

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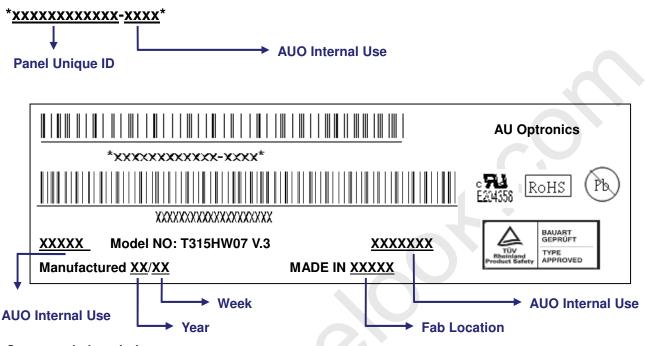


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8. Packing

8-1 DEFINITION OF LABEL:

A. Panel Label:



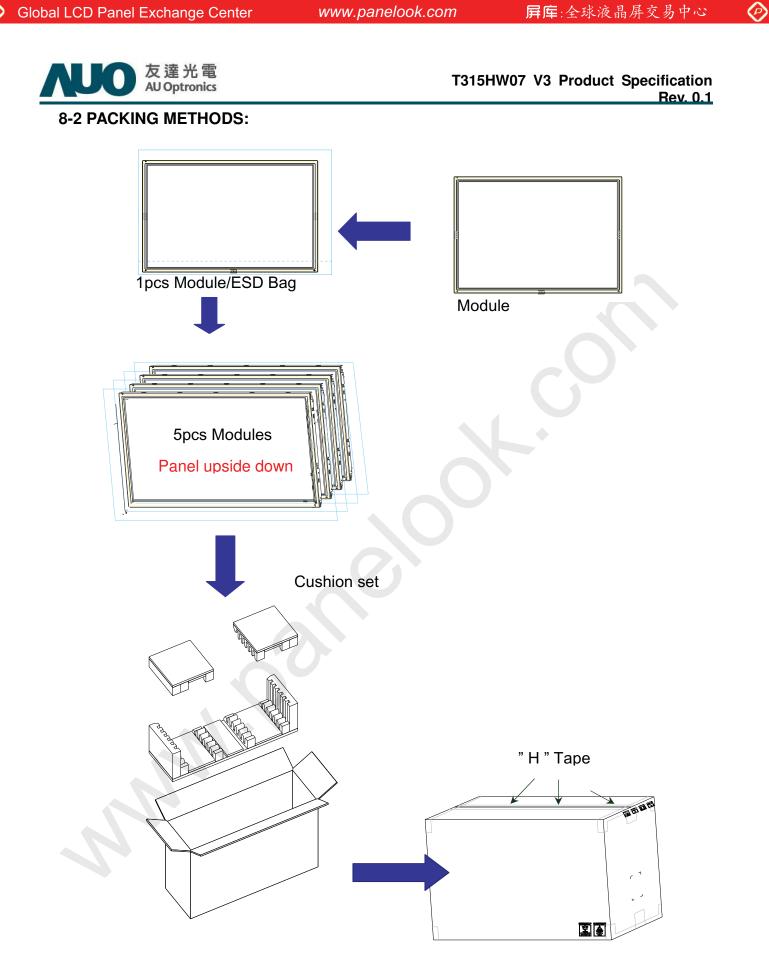
Green mark description

- (1) For Pb Free Product, AUO will add (1) for identification.
- (2) For RoHs compatible products, AUO will add RoHS for identification.

Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

B. Carton Label:

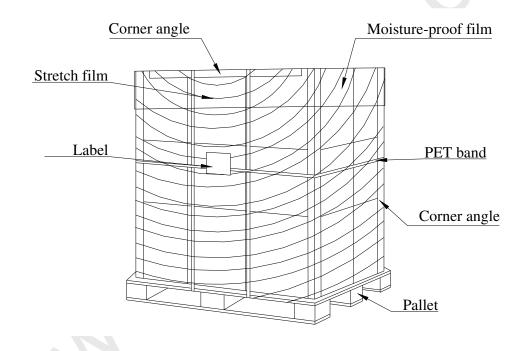






8-3 Pallet and Shipment Information

	ltem		Specification	Specification			
	nem	Quantity	Dimension	Weight (kg)	Remark		
1	Packing BOX	5pcs/box	5pcs/box 830(L)mm*285(W)mm*537(H)mm 36.5				
2	Pallet	1 1150(L)mm*840(W)mm*132(H)mm 1		13			
3	Boxes per Pallet	8 boxes/Pa	3 boxes/Pallet				
4	Panels per Pallet	40 pcs/pall	let				
	Pallet after		1150(L)mm*840(W)mm*1206(H)mm	305			
5		N/A	1150(L)mm*840(W)mm*2412(H)mm 610				
			Double Pallet	010			



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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter cause circuit broken by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- (1) The device listed in the product specification sheets was designed and manufactured for TV application
- (2) The spike noise causes the mis-operation of circuits. It should be lower than following voltage:
 V=±200mV(Over and under shoot voltage)
- (3) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (4) Brightness of CCFL depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (5) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (6) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (7) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall

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be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5° C and 35° C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.