Product Description: T370HW02 TFT-LCD PANEL with RoHS guarantee								
AUO Model Name: T3	AUO Model Name: T370HW02 VB							
Customer Part No/Project Name:								
Customer Signature	Date	AUO	Date					
		Approved By: PM Director / Frank Frank Flsu						
		Reviewed By: RD Director / Eugen	ctor / Eugene CC Chen					
		Reviewed By: Project Leader / Hua	Ing De Lin					
		Prepared By: PM / Eve Tung	198 II X					

Document Version: 0.2

Date: 2008/11/17

Product Specifications

37" HDTV Color TFT-LCD Module Model Name: T370HW02. VB

() Preliminary Specifications (*) Final Specifications

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Record of Revision

Version	Date	No	Old Description	New Description	Remark
0	2008/9		First issue		
1	2008/11		Modify the note 8 on page 8		
			& drawing		
2	2008/11		Final draft		
			a. Modify inverter power& current		
			b. LVDS definition		
			c. Drawing		

1. General Description

This specification applies to the 37.0 inch Color TFT-LCD Module T370HW02 VB. This LCD module has a TFT active matrix type liquid crystal panel 1920*1080 pixels, and diagonal size of 37.0 inch. This module supports 1920*1080 HDTV mode (Non-interlace). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T370HW02 VB has been designed to apply the 8-bit 2 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

The T370HW02 VB model is RoHS verified which can be distinguished on panel label.

* General Information

Items	Specification	Unit	Note
Active Screen Size	37.01	inch	
Display Area	819.36 (H) x 460.89(V)	mm	
Outline Dimension	877(H) x 514.6(V) x 54.3(D)	mm	With inverter
Driver Element	a-Si TFT active matrix		
Display Colors	8 bit, 16.7M	Colors	
Number of Pixels	1920 x 1080	Pixel	
Pixel Pitch	0.42675(H) x 0.42675(W)	mm	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment	Anti-Glare, 3H		Haze = 11

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause permanent damage to the unit.

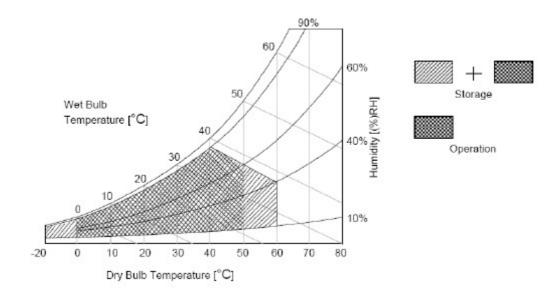
Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	Vcc	-0.3	14	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	3.6	[Volt]	Note 1
BLU Input Voltage	VDDB	-0.3	28	[Volt]	Note 1
BLU Brightness Control Voltage	Vdim	-0.3	7.0	[Volt]	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	НОР	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

Note 1: Duration:50 msec.

Note 2: Maximum Wet-Bulb should be 39°C and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40° C or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C.

Note 3: Surface temperature is measured at 50° C Dry condition



3. Electrical Specification

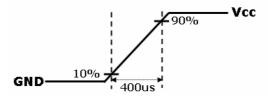
The T370HW02 VB requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the BLU, is to power inverter. (INV)

3-1 Electrical Characteristics

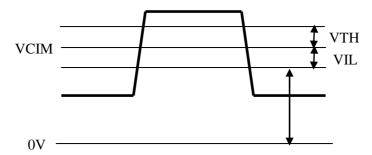
	Parameter			Va	lues	Unit	Notes	
			Min	Тур	Max			
LCD:								
Power	Supply Input Voltage	Vcc	10.8	12	13.2	Vdc	1	
Power	Supply Input Current	Icc	-	1	1.2	A	2	
Power	Consumption	Pc	-	12	14.4	Watt	2	
Inrush	Current	I_{RUSH}	-	-	4	Apeak	3	
LVDS	Differential Input High Threshold	VTH			100	mV	4	
Interface	Voltage							
	Differential Input Low Threshold	VTL	-100			mV	4	
	Voltage							
	Common Input Voltage	VCIM	1.10	1.25	1.40	V	4	
CMOS	Input High Threshold Voltage	VIH	2.4		3.3	Vdc		
Interface		(High)						
	Input Low Threshold Voltage	VIL	0		0.9	Vdc		
		(Low)						
Backlight	Power Consumption	PDDB	133	140	147	Watt		
Life Time	;		50,000			Hours		

Note:

- 1. The ripple voltage should be controlled under 10% of V_{CC}
- 2. Vcc=12.0V, f_v = 60Hz, fCLK=81.5Mhz , 25°C , Test Pattern : White Pattern
- **3.** Measurement condition :



4. VCIM = 1.2V



- 5. The performance of the Lamp in LCD panel, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC Inverter. So all the parameters of an inverter should be carefully designed as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. After confirmation, the LCD panel should be operated in the same condition as installed in your instrument.
- **6.** Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to conducting tape, TFT-LCD Module have a low luminance and the inverter has abnormal action because leakage current occurs between lamp wire and conducting tape.
- 7. The relative humidity must not exceed 80% non-condensing at temperatures of 40° C or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C. When operate at low temperatures, the brightness of CCFL will drop and the life time of CCFL will be reduced.
- 8. Specified values are for a single lamp only which is aligned horizontally. The lifetime is defined as the time which luminance of the lamp is 50% compared to its original value.

 [Operating condition: Continuous operating at Ta = 25±2°C]

3-2 Interface Connections

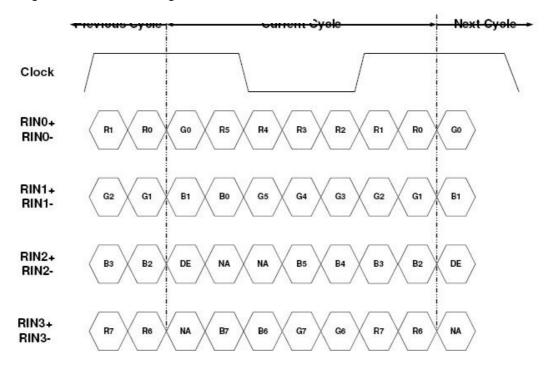
LCD connector (CN1): JAE FI-RE51S-HF

connector (CN1): JAE FI-RE51S-HF							
	JAE FI-RE51S-HF or equivalent						
VCC	+12V, DC, Regulated						
VCC	+12V, DC, Regulated						
VCC	+12V, DC, Regulated						
VCC	+12V, DC, Regulated						
VCC	+12V, DC, Regulated						
GND	Ground and Signal Return						
GND	Ground and Signal Return						
GND	Ground and Signal Return						
GND	Ground and Signal Return						
RO[0]-	Odd LVDS Signal -						
RO[0]+	Odd LVDS Signal +						
RO[1]-	Odd LVDS Signal -						
RO[1]+	Odd LVDS Signal +						
RO[2]-	Odd LVDS Signal -						
RO[2]+	Odd LVDS Signal +						
GND	Ground and Signal Return						
ROCLK-	Odd LVDS Signal -						
ROCLK+	Odd LVDS Signal +						
GND	Ground and Signal Return						
RO[3]-	Odd LVDS Signal -						
RO[3]+	Odd LVDS Signal +						
Reserved	Please leave it open						
Reserved	Please leave it open						
GND	Ground and Signal Return						
RE[0]-	Even LVDS Signal -						
RE[0]+	Even LVDS Signal +						
RE[1]-	Even LVDS Signal -						
RE[1]+	Even LVDS Signal +						
RE[2]-	Even LVDS Signal -						
RE[2]+	Even LVDS Signal +						
GND	Ground and Signal Return						
RECLK-	Even LVDS Clock -						
RECLK+	Even LVDS Clock +						
GND	Ground and Signal Return						
RE[3]-	Even LVDS Signal -						
RE[3]+	Even LVDS Signal +						
Reserved	Please leave it open						
Reserved	Please leave it open						
GND	Ground and Signal Return						
Reserved	Please leave it open						
Reserved	Please leave it open						
Reserved	Please leave it open						
Reserved	Please leave it open						
Reserved	Please leave it open						
LVDS Option	High/Open for Normal (NS), Low for JEIDA						
Reserved	Please leave it open						
Reserved	Please leave it open						
Reserved	Please leave it open						
Reserved	Please leave it open						
Reserved	Please leave it open						
Reserved	Please leave it open						
	i the state of the						

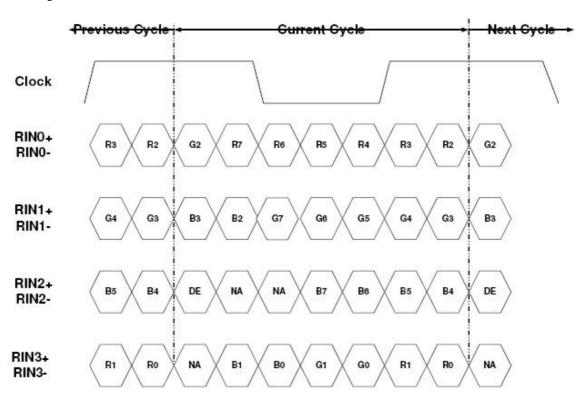
Note:

- 1. All GND (ground) pins should be connected together and should also be connected to the LCD's metal frame. All Vcc (power input) pins should be connected together.
- 2. For Pin 10, 27 and 28, panel will not damage if negligently connect these pins to high or low

LVDS Option = H(3.3V) & open for NS



LVDS Option = L(GND) for JEIDA



3-3 Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

* Timing Table DE only Mode

Signal	Item	Symbol	Min	Type	Max	Unit
	Period	Tv	1090	1125	1480	Th
Vertical Section	Active	Tdisp (v)		1080		Th
333	Blanking	Tblk (v)	10	45	400	Th
	Period	Th	1030	1100	1300	Tclk
Horizontal Section	Active	Tdisp (h)		Tclk		
33311011	Blanking	Tblk (h)	70	140	340	Tclk
LVDS Clock	Frequency	1/Tclk	50	74.25	82	MHz
Vertical Frequency	Frequency	Freq	47	60	63	Hz
Horizontal Frequency	Frequency	Freq	60	67.5	73	KHz

Notes:

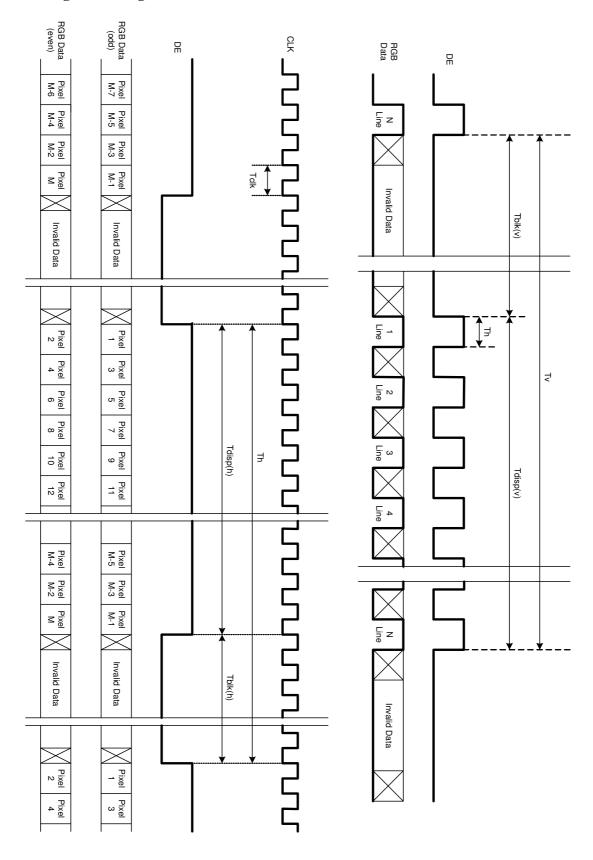
Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.

Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise the of 1st DE is displayed at the top line of screen.

- 3.) If a period of DEB "High" is less than 1920 DCLK or less than 1080 lines, the rest of the screen displays black.
- 4.) The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.

^{1.)} Display position is specific by the rise of DE signal only.

3-4 Signal Timing Waveforms



3-5 Color Input Data Reference

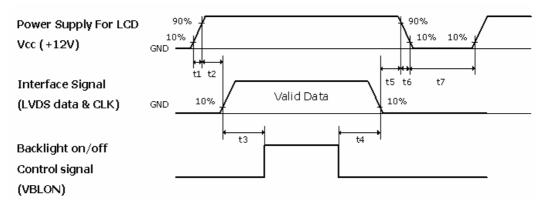
The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

			Input Color Data																						
Color		RED					GREEN						BLUE												
		MS	В					I	LSB	MS	В					I	SB	MS	В					I	LSB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	В4	В3	B2	В1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																									
	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																		Se 111 11 11 11 11 11 11 11 11 11 11 11 1							
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



3.6.1 Power Sequence for LCD



Parameter		Units			
Parameter	Min.	Тур.	Max.	Omts	
T1	0.4		30	ms	
T2	0.1		50	ms	
Т3	300			ms	
T4	10			ms	
T5	0.1		50	ms	
T6			300	ms	
T7	500			ms	

Note:

The timing controller will not be damaged in case of TV set AC input power suddenly shut down.

Once power reset, it should follow power sequence as spec. definition.

(1) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become abnormal screen.



3.7.1 Electrical specification

corresponding	5.7.1 Electrical Specification								
Item	Sym	abol	Condition		Spec	Unit	Note		
nem	Syn	1001	Condition	Min	Typical	Max	Oint	Note	
Input Voltage	V_{D}	DB		22.5	24	26.4	VDC		
Input Current	I_{DI}	ЭB	VDDB=24V	-	6	6.35	ADC	1	
Input Power	P _D	DB	VDDB=24V	-	140	147	W	1	
Inrush current	I_{RU}	SH	VDDB=24V	-	-	8.5	ADC	2	
Output Frequency	F	BL	VDDB=24V	53	55	57	KHz		
On /Off a autual analta an	V_{BLON}	ON	VDDD 24V	2	-	5	VDC		
On/Off control voltage		OFF	VDDB=24V	0	-	0.8			
Dimming Control Voltage	V_{DIM}	MAX	VDDB=24V	0	-	3.3	VDC		
Internal Dimming Ratio	DIM	I_R		20		100	%		
DWM acutual Valence	V_EPWM	MAX	VDDB=24V	2		3.3	VDC		
PWM control Voltage		MIN	VDDB=24V	0		0.8			
External PWM control Current	I_EP	WM	VDDB=24V			2	mADC		
External PWM Duty ratio	D_EP	PWM	VDDB=24V	10		100	%	3	
External PWM Frequency	F_EP	WM	VDDB=24V	140	180	240	Hz		

Note 1 : VDIM= 3.3V (MAX) (Ta= $25\pm5^{\circ}$ C, Turn on for 45minutes)

Note 2 : Measurement condition Rising time = 20 ms (VDDB : 10%~90%); Note 3 : (a) Uniformity and flicker do not guarantee below 20% dimming control. (b) 5% dimming control is function okay and no backlight shut down

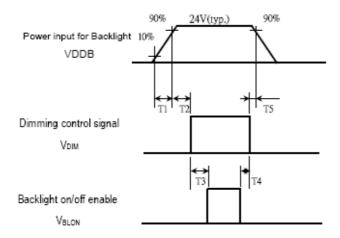


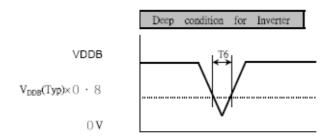
3.7.2 Input Pin Assignment CN2: Cl0114M1HRL-NH (Cvilux)

Pin No	Symbol	Description
1	VDDB (Main Power)	DV input 24.0 VDC
2	VDDB (Main Power)	DV input 24.0 VDC
3	VDDB (Main Power)	DV input 24.0 VDC
4	VDDB (Main Power)	DV input 24.0 VDC
5	VDDB (Main Power)	DV input 24.0 VDC
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	GND	Ground
1 0	G N D	Ground
1 1	DET Function	Panel status detect (Normal=0~0.8V, Abnormal=Open collector)
12	VBLON (Enable Pin)	BL On/Off control signal High/Open: On, Low: Off (Low=0 $^{\sim}$ 0.8 V, High=2.0 $^{\sim}$ 5.0 V)
13	V D IM	Internal PW M (3.3V,100% duty)for 100% < NC ; when External PW M >
1 4	P D IM	External PW M input (AC 0~3.3V, Duty: 10%~100%) < NC; when Internal PW M >



3.7.3 Power Sequence for Inverter





Parameter		Units		
	Min. Typ.		Max.	
T1	20	-	-	ms
T2	50	-	-	ms
T3	0	-	-	ms
T4	0	-	-	ms
T5	0	-	-	ms
T6	-	-	10	ms



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at $25\,^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° .

Optical stage

2°

500mm

Fig.1 1 presents additional information concerning the measurement equipment and method.

Parameter		Symbol		Values			Units	Notes
				Min.	Тур.	Max.		
Contrast Ratio	Contrast Ratio		R	2000	2500			1
Surface Lumina	ance, white	LW	/H	400	500		cd/m²	2
Luminance Var	riation	δ_{white}	9 p			1.30		3
Response time	G to G	Т	γ		6.5	8	ms	4
Color Gamut		NT	SC		72		%	
Color Coordina	ates							
	RED	R	X		0.64			
		R	Y		0.33	_		
	GREEN	G	X		0.29	- Typ.+0.03		
		G	Y	True 0.02	0.60			
	BLUE	В	X	Typ0.03	0.15			
		В	Y		0.06			
WHITE		W	'x		0.28	=		
		W	Y _Y		0.29	-		
Viewing Angle	- /							
x axis, rigl	ht(φ=0°)	θ	r		89		degree	5
x axis, left(φ=180°)		θ	1		89]	
y axis, up((φ=90°)	θ	u		89]	
y axis, dov	wn (φ=0°)	θ	d		89]	



Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\label{eq:contrast Ratio} \begin{aligned} & \underline{\textbf{Surface Luminance of L_{on5}}} \\ & \underline{\textbf{Surface Luminance of L_{off5}}} \end{aligned}$$

Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When VDDB = 24V, IDDB = 5A, L_{WH} =Lon5, where Lon5 is the luminance with all pixels displaying white at center 5 location.

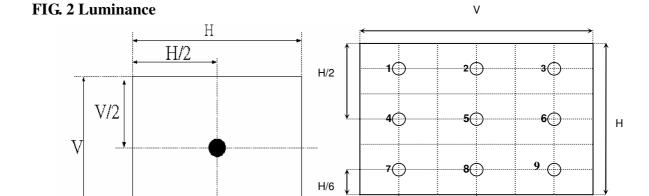
2. The variation in surface luminance, δ WHITE is defined (center of Screen) as:

$$\delta_{WHITE(9P)} = Maximum(L_{on1}, L_{on2}, ..., L_{on9}) / \ Minimum(L_{on1}, L_{on2}, ... L_{on9})$$

3. Response time $T\gamma$ is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100%) brightness matrix and is based on f_v =60Hz to optimize.

	0%	25%	50%	75%	100%
0%		t:0%-25%	t:0%-50%	t:0%-75%	t:0%-100%
25%	t:25%-0%		t:25%-50%	t:25%-75%	t:25%-100%
50%	t:50%-0%	t:50%-25%		t:50%-75%	t:50%-100%
75%	t:75%-0%	t:75%-25%	t:75%-50%		t:50%-100%
100%	t:100%-0%	t:100%-25%	t:100%-50%	t:100%-75%	

4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG4.



V/6

V/2



FIG.3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "any level of gray(bright)" and "any level of gray(dark)".

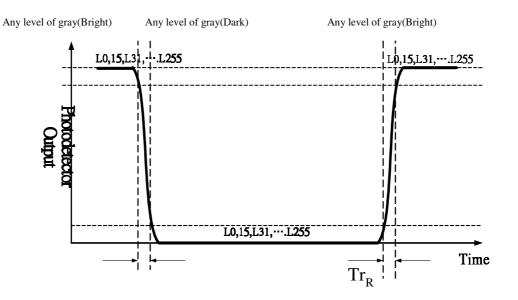
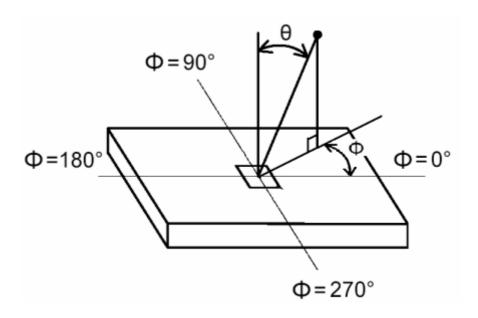


FIG.4 Viewing angle





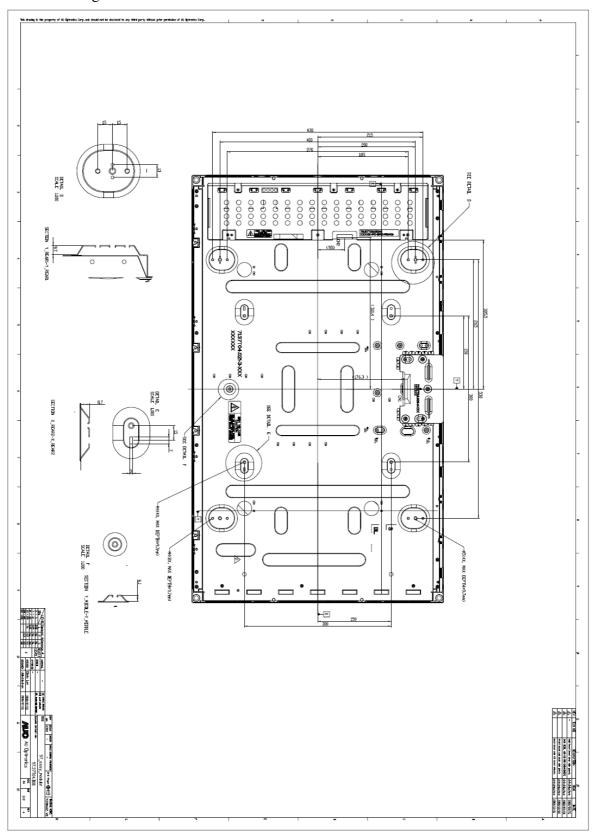
5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T370HW02 VB. In addition the figures in the next page are detailed mechanical drawing of the LCD.

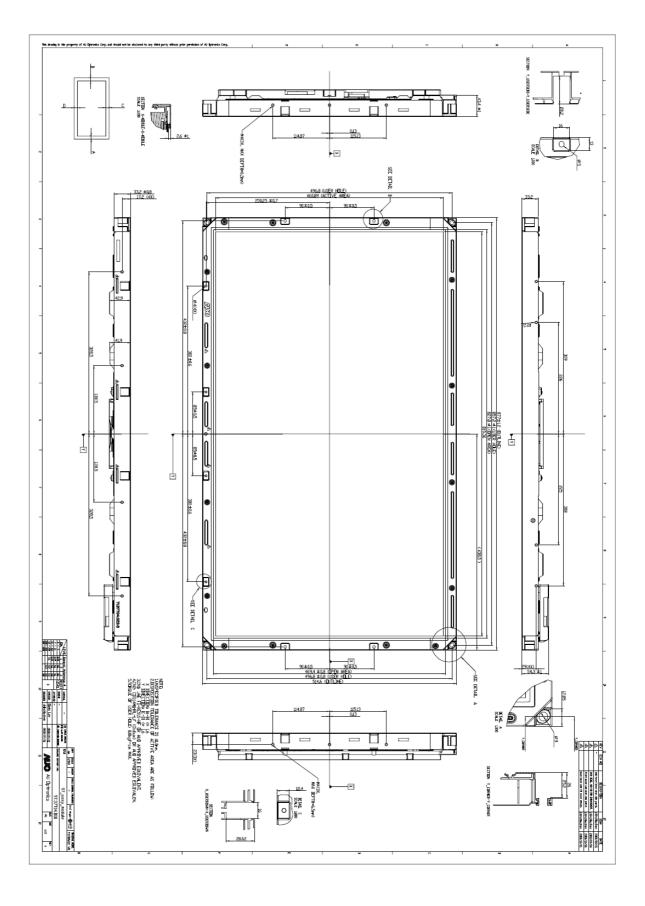
	Horizontal	877.0 mm	
Outline Dimension	Vertical	514.6mm	
Outilie Dimension	Depth	54.3 mm(with inverter)	
Bezel Opening	Horizontal	827.8 mm	
	Vertical	469.4 mm	
And Divile And	Horizontal	819.36 mm	
Active Display Area	Vertical	460.89 mm	
Weight	10000g (Typ.)		



Mechanical Figure:









6. Reliability:

Environment test condition

	Test Items	Q'ty	Conditions
1	High Temperature Stroage	3	60℃ 300 hrs
2	Low Temperature Stroage	3	-20℃, 300 hrs
3	High Temperature Operation	3	50℃, 300 hrs
4	Low Temperature Operation	3	-5℃, 300 hrs
5	Vibration (non-operation)	3	(10 ~ 300Hz/1.5G/11min SR, XYZ 30min/axis) Vibration level : 1.5G RMS, Bandwidth : 10-300Hz Duration: X, Y, Z 30min,
6	Shock (non-operation)	3	Shock level: 50G Waveform: have sine wave, 11ms Direction: ±X,±Y, ±Z One time each direction
7	Vibration (With carton)	3	Random wave (1.5 Grms 10~200Hz) 30mins / Per each X.Y.Z axes
8	Drop (With carton)	3	Height: 38cm 1 corner, 3 edges, 6 surfaces (ASTMD4169-I)



7. International Standard

7-1. Safety

(1) UL1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995

Standard for Safety of Information Technology Equipment Including electrical Business Equipment.

(2) CAN/CSA C22.2 No. 950-95/60950 Third Edition, Canadian Standards Association,

Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

(3) EN60950: 1992+A2: 1993+A2: 1993+C3: 1995+A4: 1997+A11: 1997

IEC 950: 1991+A1: 1992+A2: 1993+C3: 1995+A4:1996

European Committee for Electrotechnical Standardization (CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998



8. Packing

Panel label:



TW8800900028-ZM0200

TW88009: T: Taiwan, A/B: China 00028: Panel Serial Number

ZM0: AUO internal code

Manufactured 08/36: 2008 week 36

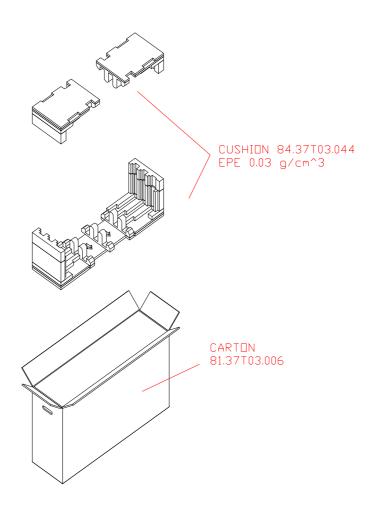
Made In Taiwan: Taiwan made

Carton Label:





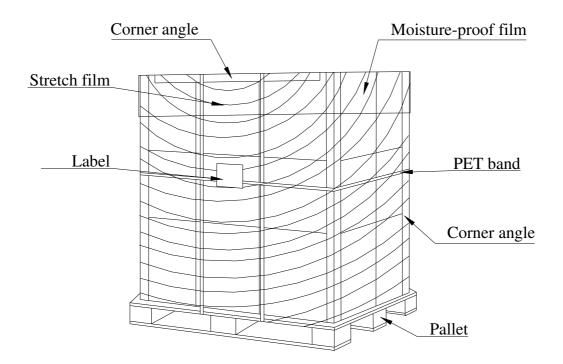
Carton:



Pallet and Shipment information

	Item	Specification						
	item	Qty.	ty. Dimension		Remark			
1	Packing BOX	3 pcs/box	3 pcs/box 965(L)mm*280(W)mm*610(H)mm 36					
2	Pallet	1 1140(L)mm*980(W)mm*140(H)mm 1		15				
3	Boxes per Pallet	8 boxes/Pallet (boxes/Pallet (By Air); 12 Boxes/Pallet (By Sea)					
4	Panels per Pallet	24pcs/pallet(By	24pcs/pallet(By Air); 36 Boxes/Pallet (By Sea)					
	Pallet after packing	24 (by Air) 1140(L)mm*980(W)mm*1360(H)mm (by Air) 25		257 (by Air)				
		36(by Sea) 1140(L)mm*980(W)mm*2110(H)mm (by Sea) 393 (by Sea)		393 (by Sea)				





9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.



9-2 OPERATING PRECAUTIONS

- (1) The device listed in the product specification sheets was designed and manufactured for TV application
- (2) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (3) Response time depends on the temperature. (In lower temperature, it becomes longer...)
- (4) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (5) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (6) When fixed patterns are displayed for a long time, remnant image is likely to occur.

Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE+

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5° C and 35° C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of flue still on the Bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the Bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Appendix - A

i. EMI specification

Model name: T37HW02 VB

Item	Min	Тур	Max	Unit
EMI level (Note)			-6	dB(μ V/m)
SSCG		350		ps

Note:

甲、Criteria: CISPR22

Z Signal generator: PSG400 (Sony EMCS)

丙、EMI site: Sony EMCS Ichinomiya Tec. or using correlation value

T · Find result should be checked by connecting with TV-set