

isc Thyristors

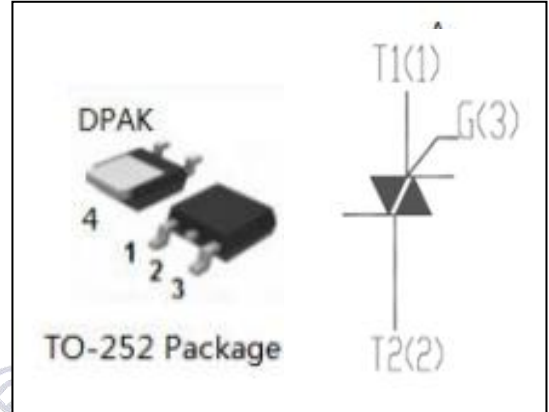
T410-600B

DESCRIPTION

- With TO-252(DPAK) packaging
- Operating in 3 quadrants
- High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Switching applications
- Phase control
- Motor control circuits
- Static switching on inductive or resistive load



ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	MAX	UNIT
V_{DRM}	Repetitive peak off-state voltage	600	V
V_{RRM}	Repetitive peak reverse voltage	600	V
$I_{\text{T(RSM)}}$	Average on-state current	4	A
		@ $T_c=110^{\circ}\text{C}$	
I_{TSM}	Surge non-repetitive on-state current	30 31	A
		50HZ 60HZ	
$P_{\text{G(AV)}}$	Average gate power dissipation (over any 20 ms period)	0.5	W
T_j	Operating junction temperature	-40~125	$^{\circ}\text{C}$
T_{stg}	Storage temperature	-40~150	$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS ($T_c=25^{\circ}\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	$V_R=V_{\text{RRM}}$ Rated; $V_D=V_{\text{DRM}}$ Rated;		0.005 1	mA
I_{DRM}	Repetitive peak off-state current				
V_{TM}	On-state voltage	$I_T=5.5\text{A}$		1.6	V
I_{GT}	Gate-trigger current	$V_D=12\text{V}; \text{RL}=30\ \Omega$	I	10	mA
			II	10	
			III	10	
V_{GT}	Gate-trigger voltage	$V_D=12\text{V}; \text{RL}=30\ \Omega$		1.3	V
$R_{\text{th (j-mb)}}$	Junction to mounting base	Half cycle		2.6	$^{\circ}\text{C/W}$

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