Product Description: T420XW01 V9 TFT-LCD PANEL with ROHS Guarantee			
AUO Model Name: T420XW01 V9			
Customer Part No/Project Name:			
Customer Signature	**Date**	AUO	**Date**
		Approved by: PM Sr. Manager/ Jennifer Lin Jennifer L 7/3/07	
		Reviewed by: RD Director / Hong-Jye Hong Hong Hong-Jye 7/3 2007 Reviewed by: Project Leader / Gavin Huang Gavin Huang 7/3 '07	
		Prepared By: PM / Stanley Chiang Stanley Chiang 7/3 '07	

Document Version:2.1
Date:2007/7/26

Product Functional Specification

42” WXGA Color TFT-LCD Module
Model Name: T420XW01 V9

( ) Preliminary Specification
(*) Final Specification

Contents

Record of Revision

Version	Date	No	Old Description	New Description	Remark
1.0	2007/4/01		Preliminary specification first release		
1.1	2007/5/28		Format modify		
2.0	2007/6/28		Final specification first release		
2.1	2007/7/26			Update Backlight Specification	
2.2	2007/8/6			Update Backlight Specification	

1. General Description

This specification applies to the 42 inch Color TFT-LCD Module T420XW01 V9. This LCD module has a TFT active matrix type liquid crystal panel 1366x768 pixels, and diagonal size of 42 inch. This module supports 1366x768 WXGA mode (Non-interlace).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T420XW01 V9 has been designed to apply the 8-bit 1 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

T420XW01 V9 Backlight unit used C-balance board (inverter-less) solution. This backlight unit should bundle integral TV power system to use.

* General Information

Items	Specification	Unit	Note
Active Screen Size	42.02	inches	
Display Area	930.25(H) x 523.01(V)	mm	
Outline Dimension	983.0(H) x 576.0(V) x 45.3(D)	mm	Without inverter
Driver Element	a-Si TFT active matrix		
Display Colors	16.7M	Colors	
Color Gamut	72	%	NTSC
Number of Pixels	1366 x 768	Pixel	
Pixel Pitch	0.681	mm	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Lamp quantity, type	18pcs, Straight type	pcs	
Surface Treatment	AG, 3H, Haze : 40%		

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Item	Symbol	Min	Max	Unit	Note
Logic/LCD Drive Voltage	V_{DD}	-0.3	14.0	[Volt]	1
Input Voltage of Signal	Vin	-0.3	3.6	[Volt]	1
Operating Temperature	T_{OP}	0	+50	[^{o}C]	2
Operating Humidity	H_{OP}	10	90	[%RH]	2
Storage Temperature	T_{ST}	-20	+60	[^{o}C]	2
Storage Humidity	H_{ST}	10	90	[%RH]	2
Shock (non-operation)		-	50	G	3
Vibration (non-operation)		-	1.5	G	4
Thermal shock		-20	60	C	5
Panel surface temp			60	C	6

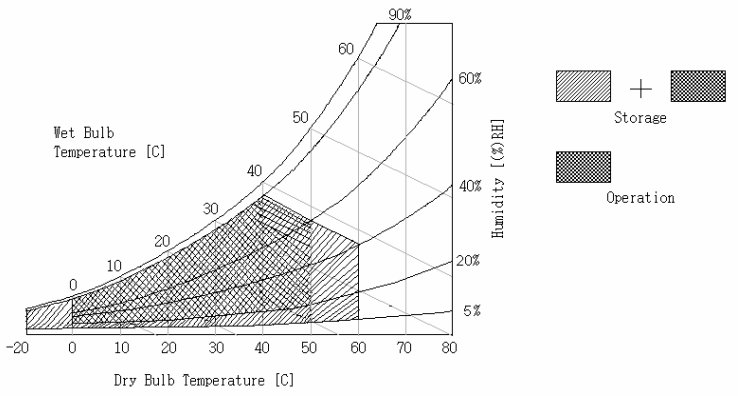
Note 1 : Duration = 50msec

Note 2 : Maximum Wet-Bulb should be 39℃ and No condensation.

Note 3 : Half sine wave, shock level : 50G(11ms), direction : ±x, ±y, ±z (one time each direction)

Note 4 : Wave form : random, vibration level : 1.5G RMS, Bandwidth : 10--300Hz
Duration : X,Y,Z 30min (one time each direction)

Note 5 : -20C/0.5hr ~ 60C/0.5hr, 10 cycles



Note 6 :Panel only (without TV set), Ambient temp 25C

3. Electrical Specification

The T420XW01 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input, which powers the CCFL, is typically generated by an inverter.

3-1 Electrical Characteristics

Parameter		Symbol	Values			Unit	Notes
			Min	Typ	Max		
LCD:							
Power Supply Input Voltage		Vdd	10.8	12	13.2	Vdc	
Power Supply Input Current		Idd	-	0.7	0.84	A	1
			-	0.6	0.72	A	2
Power Consumption		Pc	-	9	10.5	Watt	1
Inrush Current		I_{RUSH}	-	-	5	A	6
LVDS Interface	Differential Input High Threshold Voltage	V_{TH}			+100	mV	5
	Differential Input Low Threshold Voltage	V_{TL}	-100			mV	5
	Common Input Voltage	V_{CIM}	1.10	1.25	1.40	V	
CMOS Interface	Input High Threshold Voltage	V_{IH} (High)	2.4		3.3	Vdc	
	Input Low Threshold Voltage	V_{IL} (Low)	0		0.7	Vdc	
Backlight Power Consumption			-	165	-	Watt	3
Life Time			50000	60000		Hours	4

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC Inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you

confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.

Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to conducting tape, TFT-LCD Module have a low luminance and the inverter has abnormal action because leakage current occurs between lamp wire and conducting tape.

The relative humidity must not exceed 80% non-condensing at temperatures of 40℃ or less. At temperatures greater than 40℃, the wet bulb temperature must not exceed 39℃. When operate at low temperatures, the brightness of CCFL will drop and the lifetime of CCFL will be reduced.

Note :

1. Vdd=12.0V, fv=60Hz, f_{CLK}=81.5 Mhz , 25℃, Vdd Duration time= 400 *ms*, Test pattern : Full white pattern
2. Vdd=12.0V, fv=60Hz, f_{CLK}=81.5 Mhz , 25℃, Vdd Duration time= 400 *ms*, Test pattern : 8x6 mosaic pattern
3. The Backlight power consumption shown above does include loss of external inverter at 25 ℃. The used lamp current is the lamp typical current I_L=5.5mA
4. The life is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ±2℃.
5. VCIM = 1.2V

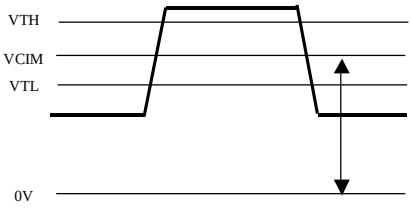
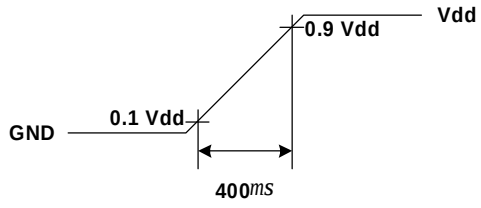


Figure : LVDS Differential Voltage

6. Measurement Condition: Rising time = 400 μs

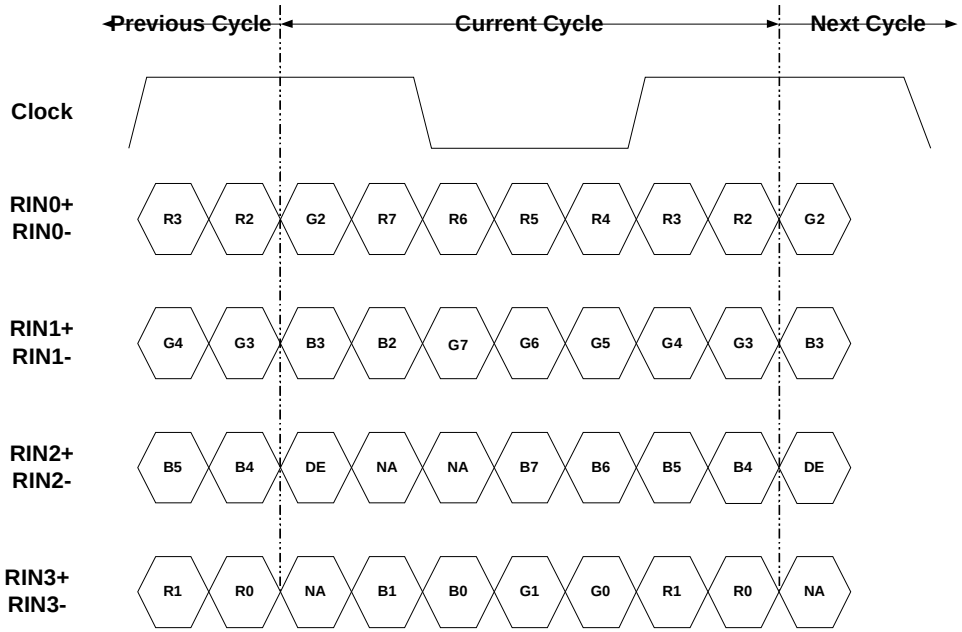


3-2 Interface Connections

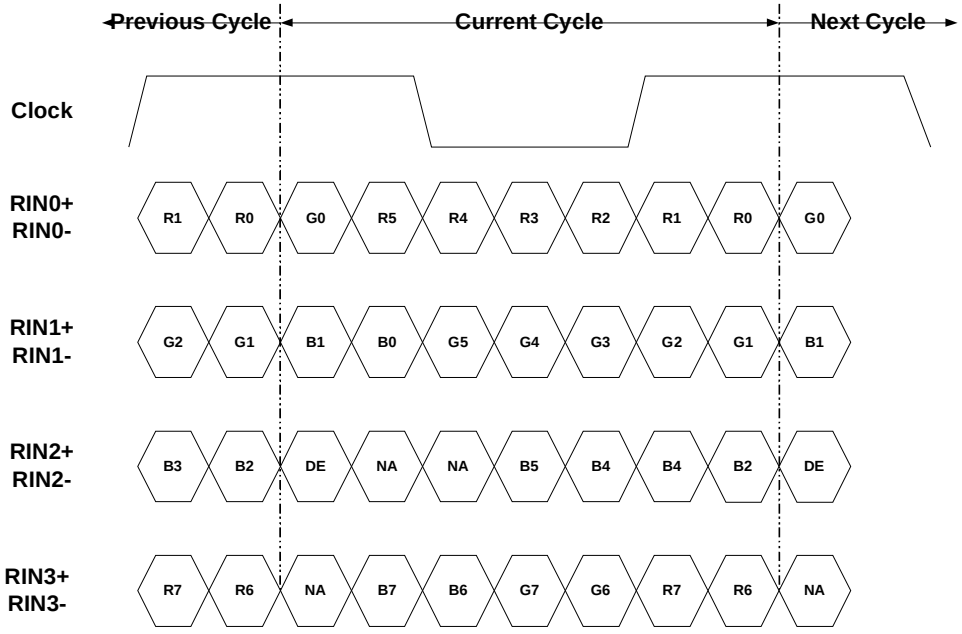
- LCD connector: FI-X30SSL-HF (JAE) or equivalent
- Mating connector: FI-30C2L (JAE) or equivalent

Pin No	Symbol	Description	Note
1	VCC	+12V, DC, Regulated	
2	VCC	+12V, DC, Regulated	
3	VCC	+12V, DC, Regulated	
4	VCC	+12V, DC, Regulated	
5	GND	Ground and Signal Return	
6	GND	Ground and Signal Return	
7	GND	Ground and Signal Return	
8	GND	Ground and Signal Return	
9	LVDS Option	Low/Open for Normal (NS), High for JEIDA	Default : NS mode
10	Reserved	Open or High	AUO internal test
11	GND	Ground and Signal Return for LVDS	
12	RXIN0-	LVDS Channel 0 negative	
13	RXIN0+	LVDS Channel 0 positive	
14	GND	Ground and Signal Return for LVDS	
15	RXIN1-	LVDS Channel 1 negative	
16	RXIN1+	LVDS Channel 1 positive	
17	GND	Ground and Signal Return for LVDS	
18	RXIN2-	LVDS Channel 2 negative	
19	RXIN2+	LVDS Channel 2 positive	
20	GND	Ground and Signal Return for LVDS	
21	RXCLKIN-	LVDS Clock negative	
22	RXCLKIN+	LVDS Clock positive	
23	GND	Ground and Signal Return for LVDS	
24	RXIN3-	LVDS Channel 3 negative	
25	RXIN3+	LVDS Channel 3 positive	
26	GND	Ground and Signal Return for LVDS	
27	Reserved	Open or High	AUO internal test
28	Reserved	Open or High	AUO internal test
29	GND	Ground and Signal Return	
30	GND	Ground and Signal Return	

LVDS Option = High è JEIDA



LVDS Option = Low/OPEN è NS



Backlight Specification

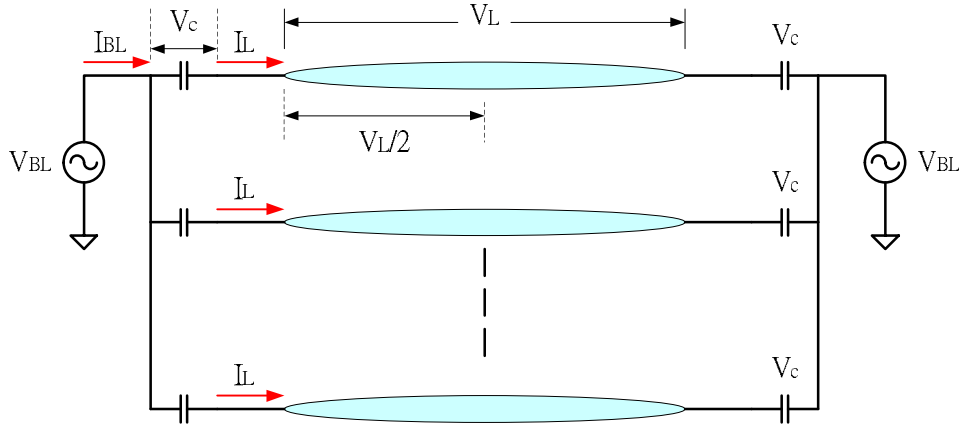
1. Electrical specification

	Description		Min	Typ	Max	Unit	Condition
1	BL Lamp Voltage	VBL	1050	1300	1450	Vrms	1). BL one side operating voltage at boost dimming ration 100% 2). Measurement data depend on Delta IPB. 3). Calculation method: (notes 1)
2	Lamp current	IL(long)	5	5.3	6.1	Irms	Measurement data depend on Delta IPB.
		IL(short)	5	5.7	6.1		
3	BL Lamp Current	IBL	105	118	125	mArms	1. BL one side operating current at boost dimming ration 100%
4	BL total Power	PO	157	165	173	W	1. Dimming at 100%
5	Starting Voltage	Vs	2140	2340	-	Vrms	BL one side striking voltage
6	Lamp frequency	fBL	60	62	64	KHz	Operation between 58KHz and 66KHz might cause waterfall noise, but not influence Panel function
7	Striking time	St	1000	-	1400	msec	
8	Lamp type		Straight type				
9	Number of lamps		18			pcs	
10	Type of current balance		C				
11	C ballaster	Cb	14.25	15	15.75	pF	
12	PWM Dimming Ratio	Dim	20	-	100	%	At 1.3 of uniformity
13	Boost Dimming Ratio (recommend)	A dim	80	100	120	%	At 1.3 of uniformity, PWM=100%
14	Boost Dimming Frequency (recommend)	f	140	180	240	Hz	

（Ta=25±5℃, Turn on for 45minutes）

Notes 1:

$$V_{BL} = \sqrt{\left(\frac{V_L}{2}\right)^2 + \left(V_C\right)^2}$$



2. Lamp specification (Recommendation)

	Description		Min	Typ	Max	Unit
1	Lamp Voltage	Vlamp	1270	1410	1550	Vrms
2	Lamp Current	Ilamp	3	6.5	7.5	mArms
3	Lamp frequency	fL	40	-	80	KHz
4	Starting Voltage	At 25C	-	1720	2060	Vrms
		At 0C	-	2060	2480	Vrms
6	Striking time	St	-	-	1000	msec
7	Life time		50K			

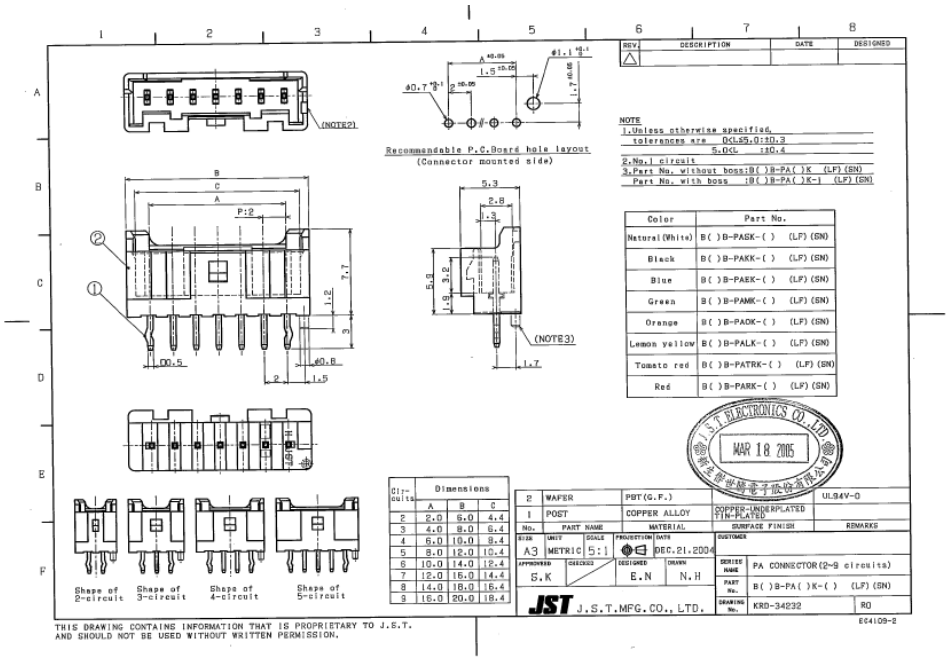
3. Pin assignment, connector drawing and connection configuration

CN1: B03B PASK-1 (JST) or equivalent

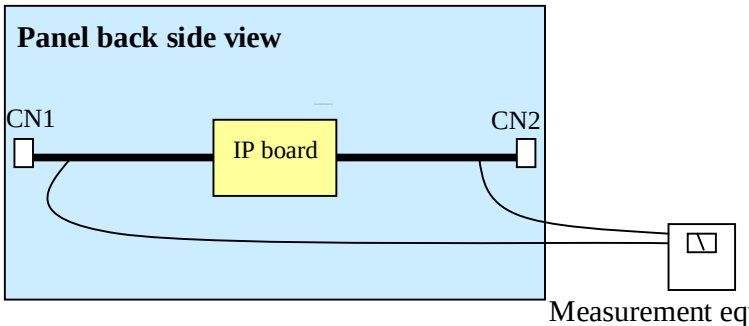
PIN #	Symbol	Description
1	High	I/P board high voltage supply
2	N.C.	No connection
3	High	I/P board high voltage supply

CN2: B03B PASK-1 (JST) or equivalent

PIN #	Symbol	Description
1	High	I/P board high voltage supply
2	N.C.	No connection
3	High	I/P board high voltage supply



4. Measurement method



Measurement equipment

Model: Tektronix P6022

3-3 Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Timing Table (DE only Mode)

Vertical Frequency Range

Signal	Item	Symbol	Min	Type	Max	Unit
Vertical Section	Period	Tv	789	806	1015	Th
	Active	Tdisp (v)	—	768	—	Th
	Blanking	Tblk (v)	16	38	247	Th
Horizontal Section	Period	Th	1440	1560	1900	Tclk
	Active	Tdisp (h)	—	1366	—	Tclk
	Blanking	Tblk (h)	48	194	534	Tclk
LVDS Clock	Frequency	Fclk(1/Tclk)	60	80	85	MHz
Vertical Frequency	Frequency	Vs	47	60	63	Hz
Horizontal Frequency	Frequency	Hs	39	48	53	KHz

1.) Display position is specific by the rise of DE signal only.

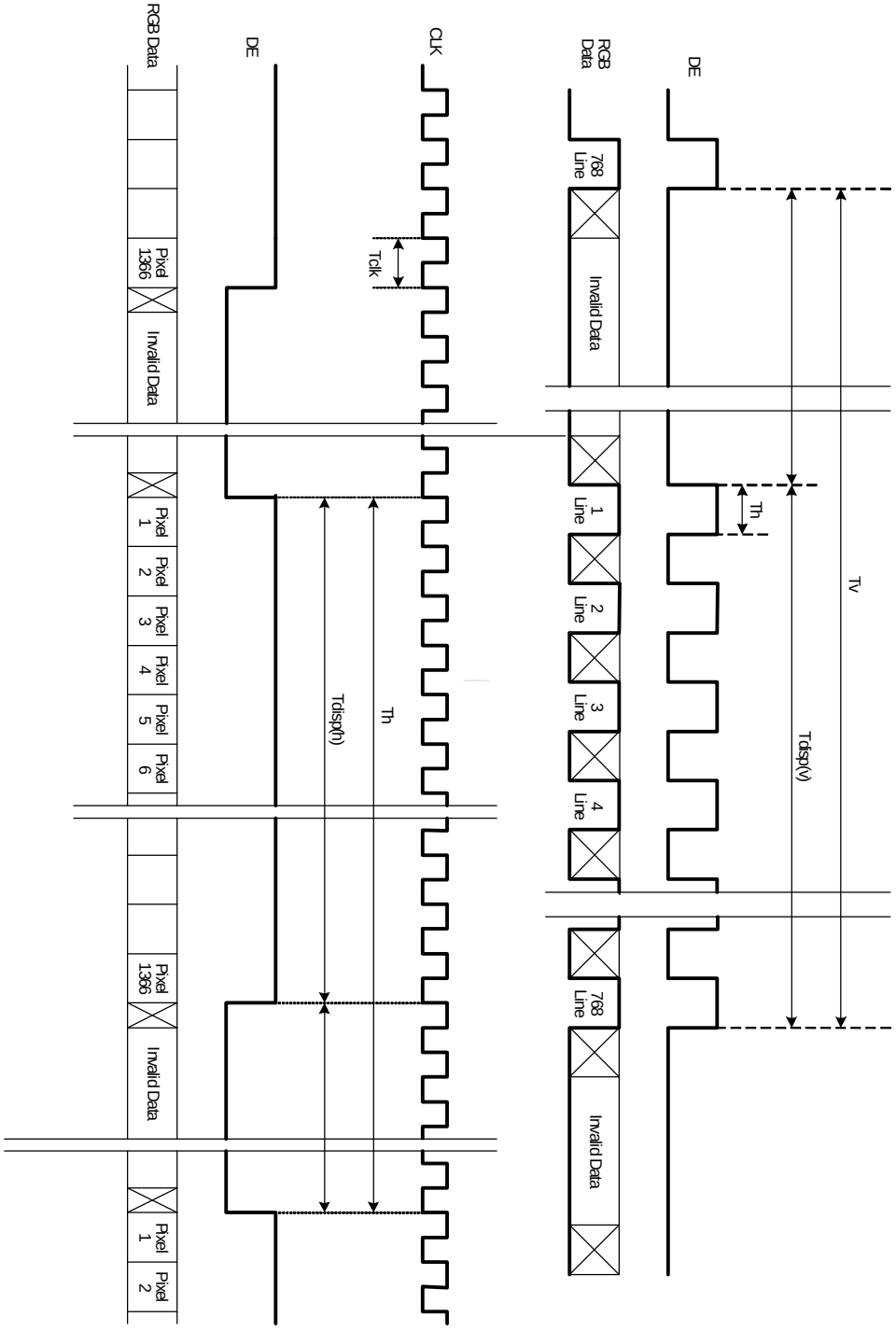
Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.

Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise the of 1st DE is displayed at the top line of screen.

2.) If a period of DE "High" is less than 1366 DCLK or less than 768 lines, the rest of the screen displays black.

3.) The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.

3-4 Signal Timing Waveforms



3-5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB							LSB	MSB							LSB	MSB							LSB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

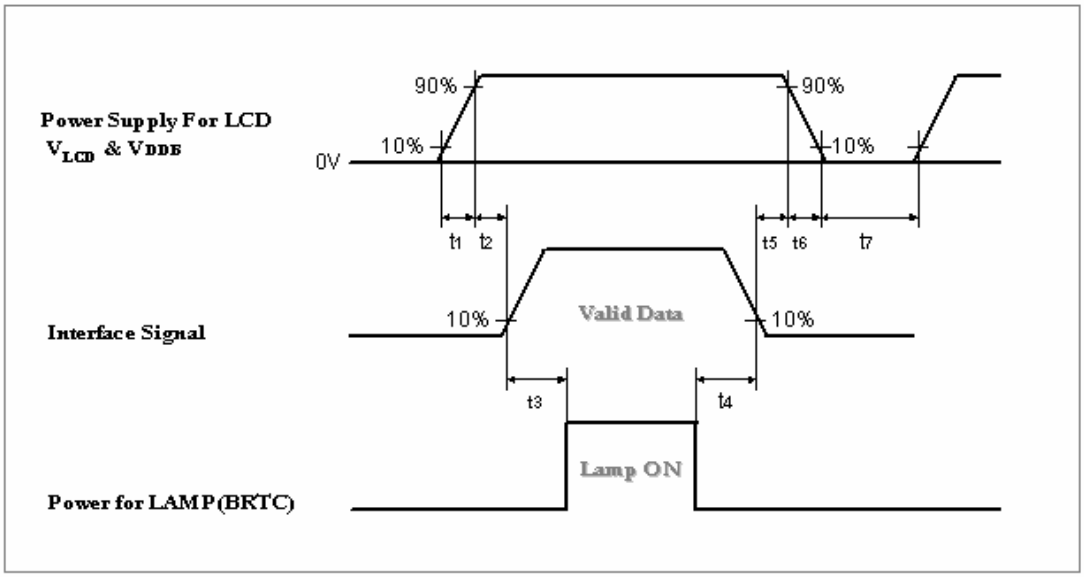
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0

	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1

	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-6 Power Sequence

1. Power sequence of panel



Parameter	Values			Units
	Min.	Typ.	Max.	
t1	0.47	-	20	ms
t2	0.5	-	50	ms
t3	200	-	-	ms
t4	10	-	-	ms
t5	0.5	-	50	ms
t6	-	-	300	ms
t7	1	-	-	s

Apply the lamp voltage within the LCD operating range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal.

Caution : The above on/off sequence should be applied to avoid abnormal function in the display. In case of handling, make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25℃. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

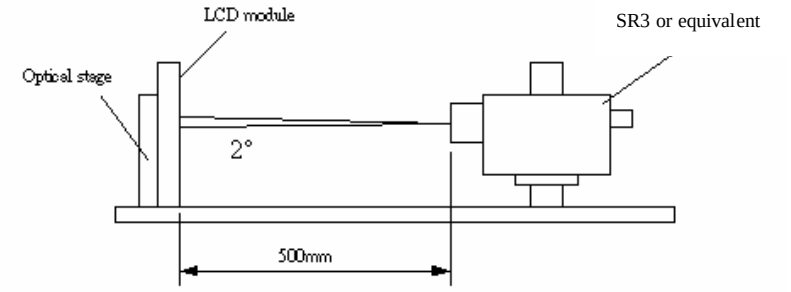


Fig.4-1 Optical measurement equipment and method

Parameter		Symbol	Values			Units	Notes
			Min.	Typ.	Max.		
Contrast Ratio		CR	1200	1500			1
Surface Luminance, white		LWH	400	500		cd/㎡	2
Luminance Variation		δ_{WHITE} 5 p			1.3		3
		δ_{BLACK} 5 p			1.5		3
Response Time (Ton / Toff)		Ton / Toff		(16/7)	(17/8)		
Response Time (Average)		Gray to Gray		6.5	8	ms	4,5 (Gray to Gray)
Color Coordinates							
	RED	R_X	Typ.-0.03	0.640	Typ.+0.03		
		R_Y		0.330			
	GREEN	G_X		0.290			
		G_Y		0.600			
	BLUE	B_X		0.150			
		B_Y		0.060			
	WHITE	W_X		0.280			
		W_Y		0.290			
Viewing Angle							Contrast Ratio>10
	x axis, right(φ=0°)	θ_r		89		Degree	6
	x axis, left(φ=180°)	θ_l		89			
	y axis, up(φ=90°)	θ_u		89			
	y axis, down (φ=0°)	θ_d		89			

Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "white" state}}{\text{Brightness on the "black" state}}$$

2. Surface luminance is luminance value at point 1 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see Fig. 4-2. When V_{BL} = (1765V), I_{BL} = (99mA), lamp frequency(typ) = 62KHz. $L_{WH}=L_{on1}$, Where L_{on1} is the luminance with all pixels displaying white at center 1 location.

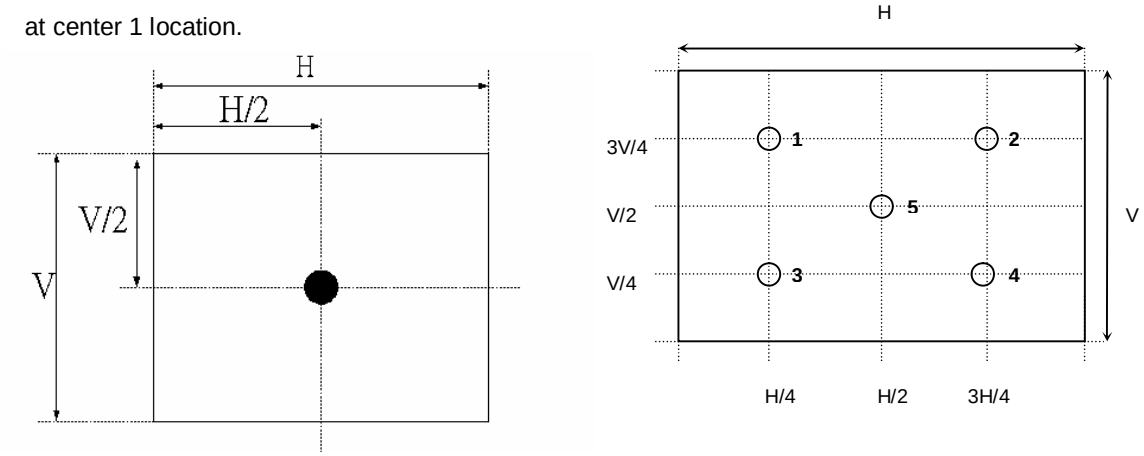


Fig.4-2 Optical measurement point

3. The variation in surface luminance, δ_{WHITE} is defined under 100% brightness as, δ_{BLACK} is defined under 0% brightness:

$$\delta_{WHITE(5P)} = \text{Maximum}(L_{on1}, L_{on2},\ldots,L_{on5})/\text{Minimum}(L_{on1}, L_{on2},\ldots L_{on5})$$

$$\delta_{BLACK(5P)} = \text{Maximum}(L_{on1}, L_{on2},\ldots,L_{on5})/\text{Minimum}(L_{on1}, L_{on2},\ldots L_{on5})$$

4. Response Time:

(a) Tr = full black to full white, 10%~90%

(b) Tf = full white to full black, 90%~10%

(c) G-to-G: average response time among brightness of 0%, 25%, 50%, 75% &100%.

	0%	25%	50%	75%	100%
0%		tr: 0% à 25%	tr: 0% à 50%	tr: 0% à 75%	tr: 0% à 100%
25%	tf: 25% à 0%		tr: 25% à 50%	tr: 25% à 75%	tr: 25% à 100%
50%	tf: 50% à 0%	tf: 50% à 25%		tr: 50% à 75%	tr: 50% à 100%
75%	tf: 75% à 0%	tf: 75% à 25%	tf: 75% à 50%		tr: 75% à 100%
100%	tf: 100% à 0%	tf: 100% à 25%	tf: 100% à 50%	tf: 100% à 75%	

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Fig. 4-3.

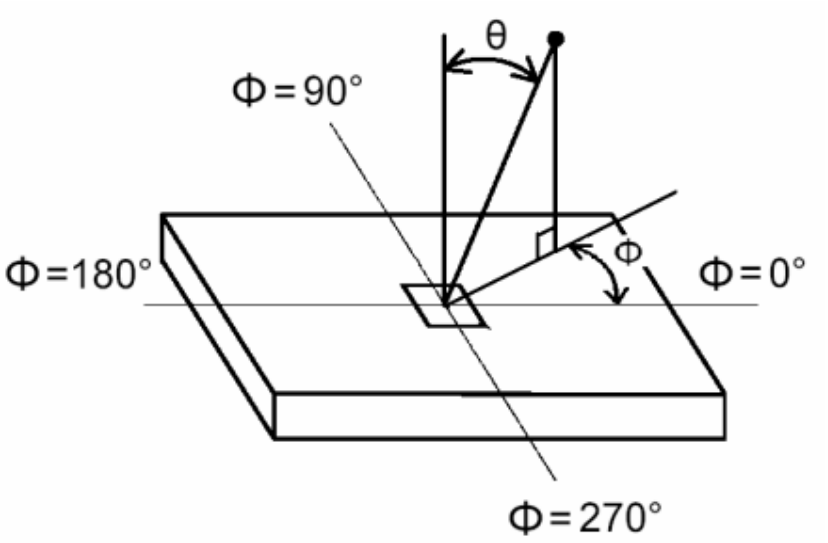


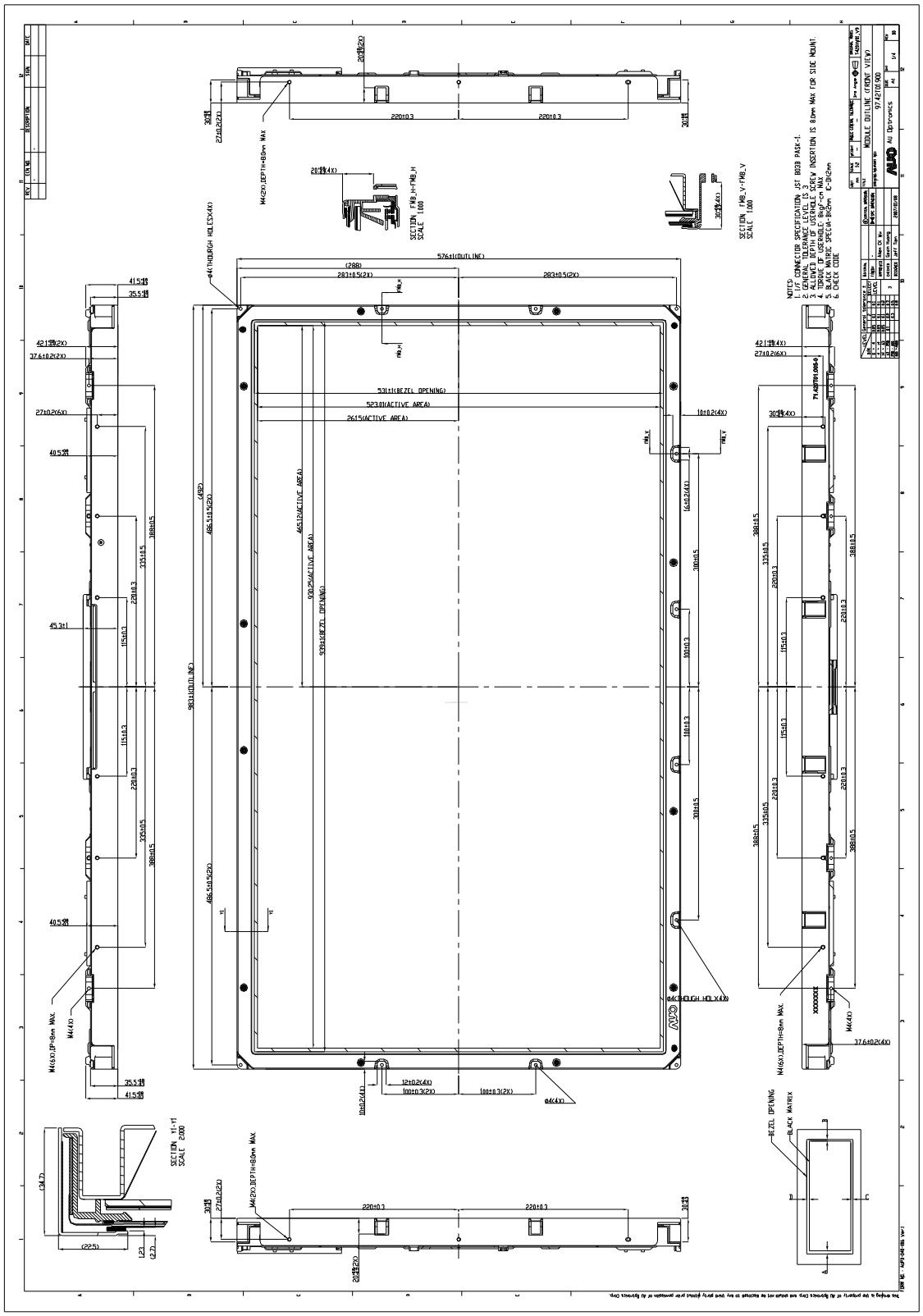
Fig.4-3Viewing Angle Definition

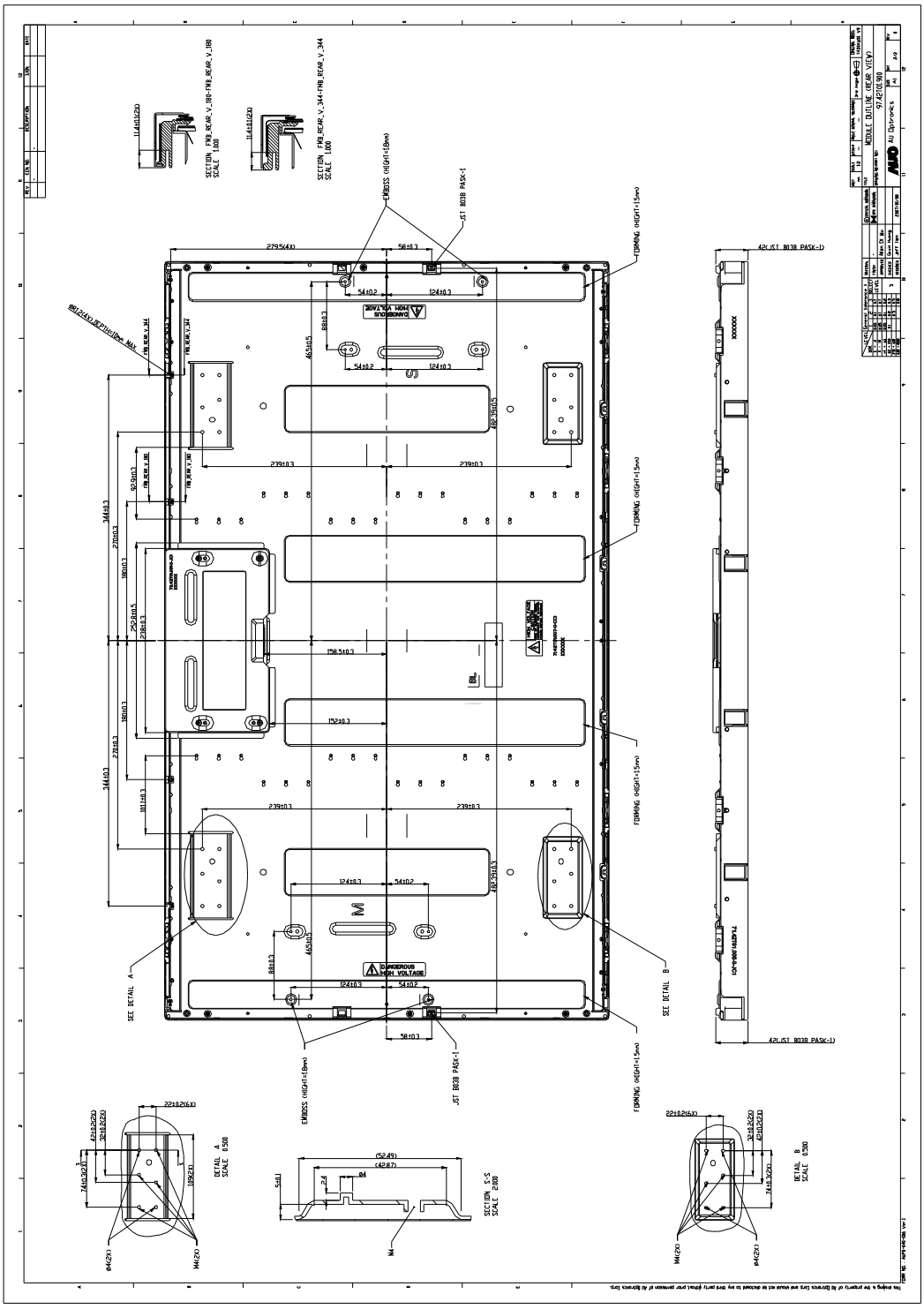
5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T420XW01. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal (typ.)	983.0 mm
	Vertical (typ.)	576.0 mm
	Depth (typ.)	45.3 mm (T-con cover shield)
Bezel Area	Horizontal (typ.)	939.0 mm
	Vertical (typ.)	531.3 mm
Active Display Area	Horizontal	930.25mm
	Vertical	523.01mm
Weight	12400g (typ)	
Surface Treatment	AG, 3H	

2D drawing





6. Reliability

Environment test condition

No	Test Item	Condition
1	**High temperature storage test**	Ta=60℃, 300hr judge
2	**Low temperature storage test**	Ta=-20℃, 300hr judge
3	**High temperature/High humidity test**	Ta=50℃, 80%RH, 300hr judge
4	**High temperature operation test**	Ta=50℃, 300hr judge
5	**Low temperature operation test**	Ta=0℃, 300hr judge
6	**Vibration test (non-operating)**	Wave form: random Vibration level : 1.5G RMS Bandwidth : 10-500Hz Duration: X, Y, Z 10min one time each direction
7	**Shock test (non-operating)**	Shock level: 50G Waveform: half sine wave, 11ms Direction: ±X, ±Y, ±Z One time each direction Time cycle no.: once for each time
8	**Vibration test (with carton)**	Random wave (1.5Grms 10~200Hz) 30mins / Per each X.Y.Z axes
9	**Drop test (with carton)**	Height: 31 cm 1 corner, 3 edges, 6 surfaces (ASTMD4169-I)

7. International Standard

7-1. Safety

(1) UL6500, UL 60065 Underwriters Laboratories, Inc. (AUO file number : E204356)
Standard for Safety of Information Technology Equipment Including electrical Business Equipment.

(2) CAN/CSA C22.2 No. 950-95 Third Edition, Canadian Standards Association, Jan. 28, 1995
Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

(3) EN60950: 1992+A2: 1993+A2: 1993+C3: 1995+A4: 1997+A11: 1997
IEC 950: 1991+A1: 1992+A2: 1993+C3: 1995+A4:1996
IEC 60065: version 7th
European Committee for Electro technical Standardization (CENELEC)
EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

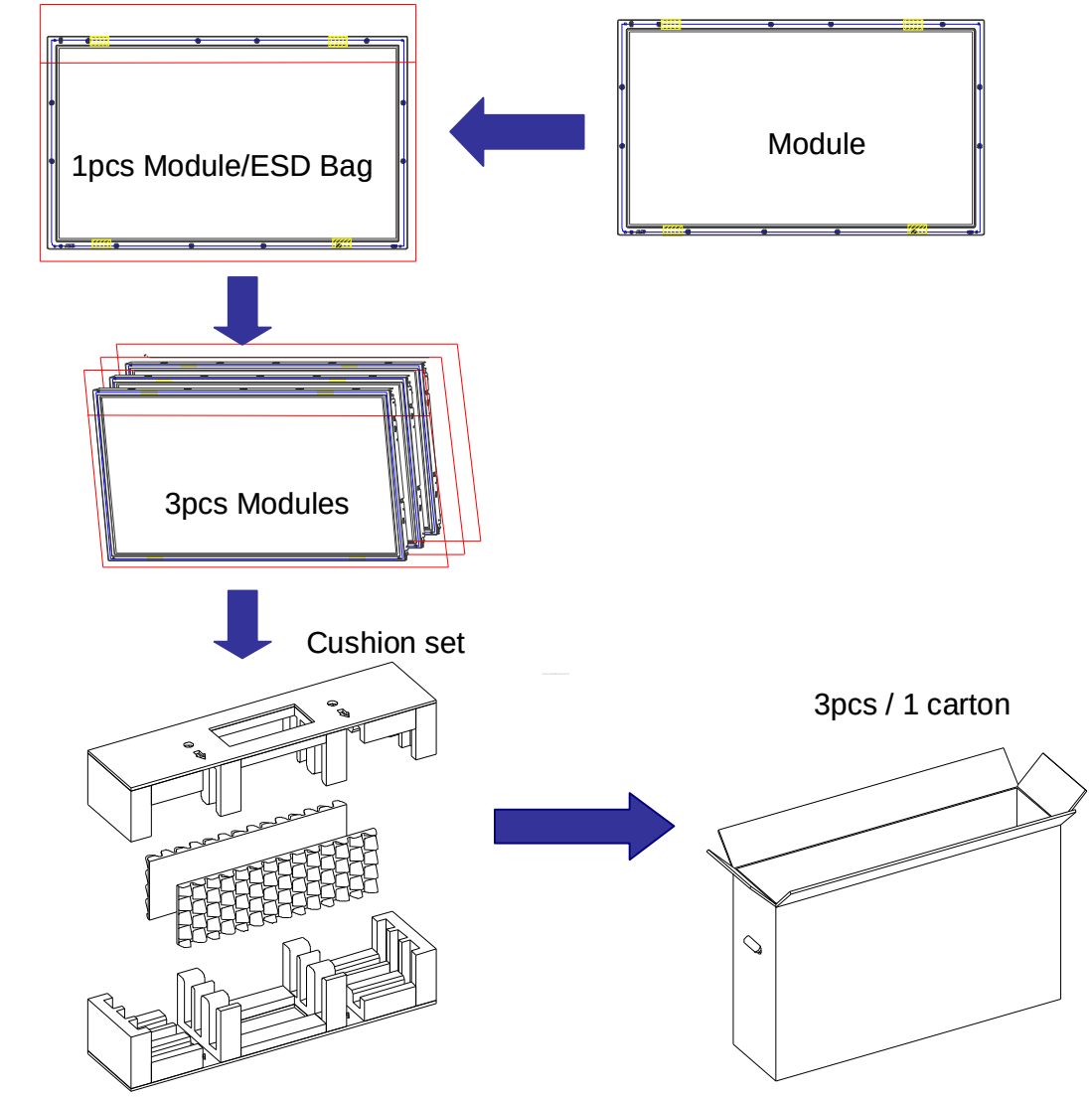
a) ANSI C63.4 “Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. “American National standards Institute(ANSI), 1992

b) C.I.S.P.R “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.” International Special committee on Radio Interference.

c) EN 55022 “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.” European Committee for Electrotechnical Standardization. (CENELEC), 1998

8. Packing

Packing Instruction



Package information:

Carton outside dimension : 1087x285x716mm

Carton/Package weight : 3kg

Gross weight(per Box) : 48kg

Shipping label

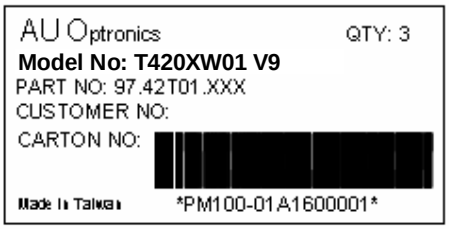
Green Mark Description:

For Pb Free products, AUO will add for identification.

For RoHS compatible products, AUO will add for identification.

Note. The Green Mark will be present only when the green documents have been ready by AUO Internal Green Team. (The definition of green design follows the AUO green design checklist.)

Carton label



Pallet information

By air cargo : : (4x1) x2 layers, one pallet put 8 boxes, total 24 pcs module.

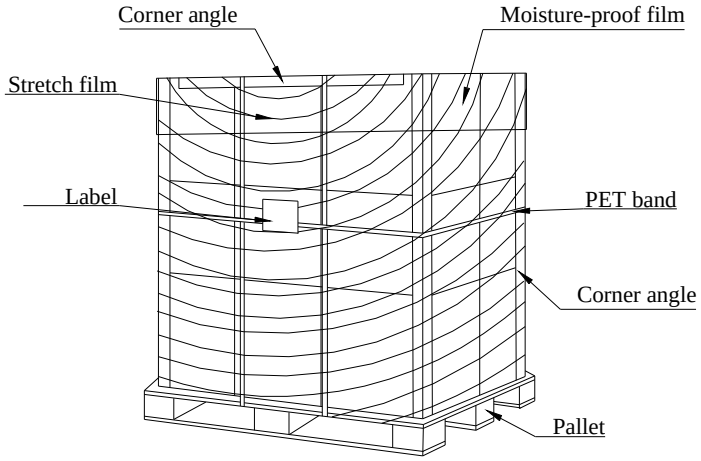
By sea : (4x1) x3 layers, one pallet put 12 boxes, total 24 pcs module.

Pallet dimension : 1150x1100x120mm

Pallet weight : 10kg

By air total weight : 48 kg/box X 8 boxes=384 kg (with pallet weight 394kg)

By sea total weight : 48 kg/box X 12 boxes=576 kg (with pallet weight 586kg)



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged on back side of panel

(2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.

(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.

(4) You should adopt radiation structure to satisfy the temperature specification.

(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.

(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)

(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.

(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.

(9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)

(2) Response time depends on the temperature. (In lower temperature, it becomes longer..)

(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.

(4) Be careful for condensation at sudden temperature change. Condensation makes damage to

polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.

(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.

(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.

(2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

(1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.

(2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of flue still on the Bezel after the protection film is peeled off.

(3) You can remove the glue easily. When the glue remains on the Bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

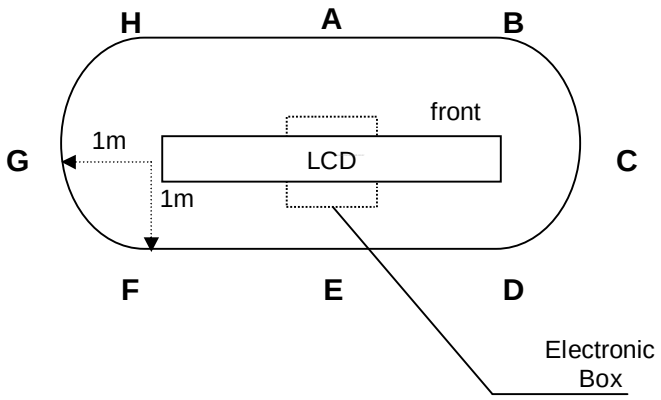
Appendix :

1. Acoustical Noise Requirement

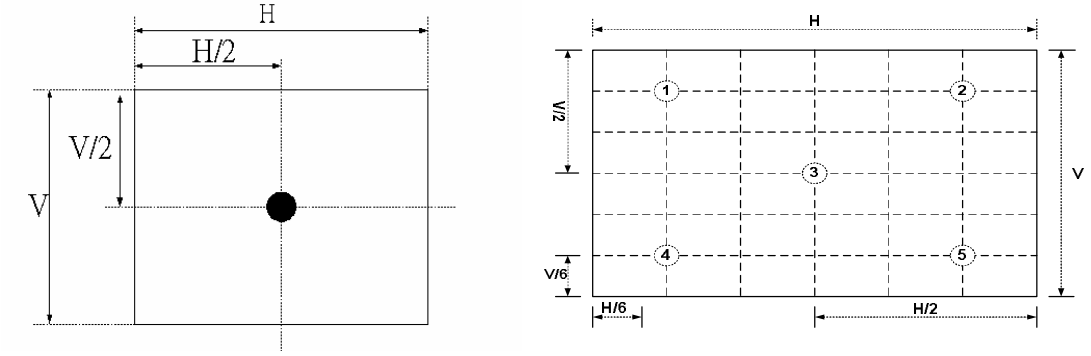
Measurement of all residual noises (e.g. back light, inverter, fans …) will be done in a silent reverberant room. If available, the electronic box is placed under the LCD. Measure the sound level frequency dependant on 8 points around the LCD. The position in height of the sound audiometer is the middle of the LCD. Measures this on frequency span 25 Hz -20 kHz (gives an overview of the total spectrum) and measure this on frequency span 25 Hz - 1500 Hz.

Performance parameter	Class	LCD size	Requirement
General audible noise. Sound level.	ALL	ALL	< 20 (dBA)

$$\text{Sound level} = \frac{\Sigma \text{ sound level from A to H}}{8} \text{ (dBA)}$$



2. Luminance variation at 30% dimming(δ,5P): 1.5(Max.)



3. Impedance of Pin9 of LVDS : 4.7K (Ω)