



Model Name: T460HW04 V5

Issue Date: 2010/06/10

()Preliminary Specifications

(*) Final Specifications

Customer Signature	Date	AUO	Date
Approved By		Approval By PM Director YenTing Chiu YenTing Chiu	2019/612.
Note		Reviewed By RD Director Eugene CC Chen Gran Chen Evan Chen Evan Chen Prepared By PM Maggie Tai Maggil Tai 2010	, >





Contents

No		
		CONTENTS
		RECORD OF REVISIONS
1		GENERAL DESCRIPTION
2		ABSOLUTE MAXIMUM RATINGS
3		ELECTRICAL SPECIFICATION
	3-1	ELECTRIACL CHARACTERISTICS
	3-2	INTERFACE CONNECTIONS
	3-3	SIGNAL TIMING SPECIFICATION
	3-4	SIGNAL TIMING WAVEFORM
	3-5	COLOR INPUT DATA REFERENCE
	3-6	POWER SEQUENCE
	3-7	BACKLIGHT SPECIFICATION
4		OPTICAL SPECIFICATION
5		MECHANICAL CHARACTERISTICS
6		RELIABILITY TEST ITEMS
7		INTERNATIONAL STANDARD
	7-1	SAFETY
	7-2	EMC
8		PACKING
	8-1	DEFINITION OF LABEL
	8-2	PACKING METHODS
	8-3	PALLET AND SHIPMENT INFORMATION
9		PRECAUTION
	9-1	MOUNTING PRECAUTIONS
	9-2	OPERATING PRECAUTIONS
	9-3	ELECTROSTATIC DISCHARGE CONTROL
	9-4	PRECAUTIONS FOR STRONG LIGHT EXPOSURE
	9-5	STORAGE
	9-6	HANDLING PRECAUTIONS FOR PROTECT FILM





Record of Revision

Version	Date	Page	Description
0.0	2010/06/08		First release
1.0	2010/06/10	7,	Update 3.1 Electrical Characteristics part note item 6~7.
1.0	2010/06/10	29, 30	Add Appendix part.
		\	
	l	I	





1. General Description

This specification applies to the 46.0 inch Color TFT-LCD Module T460HW04 V5. This LCD module has a TFT active matrix type liquid crystal panel 1,920x1,080 pixels, and diagonal size of 46.0 inch. This module supports 1,920x1080 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 10-bit gray scale signal for each dot.

The T460HW04 V5 has been designed to apply the 10-bit 2 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

* General Information

Items	Specification	Unit	Note
Active Screen Size	46.00	inch	
Display Area	1018.08(H) x 572.67(V)	mm	
Outline Dimension	1060.9(H) X 620.3(V) x 24.1(D)	mm	D : Front bezel to T-CON cover
Driver Element	a-Si TFT active matrix		
Display Colors	10 bit(8+FRC), 1073.7M	Colors	
Number of Pixels	1,920x1080	Pixel	
Pixel Pitch	0.53025	mm	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment	Anti-Glare, 3H		Haze=11%





Global LCD Panel Exchange Center

T460HW04 V5 Product Specification

2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	Vcc	-0.3	14	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	4	[Volt]	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

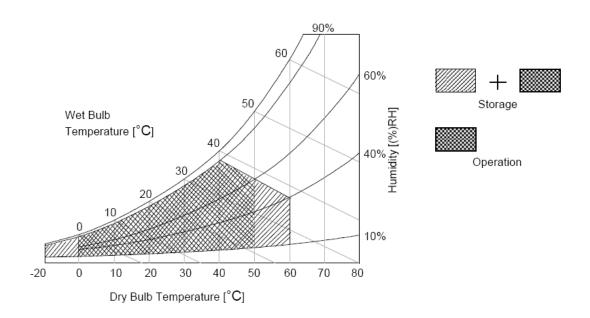
Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be 39 $^{\circ}$ C and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40°C or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C.

Note 3: Surface temperature is measured at 50°C Dry condition

Note 4: Storage period should refer to RA criteria





Global LCD Panel Exchange Center



T460HW04 V5 Product Specification

3. Electrical Specification

The T460HW04 V5 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second is employed for LED light-bar.

3.1 Electrical Characteristics

	Symbol		Value		Unit	Note	
	Parameter	Symbol	Min.	Тур.	Max	Uniit	Note
LCD							
Power Supp	oly Input Voltage	V _{DD}	10.8	12	13.2	V _{DC}	1
Power Supp	oly Input Current	I _{DD}	-	0.876	1.542	Α	2
Power Cons	sumption	Pc	-	10.512	18.504	Watt	2
Inrush Curr	ent	I _{RUSH}	-	ī	4	Α	3
	Differential Input High Threshold Voltage	V _{TH}			+100	mV _{DC}	4
LVDS Interface	Differential Input Low Threshold Voltage	V _{TL}	-100)		mV _{DC}	4
	Input Common Mode Voltage	V _{ICM}	1.1	1.25	1.4	V_{DC}	4
LVDS Interface	Input Channel Pair Skew Margin	t _{SKEW (CP)}	-500		+500	ps	5
CMOS	Input High Threshold Voltage	V _{IH} (High)	2.7		3.3	V_{DC}	
Interface	Input Low Threshold Voltage	V _{IL} (Low)	0		0.6	V _{DC}	
Backlight P	ower Consumption	P _{BL}	62	68	74	Watt	
Life Time (N	MTTF)		30000			Hours	6,7

Note:

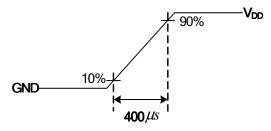
- The ripple voltage should be controlled under 10% of V_{CC}
- **Test Condition:**
 - (1) $V_{DD} = 12.0V$
 - (2) Fv = Type Timing, 60Hz, 120Hz or Other
 - (3) $F_{CLK} = Max freq.$
 - (4) Temperature = 25 °C
 - (5) Test Pattern: White Pattern

© Copyright AUO Optronics Corp. 2009 All Rights Reserved.

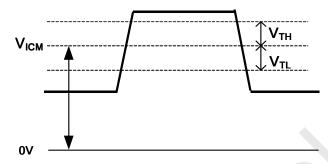




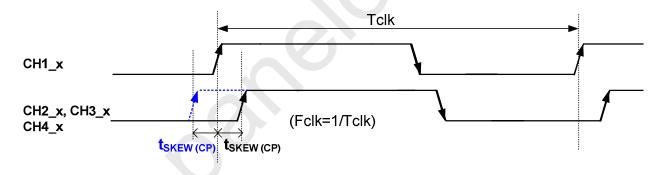
3. Measurement condition : Rising time = 400us



4. $V_{ICM} = 1.25V$



5. Input Channel Pair Skew Margin



- **6.** The relative humidity must not exceed 80% non-condensing at temperatures of 40° C or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C. When operate at low temperatures, the brightness of LED will drop and the life time of LED will be reduced.
- 7. The lifetime(MTTF) is defined as the time which luminance of the LED is 50% compared to its original value. [Operating condition: Continuous operating at $Ta = 25\pm2^{\circ}C$]





3.2 Interface Connections

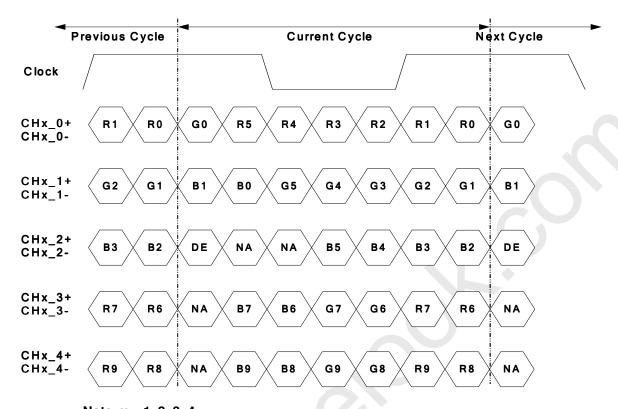
● LCD connector : JAE FI-RE51S-HF

PIN	Symbol	Description	PIN	Symbol	Description
1	V_{DD}	Power Supply, +12V DC Regulated	26	CH2_0+	LVDS Channel 2, Signal 0+
2	V_{DD}	Power Supply, +12V DC Regulated	27	CH2_1-	LVDS Channel 2, Signal 1-
3	V_{DD}	Power Supply, +12V DC Regulated	28	CH2_1+	LVDS Channel 2, Signal 1+
4	V_{DD}	Power Supply, +12V DC Regulated	29	CH2_2-	LVDS Channel 2, Signal 2-
5	V_{DD}	Power Supply, +12V DC Regulated	30	CH2_2+	LVDS Channel 2, Signal 2+
6	Reserved	AUO Internal Use Only	31	GND	Ground
7	GND	Ground	32	CH2_CLK-	LVDS Channel 2, Clock -
8	GND	Ground	33	CH2_CLK+	LVDS Channel 2, Clock +
9	GND	Ground	34	GND	Ground
10	CH1_0-	LVDS Channel 1, Signal 0-	35	CH2_3-	LVDS Channel 2, Signal 3-
11	CH1_0+	LVDS Channel 1, Signal 0+	36	CH2_3+	LVDS Channel 2, Signal 3+
12	CH1_1-	LVDS Channel 1, Signal 1-	37	CH2_4-	LVDS Channel 2, Signal 4-
13	CH1_1+	LVDS Channel 1, Signal 1+	38	CH2_4+	LVDS Channel 2, Signal 4+
14	CH1_2-	LVDS Channel 1, Signal 2-	39	GND	Ground
15	CH1_2+	LVDS Channel 1, Signal 2+	40	SCL	EEPROM Serial Clock
16	GND	Ground	41	SDA	EEPROM Serial Data
17	CH1_CLK-	LVDS Channel 1, Clock -	42	SONY	(SONY internal use only)
				Reserved	
18	CH1_CLK+	LVDS Channel 1, Clock +	43	BUS_SW	BUS_SW (SONY internal use only)
19	GND	Ground	44	Panel_SEL	reserve 0603 footprint of resistor to ground
					(SONY internal use only)
20	CH1_3-	LVDS Channel 1, Signal 3-	45	SONY	(SONY internal use only)
				Reserved	
21	CH1_3+	LVDS Channel 1, Signal 3+	46	SA_MODE	SA_MODE (SONY internal use only)
22	CH1_4-	LVDS Channel 1, Signal 4-	47	SONY	(SONY internal use only)
				Reserved	
23	CH1_4+	LVDS Channel 1, Signal 4+	48	FRC_RST	FRC_RST (SONY internal use only)
24	GND	Ground	49	SONY	(SONY internal use only)
				Reserved	
25	CH2_0-	LVDS Channel 2, Signal 0-	50	TCON_RDY	Send TCON Ready to system(SONY internal
					use only)
			51	NC	NC



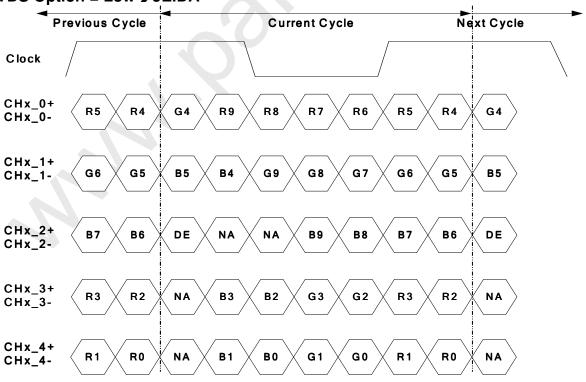


LVDS Option = High/Open→NS



Note: x = 1, 2, 3, 4...

LVDS Option = Low→JEIDA



Note: x = 1, 2, 3, 4...





3.3 Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Signal	Item	Symbol	Min.	Тур.	Max	Unit
	Period	Tv	-	1134	-	Th
	Active	Tdisp (v)		1080		Th
	Blanking	Tblk (v)	-	54		Th
Vertical Section	Front porch	Tfp (v)	-	34	-	Th
	Back porch	Tbp (v)	-	16		Th
	V_sync	TVsync_wdth	-	4		Th
	Polarity	POL (v)				
	Period	Th	-	1092	-	Tclk
	Active	Tdisp (h)		960		Tclk
	Blanking	Tblk (h)		132	-	Tclk
Horizontal Section	Front porch	Tfp (h)	-	16	-	Tclk
	Back porch	Tbp (h)	-	100	-	Tclk
	H_sync	THsync_wdth	-	16	-	Tclk
	Polarity	POL (h)		+		
Clock	Frequency	Fclk=1/Tclk	-	74.23	-	MHz
Vertical Frequency	Frequency	Fv	-	59.94	-	Hz
Horizontal Frequency	Frequency	Fh	-	67.97	-	KHz

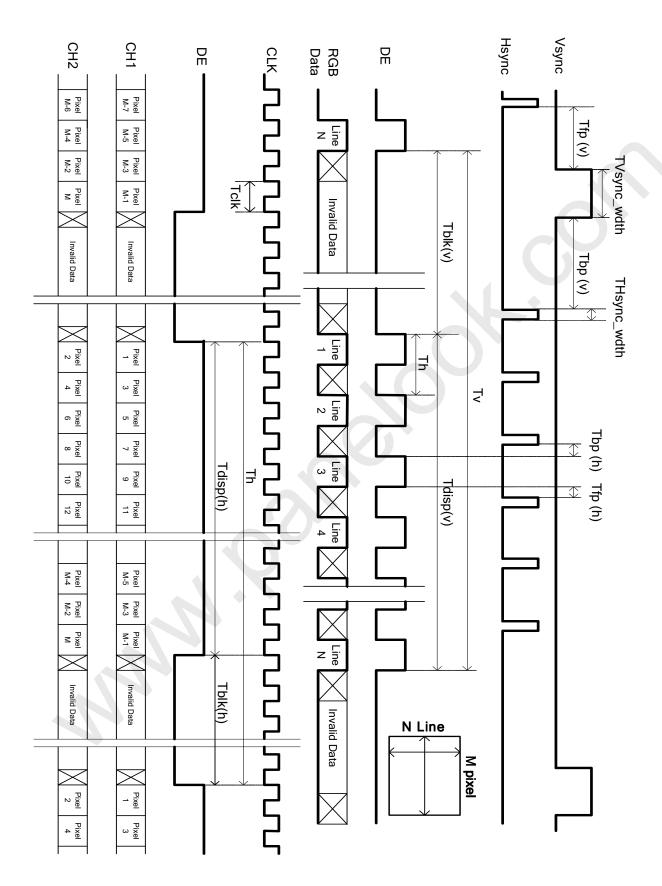
Notes:

- (1) Display position is specific by the rise of DE signal only.
 Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.
- (2) Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen.
- (3)If a period of DE "High" is less than 1920 DCLK or less than 1080 lines, the rest of the screen displays black.
- (4)The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.





3.4 Signal Timing Waveforms







3.5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 10 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

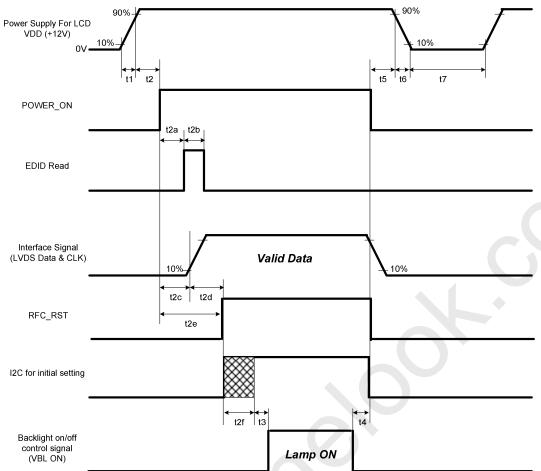
									الر	_01	٦ 	DΡ	NΙΑ		(E)	-	(LI	ICE													
														lr	put	Co	lor [Data	l												
	Color		RED						GREEN								BLUE														
	00101	MS	BB				LSE					MSB LSB						MSB LSB													
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	В9	B8	В7	B6	B5	В4	ВЗ	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Blue(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R																															
	RED(1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
G																															
	GREEN(1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
В																															
	BLUE(1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1



Global LCD Panel Exchange Center

T460HW04 V5 Product Specification

3.6 Power Sequence for LCD



Parameter		Standard*1		Standalone*1	Lloit
Parameter	Min.	Type.	Max.	Type.	Unit
t1	0.4		30		ms
t2	10			57	ms
t2a	10		100		ms
t2b	0*4		100*4		ms
t2c	10				ms
t2d	10				ms
t2e				102	ms
t2f			250	204	ms
t3	500				ms
t4	0*2				ms
t5	0				ms
t6			*3		ms
t7	500				ms

Note:

- (1) Standard mode is used for customer's operation. Standalone mode is used for panel factory operation.
- (2) t4=0: concern for residual pattern before BLU turn off.
- (3) t6 : voltage of VDD must decay smoothly after power-off. (customer system decide this value)
- (4) t2b: customer decides this value

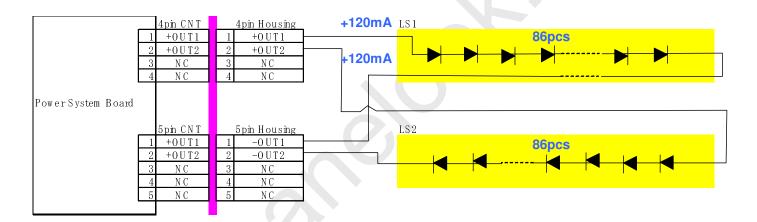


3.7 Backlight Specification

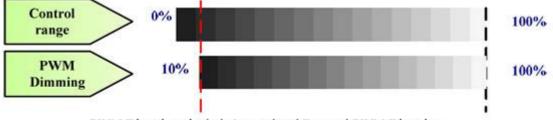
The backlight unit contains 2pcs light bar

3.7.1 Lightbar Driven Condition

Dove	nmeter	Cymphol		Value		l lmit	Note		
Para	Symbol	Min	Тур	Max	Unit	Note			
Forward Current	Anode	I _F (Anode)	-	120	-	mA			
(one light bar) Cathode		I _F (Cathode)	-	120	1	mA			
Forward Voltage (one light bar)	V_{F}	ı	283.8	300	V			
Total Power Cons	umption(2 light bar)	P_BL	62	68	74	W			
PWM Operation F	F_PWM	140	180	240	Hz	1 (reference only)			
PWM Dimming Du	ıty Ratio	D_PWM	10	-	100	%	2 (reference only)		



Note 1: Dimming range



PWM Dimming: include Internal and External PWM Dimming

Note 2: Low dimming ratio operation

When PWM dimming duty ratio is operated lower than recommended value, feedback signal and all protection functions should be confirmed by LIPS design. Display performance should also be confirmed by customer's implement.





3.7.2 Input Pin Assignment

2 light bar (1string/light bar)

4 Pin CN: Molex 51003-0400

	CN2
	4pin
1	+ OUT 1
2	+ OUT 2
3	NC
4	NC

5 Pin CN: Molex 51003-0500

	CN3
	5pin
1	- OUT 1
2	- OUT 2
3	NC
4	NC
5	NC

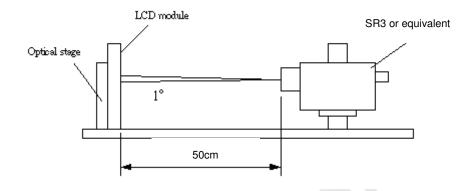




4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of ϕ and θ equal to 0 °.

Fig.1 presents additional information concerning the measurement equipment and method.



Parameter	Symbol		Values		Unit	Notes
Parameter	Symbol Min. Typ.			Max	Uffil	notes
Contrast Ratio	CR	3200	4000			1
Surface Luminance (White)	L _{WH}	320	400		cd/m ²	2
Luminance Variation	δ _{WHITE(9P)}			1.3		3
Response Time (G to G)	Тү		5.5		Ms	4
Color Gamut	NTSC		72		%	
Color Coordinates						
Red	R_X		0.640			
	R _Y		0.330			
Green	G _X		0.300			
	G_Y	Typ0.03	0.620	Typ.+0.03		
Blue	B _X	Τγρυ.υ3	0.150	тур.+0.03		
	B _Y		0.050			
White	W _X		0.280			
	W_{Y}		0.290			
Viewing Angle						5
x axis, right(φ=0°)	θ_{r}		89		degree	
x axis, left(φ=180°)	θι		89		degree	
y axis, up(φ=90°)	θ_{u}		89		degree	
y axis, down (φ=270°)	$\theta_{\sf d}$		89		degree	





Note:

1. Contrast Ratio (CR) is defined mathematically as:

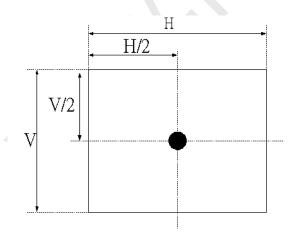
Contrast Ratio=
$$\frac{\text{Surface Luminance of L}_{\text{on5}}}{\text{Surface Luminance of L}_{\text{off5}}}$$

- 2. Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. L_{WH}=Lon5 where Lon5 is the luminance with all pixels displaying white at center 5 location.
- 3. The variation in surface luminance, δ WHITE is defined (center of Screen) as: $\delta_{WHITE(9P)} = Maximum(L_{on1}, L_{on2},...,L_{on9}) / Minimum(L_{on1}, L_{on2},...L_{on9})$
- 4. Response time T_{γ} is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on F_{ν} =60Hz to optimize.

Me	easured	Target								
Response Time		0%	25%	50%	75%	100%				
	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%				
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%				
Start	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%				
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%				
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%					

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG3.

FIG. 2 Luminance



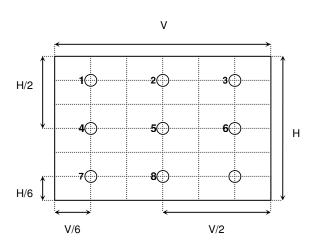
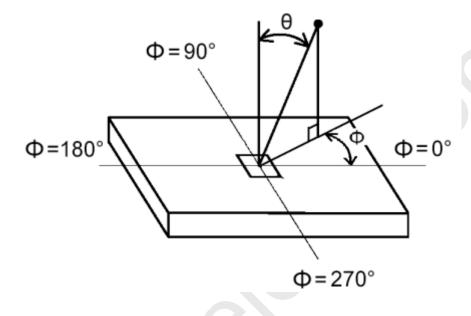






FIG.3 Viewing Angle







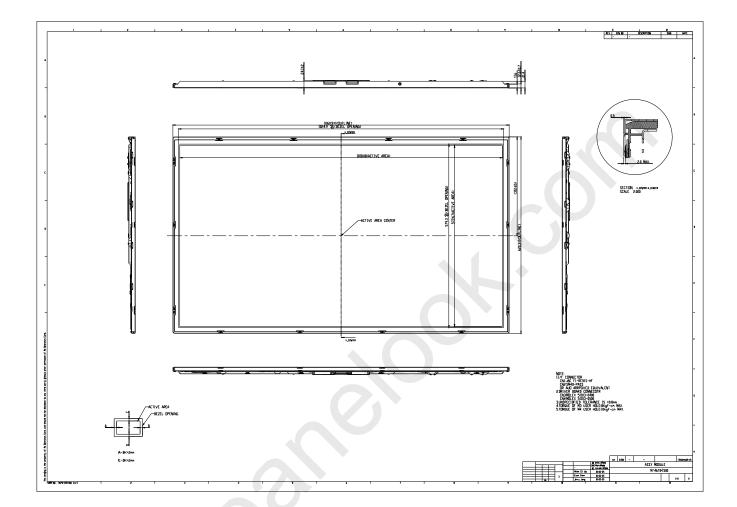
5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T460HW04 V5. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	1060.9 mm						
Outline Dimension	Vertical	620.3 mm						
	Depth	24.1 mm (Front bezel to T-CON cover)						
Borol Opening	Horizontal	1024.9 mm						
Bezel Opening	Vertical	579.3 mm						
Active Diepley Area	Horizontal	1018.08 mm						
Active Display Area	Vertical	572.67 mm						
Weight	9,800 (Typ.)							
Surface Treatment	Anti-Glare, 3H							



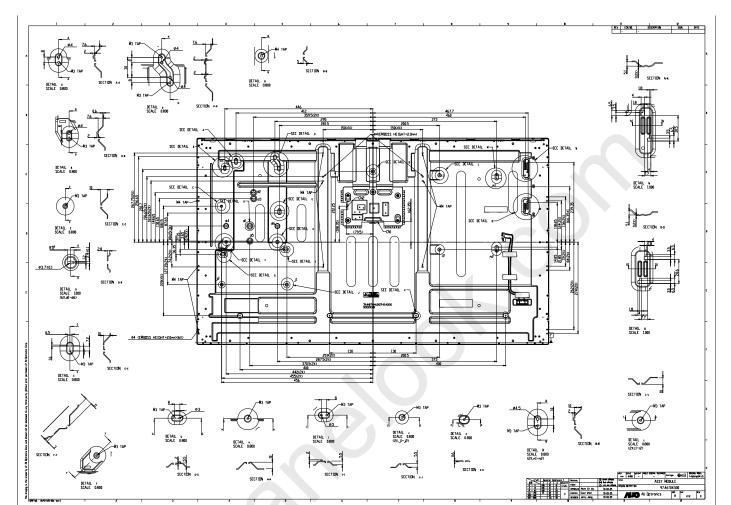
Front View







Back View







6. Reliability Test Items

	Test Item	Q'ty	Condition
1	High temperature storage test	3	60℃, 300hrs
2	Low temperature storage test	3	-20°ℂ, 300hrs
3	High temperature operation test	3	50℃, 300hrs
4	Low temperature operation test	3	-5°C, 300hrs
			Wave form: random
-	Mile and in a decad (an an anadrica)	3	Vibration level: 1.0G RMS
5	Vibration test (non-operation)	3	Bandwidth: 10-300Hz,
			Duration: X, Y, Z ,10mins each
			Shock level: 35G(±Z), 50G(±X, ±Y)
6	6 Shock test (non-operation) 3 Waveform: half since wave, 11ms		Waveform: half since wave, 11ms
			Direction: ±X, ±Y, ±Z, One time each direction
			Random wave (1.0G RMS, 10-200Hz)
7	Vibration test (With carton)	1 (PKG)	X,Y,Z axes, 10mins each
. <u> </u>			25.4 cm, Surround four flats and bottom flat twice
8	Drop test (With carton)	1 (PKG)	(Front→Rear→Left→Right→Bottom→Bottom 2 nd)
	Diop test (with carton)	i (i KG)	(ASTMD4169)





7. International Standard

7.1 Safety

- (1) UL 60950-1, UL 60065; Standard for Safety of Information Technology Equipment Including electrical Business Equipment.
- (2) IEC 60950-1: 2001, IEC 60065:2001; Standard for Safety of International Electrotechnical Commission
- (3) EN 60950 : 2001+A11, EN 60065:2002+A1:2006; European Committee for Electrotechnical Standardization (CENELEC), EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7.2 EMC

- (1) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- (2) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- (3) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998





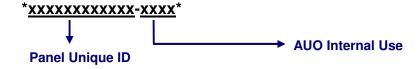
Global LCD Panel Exchange Center

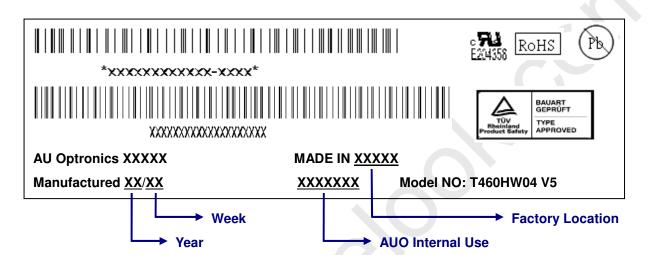
T460HW04 V5 Product Specification

8. Packing

8-1 DEFINITION OF LABEL:

A. Panel Label:



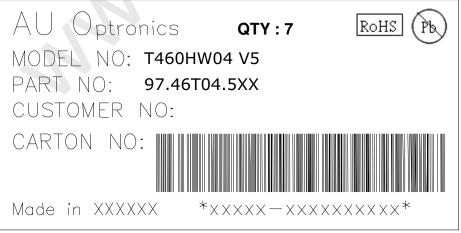


Green mark description

- (1) For Pb Free Product, AUO will add (Pb) for identification.
- (2) For RoHs compatible products, AUO will add RoHS for identification.

Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

B. Carton Label:

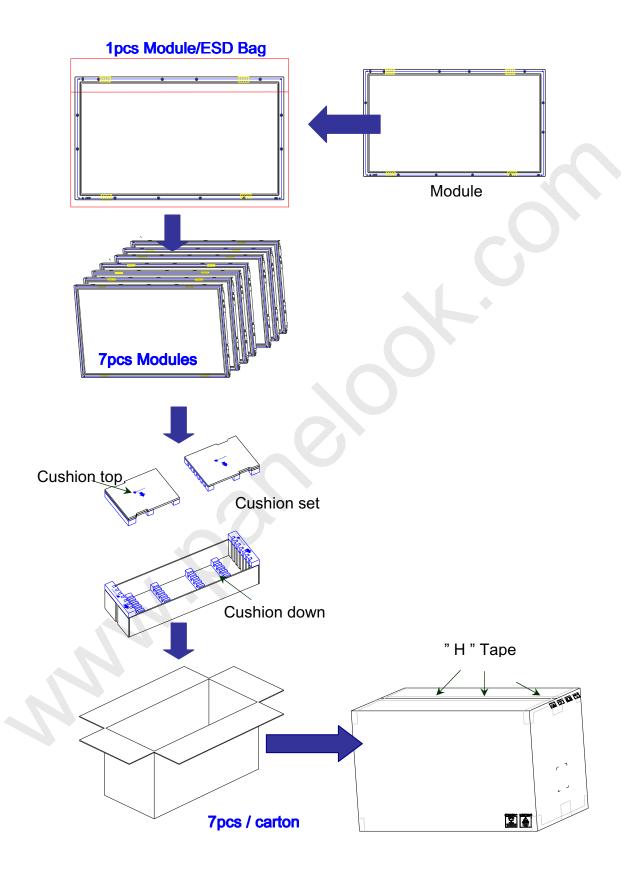


© Copyright AUO Optronics Corp. 2009 All Rights Reserved.





8-2 PACKING METHODS:

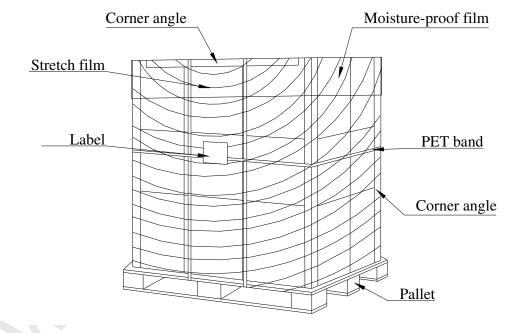






8-3 Pallet and Shipment Information

			Packing							
	Item	Item Qty. Dimension Weight (kg)								
1	Packing Box	7 pcs/box	1160(L)mm*375(W)mm*690(H)mm	160(L)mm*375(W)mm*690(H)mm 76						
2	Pallet	1 1180(L)mm*1150(W)mm*132(H)mm 18								
3	Boxes per Pallet	3 boxes/Pal	boxes/Pallet (By Air); 3 Boxes/Pallet (By Sea)							
4	Panels per Pallet	per Pallet 21pcs/pallet(By Air) ; 21 pcs/Pallet (By Sea)								
5	Pallet 21(by Air) 1180(L)mm*1150(W)mm*822(H)mm (by Air) 246 (by Air)									
	after packing	63(by Sea)	1180(L)mm*1150(W)mm*2466(H)mm (by Sea)	738 (by Sea)	40ft HQ					





8. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter cause circuit broken by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- (1) The device listed in the product specification sheets was designed and manufactured for TV application
- (2) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (3) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (4) Brightness of LED depends on the temperature. And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (5) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (6) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (7) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize





the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.





T315HW04 V1 Product Specification
Rev. 0.2

Appendix 1

EDID Setting

Item	Description								Value							
Vendor code	Vendor Code 0 : - 1 : AUO									1						
Panel Inch	Panel Inch, setting function: 21.6inch= 22 /									32						
H.Resolution	Panel Horizontal resolution information. 16bit: 0x02 = MS Byte, 0x03 = LS Byte (1) Horizontal resolution = 3840 (2) Horizontal resolution = 1920 (3) Horizontal resolution = 1366										1920					
V.Resolution	Panel Vertical Resolution information: 16bit : 0x04 = MS Byte, 0x05 = LS Byte (1) Vertical resolution = 2160 (2) Vertical resolution = 1080 (3) Vertical resolution = 788									1080						
V. Frequency	Panel Vertical frequency information. 0: 50Hz / 60Hz 1: 100Hz / 120Hz 2: 200Hz / 240Hz									1						
Data format	Panel LVDS Data format information. 0: 6bit									2						
Part Humber ^{*Note(1)}	Panel maker's version information. ② Example: T260XW05 V0 Item: (0), (1), (2), (3), (4), (5), (6), (7), (8), (9), (10), (11)	T	3	1	5	Н	5 W	0	4		T	0 11 1	12	13	14 15	Capitalization





T315HW04 V1 Product Specification
Rev. 0.2

Appendix 2

i. EMI specification

Model name: T315HW04 V1

Item	Min	Тур	Max	Unit
EMI level (Note)			-6	$dB(\mu V/m)$
SSCG		350		ps

Note:

Z Signal generator: PSG400 (Sony EMCS)

丙、EMI site: Sony EMCS Ichinomiya Tec. or using correlation value

op op Find result should be checked by connecting with TV-set