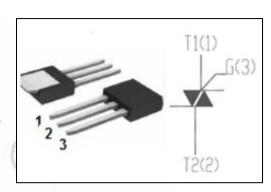


# isc Thyristors T835-600H

#### **APPLICATIONS**

- With TO-251(IPAK) package.
- Be suitable for general purpose AC switching, they can be used as an ON/OFF function in applications.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.



## ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER			UNIT
$V_{DRM}$	Repetitive peak off-state voltage			V
$V_{RRM}$	Repetitive peak reverse voltage			V
I <sub>T(RMS)</sub>	RMS on-state current @T₀=110°C			Α
I <sub>TSM</sub>	Surge non-repetitive on-state current	F=50HZ; t=20ms F=60HZ;16.7ms	80 84	А
$P_{G(AV)}$	Average gate power dissipation	@T <sub>j</sub> =125℃ t <sub>P</sub> =20 μ s	1	W
Tj	Operating junction temperature	South Mills	-40~125	$^{\circ}$
T <sub>stg</sub>	Storage temperature		-40~150	$^{\circ}$

### **ELECTRICAL CHARACTERISTICS (Tc=25℃ unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>RM</sub> =V <sub>RRM</sub>	Tj=25℃		5	μ <b>A</b>
			Tj=125℃		1	mA
I <sub>DRM</sub>	Repetitive peak off-state current	V <sub>DM</sub> =V <sub>DRM</sub>	Tj=25℃		5	μ <b>А</b>
			T <sub>j</sub> =125℃		1	mA
V <sub>TM</sub>	On-state voltage	I <sub>TM</sub> = 11A; t <sub>p</sub> =380 μ s			1.55	V
I <sub>GT</sub>	Gate-trigger current ( minimum IGT is guaranted at 5% of IGT max) Quadrant(I - II - III)	V <sub>D</sub> = 12 V;R <sub>L</sub> =30 Ω			35	mA
V <sub>GT</sub>	Gate-trigger voltage Quadrant (I - II - III)	$V_D = 12 \text{ V;R}_L = 30 \Omega$			1.3	V
R <sub>th(j-c)</sub>	Thermal resistance	Junction to case			1.6	°C/W
R <sub>th(j-a)</sub>	Thermal resistance	Junction to case			100	°C/W



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