

isc Thyristors

INCHANGE SEMICONDUCTOR

T835T-8FP

T1(1)

T2(2)

123

3(3)

DESCRIPTION

- With TO-220F packaging
- Operating in 3 quadrants
- High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- · Solid state relays; heating and cooking appliances
- Switching applications

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

| SYMBOL | PARAMETER | | | UNIT |
|---------------------|---|--------------|----------|------|
| V _{DRM} | Repetitive peak off-state voltage | | | V |
| V _{RRM} | Repetitive peak reverse voltage | | | V |
| I _{T(RSM)} | Average on-state current @Tc=113℃ | | 8 | Α |
| I _{TSM} | Surge non-repetitive on-state current | 50HZ 60HZ | 60 63 | Α |
| $P_{G(AV)}$ | Average gate power dissipation (over any 20 ms period) $@Tc=150^{\circ}C$ | | | w |
| Tj | Operating junction temperature | | | °C |
| T _{stg} | Storage temperature | | -40~150 | °C |

ELECTRICAL CHARACTERISTICS (Tc=25[°]C unless otherwise specified)

| SYMBOL | PARAMETER | CONDITIONS | | MIN | МАХ | UNIT |
|------------------|-----------------------------------|--|-----------------|-----|-------|------|
| I _{RRM} | Repetitive peak reverse current | V _R =V _{RRM} Rated; | Tj=25℃ ; | | 5 | |
| I _{DRM} | Repetitive peak off-state current | V _D =V _{DRM} Rated; | Tj=150℃ | | 800 | μΑ |
| V _{TM} | On-state voltage | I _T =11.3A | | | 1.55 | V |
| I _{GT} | Gate-trigger current | | Ι | | 35 | |
| | | V _D =12V;R _L =30 Ω | II | | 35 mA | mA |
| | | | III | | 35 | |
| V _{GT} | Gate-trigger voltage | V _D =12V;R _L =30 Ω | | | 1.3 | V |
| Rth (j-c) | Junction to case | Half cycle | | | 4.5 | ℃/W |

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