

### FEATURES

- · Low on-state voltage
- High dV/dt capability
- Guaranteed Maximum Turn-OffTime
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

# APPLICATIONS

- Power supplies
- Motor control
- · Light dimmers

# ABSOLUTE MAXIMUM RATINGS



T9G0161203DH

Phase Control Thyristors

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage		1600	V
It(av)	Average Forward Current	Sinewave,180° conduction,T <sub>c</sub> =65°C	1200	А
It(RMS)	Maximum RMS on-state current	1880	A	
I <sub>TSM</sub>	Max. peak, one-cycle	8.3 msec (60Hz), sinusoidal wave shape, 180° conduction, Tj = 125°C	28500	- A
	forward, non-repetitive surge current	10 msec (50Hz), sinusoidal wave shape, 180° conduction, Tj = 125℃	26000	
P <sub>G(AV)</sub>	Average gate power dissipation		5	W
TJ	Junction Temperature		-40~125	°C
T <sub>stg</sub>	Storage Temperature Range		-40~150	°C

# THERMAL CHARACTERISTICS

SYMBOL			PARAMETER	МАХ	UNIT
Rth j-c	Thermal Re	Thermal Resistance, Junction to Case		0.025	°C/W

#### **ELECTRICAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	TYPE	MAX	UNIT
V <sub>TM</sub>	Forward Voltage Drop	I <sub>™</sub> =1600A, T <sub>J</sub> = 25 ℃		1.35	V
I <sub>drm</sub> I <sub>rrm</sub>	Max. peak reverse and off-state leakage current	$T_J$ = 125°C, rated V <sub>DRM</sub> /V <sub>RRM</sub> applied		75	mA
I <sub>GT</sub>	DC gate current required to trigger	V₀ = 12 V;TJ = 25 ℃		200	mA
$V_{\text{GT}}$	DC gate voltage required to trigger	V <sub>D</sub> = 12 V;T <sub>J</sub> = 25 ℃		3	V
tq	Typical turn-off time	I <sub>TM</sub> >1000A, T <sub>J</sub> = 125°C, di/dt = 25A/μs, V <sub>R</sub> ≥5 V, dv/dt = 20V/μs,Duty cycle≥0.01%		250	μs

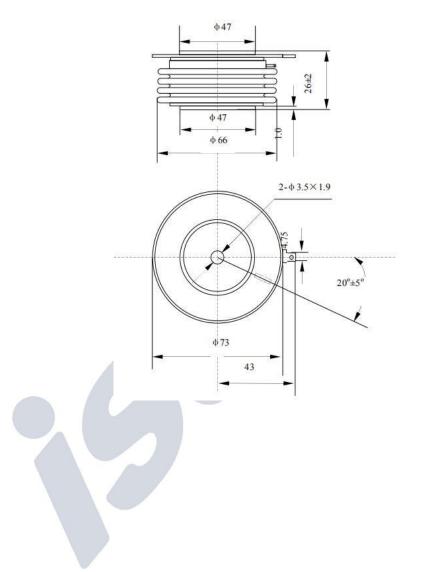


# T9G0161203DH

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# PACKAGE OUTLINE

Dimensions in mm (1mm = 0.0394")



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