TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

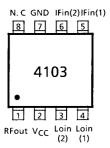
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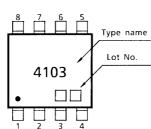
1.9 GHz Band Up Converter Application

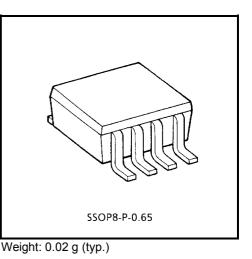
Features

- Built in Lo and IF buffer amplifiers.
- Double balanced MIX circuit
- High conversion gain: $G_C = 3dB$ (typ.)
- Recommended operating voltage: $V_{CC} = 2.7 \sim 3.3 \text{ V}$

Pin Assignment (top view)







Marking



Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V _{CC}	5	V
Total power dissipation	P _D (Note1)	300	mW
Operating temperature	T _{opr}	-40~85	°C
Storage temperature range	T _{stg}	-55~125	°C

Note 1: When mounted on the glass epoxy board of 2.5 $\text{cm}^2 \times 1.6 \text{ t}$

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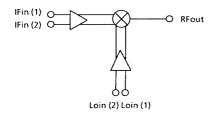
The information contained herein is subject to change without notice.

Electrical Characteristics (V_{CC} = 3 V, Ta = 25°C, Zg = ZI = 50 Ω)

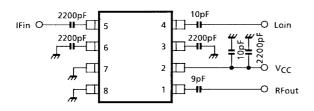
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
RF frequency range	f _{RFout}			1895	_	1918	MHz
IF frequency range	f _{IFin}		—	220	_	250	MHz
Lo frequency range	f _{Loin}			1645	_	1698	MHz
Circuit current	ICC	_	non carrier	23	26.5	33	mA
Conversion gain	G _C			1	3	_	dB
Output power at 1dB gain compression	Po1dB		PLoin = −20dBmW	-19	-17	_	dB
Lo-RF leakage power	P _{RFLo}	1		_	_	-20	dBmW
Lo-IF leakege power	PIFLo	1	P _{RFout} = -18dBmW	_	_	-33	dBmW
Adjacent channel leakage power ratio	Padj]	P _{IFin} = Adjusted ∆f = 600 kHz (Note2)	_	-63	_	dB

Note 2: Input signal is modulated to $\pi/4QPSK$ ($\alpha = 0.5$). Bit rate is 384 kbps.

Block Diagram



Test Circuit 1



Notice

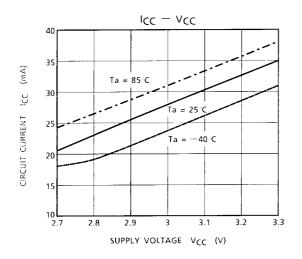
The circuits and measurements contained in this document are given only in the context of as examples of applications for these products.

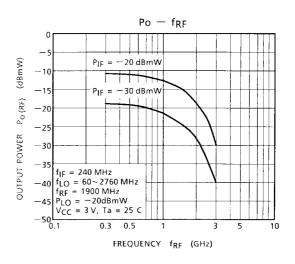
Moreover, these example application circuits are not intended for mass production, since the high-frequency characteristics (the AC characteristics) of these devices will be affected by the external components which the customer uses, by the design of the circuit and by various other conditions.

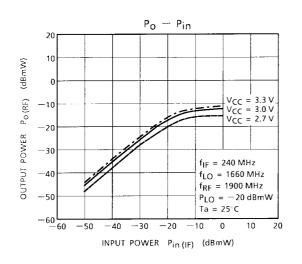
It is the responsibility of the customer to design external circuits which correctly implement the intended application, and to check the characteristics of the design.

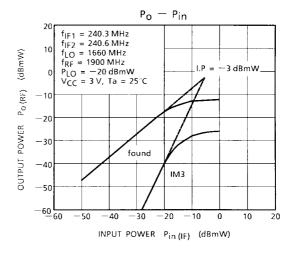
TOSHIBA assume no responsibility for the integrity of customer circuit designs or applications.

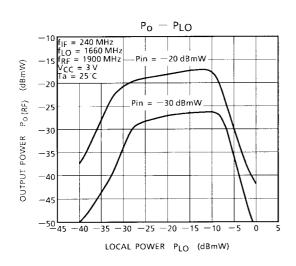
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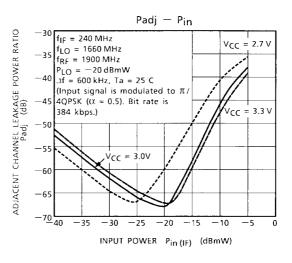










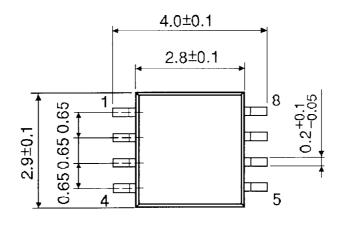


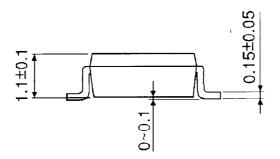
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Package Dimensions

SSOP8-P-0.65

Unit : mm





Weight : 0.02 g (Typ.)