TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA7279P,TA7279AP

DUAL BRIDGE DRIVER

The TA7279P, TA7279AP are dual bridge driver designed for DC motor rotation control.

FEATURES

- Wide Range of Operating Voltage
 V CC (opr.) = 6~18 V (P, AP),
 V S (opr.) = 0~16 V (P) / = 0~18 V (AP)
- Output Current Up to 1.0 A (AVE.), 3.0 A (PEAK)
- Built-in Thermal Shut Down and Current Limiter
- Input Hysteresis for Stable Operation

BLOCK DIAGRAM



Weight: 3.00 g (Typ.)



PIN FUNCTION

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION		
1 V	IN2-A	A-ch input terminal		
2 V	IN2-A			
3	V _{I N1-A}	A-ch input terminal		
4 OUT1	-A A	-ch output terminal		
5 V	S ⁻ A	A-ch Motor drive power supply		
6 OUT2	-A A	-ch output terminal		
7	GND	CND torminal		
8 GND		Give terminar		
9 OUT2	-B B	-ch output terminal		
10 V	S-B	B-ch Moter drive power supply		
11 OUT1	-B B	-ch output terminal		
12 V	IN1-B	B-ch input terminal		
13 V	IN2-B	B-ch input terminal		
14 V	CC	Logic power supply		

APPLICATION NOTE

(1) Input circuit



Input terminals of (2), (3), (12) and (13) Pin are all high active type and have a hysteresis.

 $3\ \mu A$ Typ. of input current is required.

The input circuit is an active high type, as shown in the diagram. When voltage higher than the specified $V_{\rm IN}$ (H) is applied, the output is logic "H". When voltage lower than the specified

 V_{IN} (L) is applied or if the input is grounded, the output is logic "L". Since the input current IN flows to the input when logic "H", be careful with the output impedance at the previous step.

(2) Ou tput circuit



FUNCTION

IN1 IN	2	OUT1	OUT2	MODE		
11		L	L	Brake		
0 1		L	Н	CW / CCW		
10		Н	L	CCW / CW		
0 0		High Im	STOP			

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC S		YMBOL	RATING	UNIT	
Supply Voltage	AP	Vec (MAX)	25	V	
Supply Voltage	Р		20		
Motor Drivo Voltago	AP		25	V	
Motor Drive Voltage	Р	vS(IVIAA.)	18		
	PEAK I	_O (PEAK)	3.0	A	
Output Current	AVE. I	_O (AVE.) 1.	0		
Power Dissipation		P _D (Note)	2.3	W	
Operating Temperature		T _{opr}	−30~75 °	С	
Storage Temperature		T _{stg}	−55~150 °	С	

Note: N o heat sink.

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC SYM		BOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Supply Current		I _{CC1}	1	V _{CC} = 18 V, Output Off, Stop mode	14 28 41 10 29 38			mA
		I _{CC2} 1		V _{CC} = 18 V, Output Off, CW / CCW mode				
		I _{CC3} 1		V _{CC} = 18 V, Output Off, Brake mode	8 20		35	
Input Operating Voltage	1 (High)	V _{IN (H)}	— T	_j = 25°C	3.0	— V	СС	V
	2 (Low)	V _{IN (L)}	— T	j = 25°C		<u> </u>	8	
Input Current		I _{IN} 2		Sink, V _{IN} = 3 V	— 3		10	μA
Output Saturation Voltage	Upper	V _{SATU-1}	3	I _O = 0.1 A, V _{CC} = V _S = 18 V	_	— 1.	1	V
	Lower	V _{SATL-1} 3		I _O = 0.1 A, V _{CC} = V _S = 18 V		— 1.	0	
	Upper	V _{SATU-2}	3	I _O = 1.0 A, V _{CC} = V _S = 18 V	— 1.	2	1.5	
	Lower	V _{SATL-2} 3		I _O = 1.0 A, V _{CC} = V _S = 18 V	<u> </u>	05	1.4	
Leakage Current	Upper	I _{LU}		V _S = 25 V			50	μA
	Lower	ILL	— V	_S = 25 V			50	
Diode Forward Drop	Upper	V _{FU} 4		I _F = 1 A	— 2.	0	_	V
	Lower	V _{FL} 4		I _F = 1 A	— 1.	3	_	v

TEST CIRCUIT 1.

ICC1, 2, 3



TEST CIRCUIT 2. I_{IN (H), (L)}



TEST CIRCUIT 3.

VSATU-1, 2 / VSATL-1, 2



TEST CIRCUIT 4. $V_{FU, L}$





APPLICATION CIRCUIT



Problems may result if a capacitor is inserted in parallel to the motor as a measure against noise. If measures against noise are necessary, connect capacitors as shown in the diagram below. A larger bypass capacitor between V_{CC} and GND is effective against noise and other problems. (A capacitance higher than 100 μ F is recommended.)



Note: Utmost care is necessary in the design of the output line, V_S and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

PACKAGE DIMENSIONS

HDIP14-P-500-2.54A

Unit: mm



Weight: 3.00 g (Typ.)

RESTRICTIONS ON PRODUCT USE

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