

Especially economical and universal operational amplifiers in package 5 G 8 DIN 41873 (TO 99) which by their excellent performance qualities are well suited for a wide range of applications. No external components for frequency compensation are required. TAA 2761 A (8 pins) in plastic plug-in package.

For single amplifier performance, see TAA 761 data sheet.

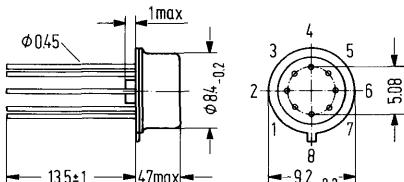
Additional features:

- Wide common-mode range
- Large supply voltage range
- Wide temperature range (TAA 2762)
- Protection against destruction
- High output current
- Large control range
- No frequency compensation

Type	Ordering codes
TAA 2761	Q67000-A1027
TAA 2761 A	Q67000-A1028
TAA 2762	Q67000-A1029
TAA 2765	Q67000-A1030
TAA 2765 A	Q67000-A1031

Package outlines

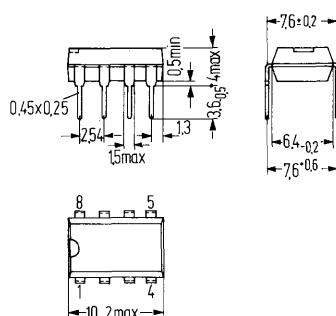
TAA 2761, TAA 2762, TAA 2765



Package similar to 5 G 8 DIN 41873
(similar TO-99)
weight approx. 1.1 g

Dimensions in mm

TAA 2761 A, TAA 2765 A



Plastic plug-in package, 8 pins;
20 A 8 DIN 41866, weight approx. .7 g

Maximum ratings

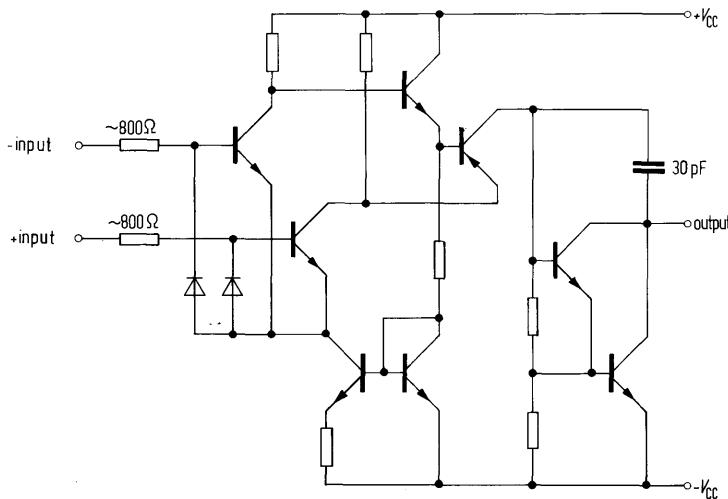
	TAA 2761/A TAA 2762 TAA 2765/A
Supply voltage	V_{CC} ± 15
Output current	I_o 70 mA
Differential input voltage	V_{ID} $\pm V_{CC}$
Junction temperature	T_j 150 °C
Storage temperature	T_s -55 to +125 °C
Thermal resistance:	
System-case (TAA 2761/2/5)	$R_{thScase}$ 80 K/W
System-ambient air (TAA 2761/2/5)	R_{thSamb} 190 K/W
System-ambient air (TAA 2761 A/2765 A)	R_{thSamb} 140 K/W

	TAA 2761/A TAA 2762 TAA 2765/A
V_{CC}	± 2 to ± 15 V
T_{amb}	0 to +70 °C
T_{amb}	-55 to +125 °C
T_{amb}	-25 to +85 °C

Range of operation

Supply voltage	V_{CC}
Ambient temperature in operation	± 2 to ± 15 V
TAA 2761/A	T_{amb} 0 to +70 °C
TAA 2762	T_{amb} -55 to +125 °C
TAA 2765/A	T_{amb} -25 to +85 °C

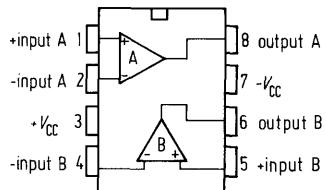
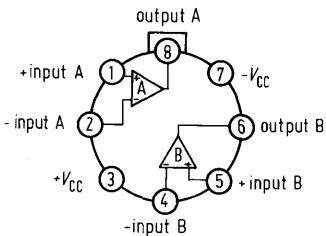
Circuit of one operational amplifier



Pin configuration

TAA 2761
TAA 2762
TAA 2765

TAA 2761 A
TAA 2765 A



TAA 2761; A

TAA 2762

TAA 2765; A

Operating characteristics(V_{cc} = ±15 V)**TAA 2761/A****TAA 2765/A**T_{amb} = 25 °C**TAA 2762**T_{amb} = 25 °CT_{amb} = -55 to +125 °C

min max

		min	typ	max	min	typ	max	min	max	
Supply current	I _{cc}		.5	1.5		.5	1.5			mA
Input offset voltage (R _G = 50 Ω)	V _{io}	-6		6	-4		4	-6	6	mV
Input offset current	I _{io}	-300	±80	300	-100	±50	100	-300	300	nA
Input current	I _i		.5	1.0		.3	.7		1.0	μA
Output voltage: R _L = 2 kΩ	V _{qpp}	14.9			-14	14.9		-14	14.8	V
R _L = 620 Ω	V _{qpp}	14.9			-12.5	14.9		-12.5	14.8	V
Input impedance (f = 1 kHz)	Z _i		200			200				kΩ
Open loop voltage gain R _L = 2 kΩ, f = 100 Hz G _v		80	85		85	87		80		dB
R _L = 10 kΩ, f = 100 Hz G _v			90			92				dB
Output leakage current	I _{qlk}		1	10		1	10			μA
Input common mode range (R _L = 2 kΩ)	V _{ICM}	12	±13.5	-12	12	±13.5	-12			V
Common mode rejection mode (R _L = 2 kΩ)	CMRR	65	79		70	81				dB
Sensitivity to supply voltage variations (G _v = 100)	$\frac{\Delta V_{io}}{\Delta V_{cc}}$		25	100		25	100			μV/V
Temp. coefficient of V _{io} (R _G = 50 Ω)	α_{vio}		6			6	25			μV/K
Temp. coefficient of I _{io} (R _G = 50 Ω)	α_{lio}		.3			.3	1.5			nA/K
Noise voltage (to spec. V _N DIN 45405; measured at input R _s = 2,5 kΩ)			3			3				μV
Output saturation voltage (I _o = 10 mA) (V _{cc} = ±5 V)	V _{qsat}			1			1			V
Supply current	I _{cc}		.5			.5				mA
Input offset voltage	V _{io}	-6		6	-4		4			mV
Input offset current	I _{io}	-300		300	-100		100			nA
Input current	I _i			1.0			.6			μA
Output voltage (R _o = 2 kΩ)	V _{qpp}	4.9			4.9			4.8	-4	V
Open loop voltage gain (R _e = 2 kΩ, f = 1 Hz)	G _v	70			70					dB