TOSHIBA Bi-CMOS INTEGRATED CIRCUIT SILICON MONOLITHIC

TB1246AN

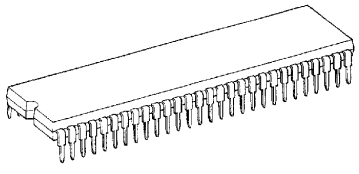
NTSC 1 CHIP (IF + VCD PROCESSOR) IC

TA1246AN is the IF & Video processing IC for NTSC color TV system. This IC demodulates NTSC PIF, SIF and composite video signal to R / G / B primary colors and Audio signals.

TA1246AN has the analog R / G / B interface, therefore it is easy to make up PIP system by using this IC.

Because of the built-in video and audio switch, TA1246AN can deal with an external channel without extra switch.

TA1246AN has an I^2C BUS interface. Various controls (Brightness, Color etc.) can be done via two bus lines.



SDIP56-P-600-1.78

Weight : 5.55 g (Typ.)

FEATURES

IF stage

- Intercarrier Input
- Double Time Constant IF AGC
- Bus Controlled RF AGC
- Bus Controlled PIF VCO
- PLL SIF Demodulation (Thank coil-less)

Video stage

- Built-in Video Switch (2 Inputs / 1 Output)
- Built-in Chroma Trap
- Built-in Y Delay Line
- Black Expansion
- DL Type Sharpness Control

Chroma stage

- Built-in 1 H DL
- Built-in BPF / TOF
- Fsc Continuous Wave Output

Text stage

- Fast Blanking
- Analog R / G / B Interface
- Cut-Off / Drive Adjustment
- ABCL

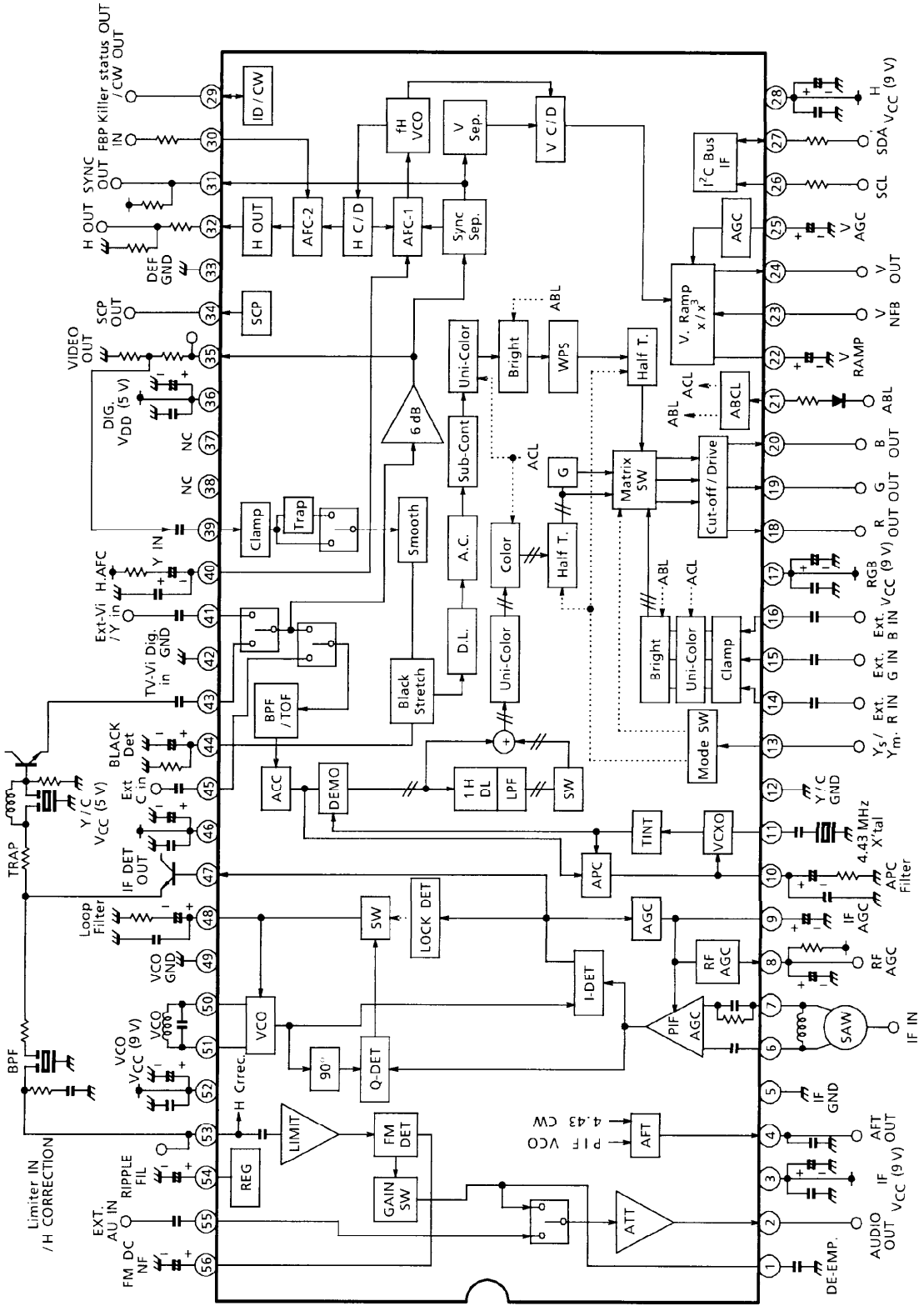
Deflection stage

- Resonator less H-VCO
- Dual Horizontal AFC
- Horizontal Phase Control
- Vertical Phase & Amplitude Control
- H / V Lock Detection
- Sand Castle Pulse Output (HD + VD + Gate Pulse)
- No Vertical Output Mode

Audio Stage

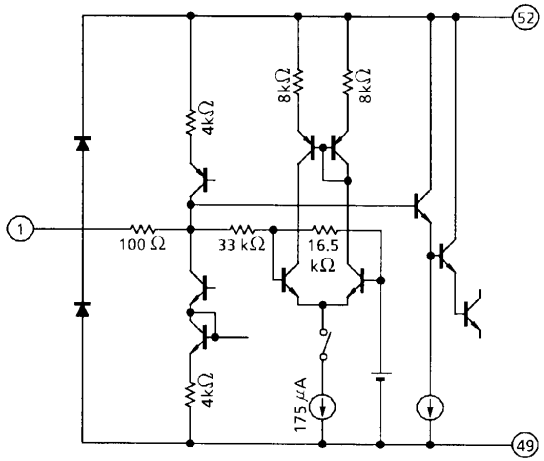
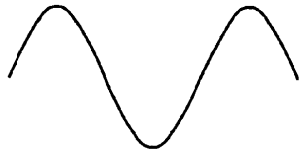
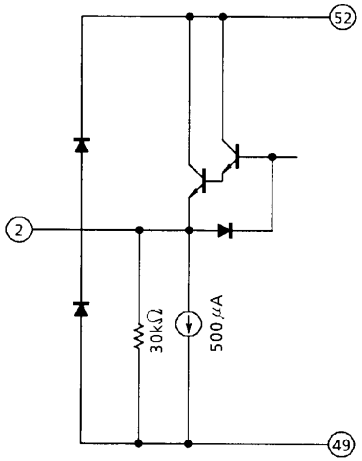
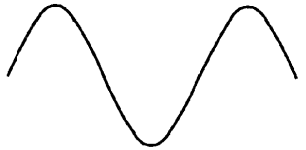
- Built-in Audio Switch (2 Inputs / 1 Output)
- Built-in Audio Attenuator

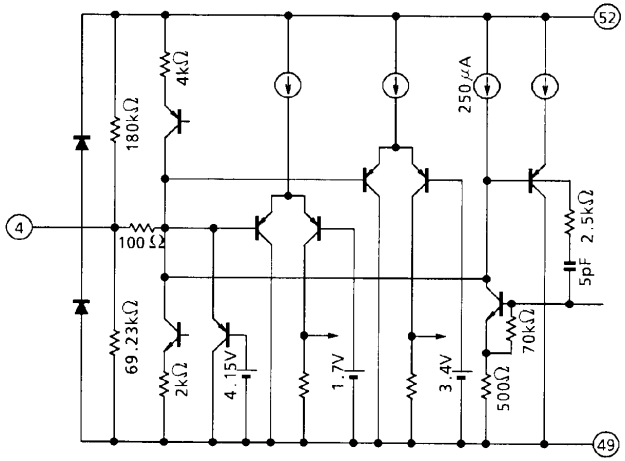
BLOCK DIAGRAM

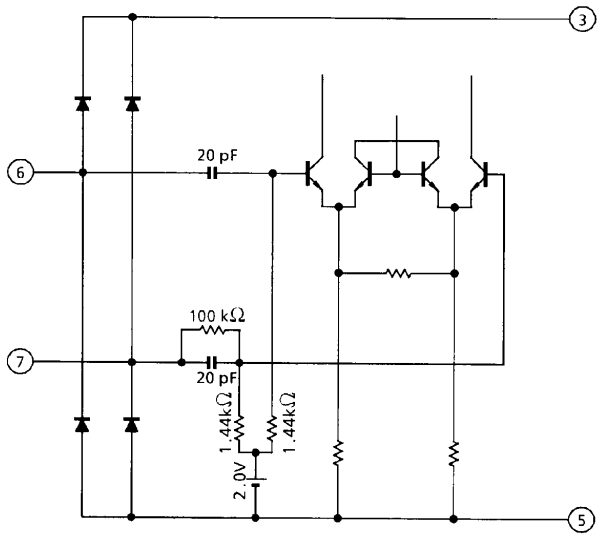
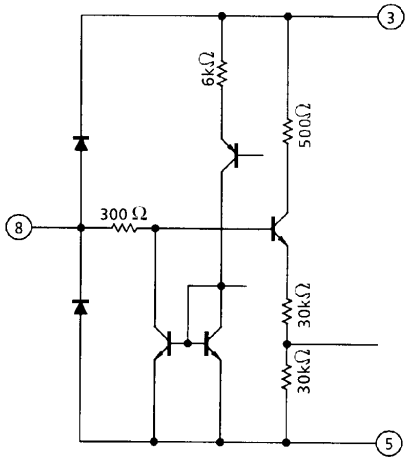


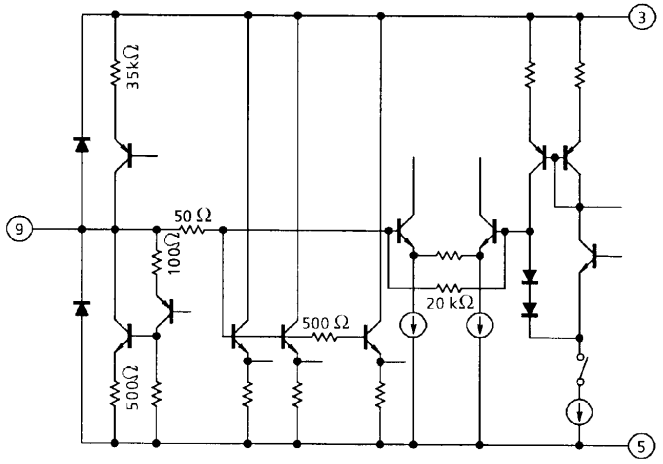
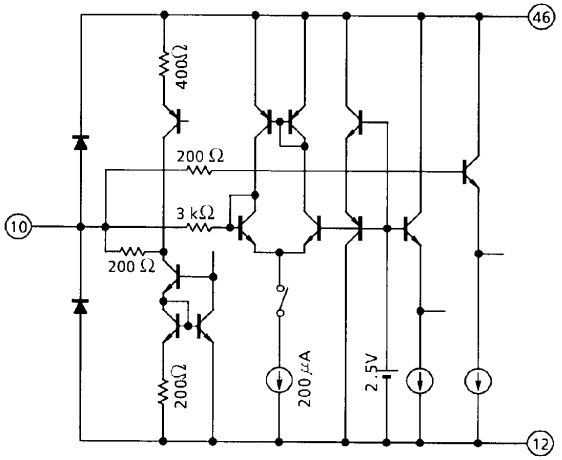
TA1246AN−3

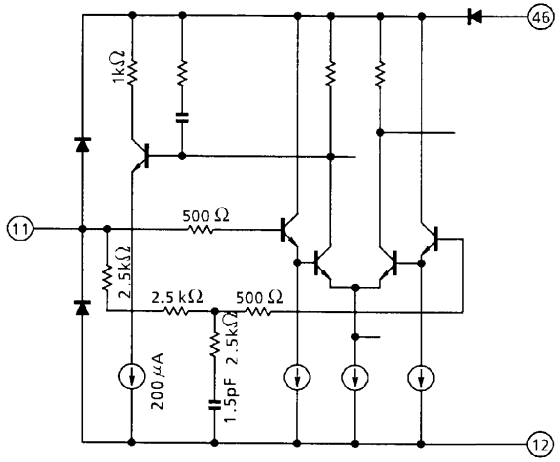
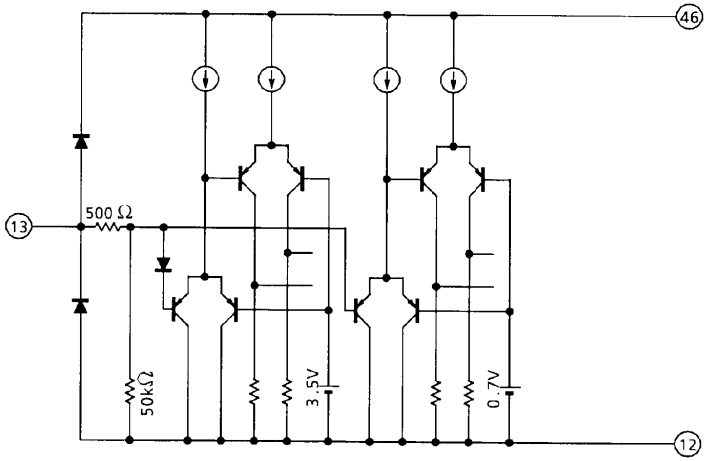
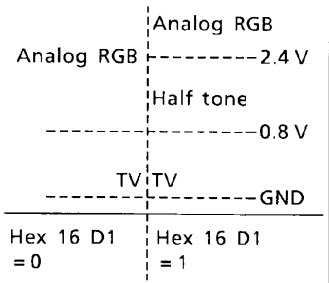
TERMINAL INTERFACE

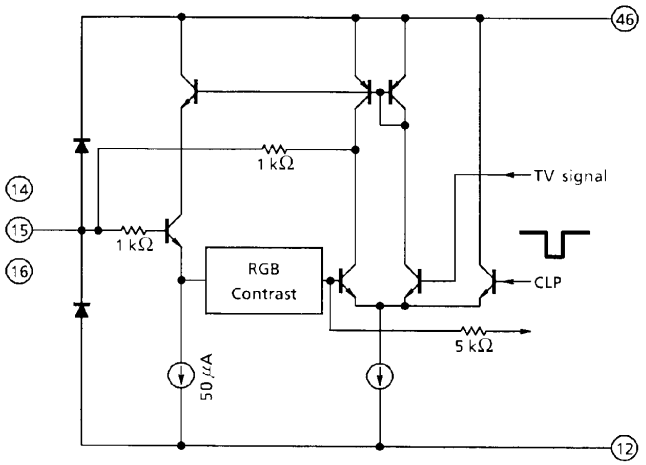
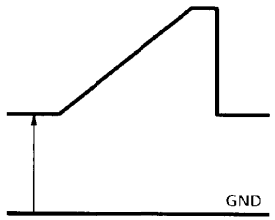
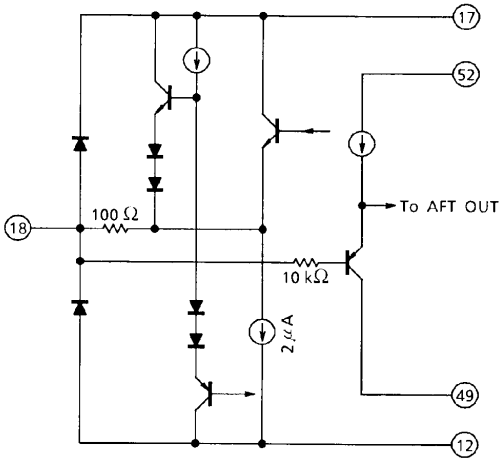
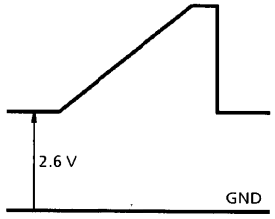
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
1	De-Emphasis	The terminal to be connected with capacitor for de-emphasis. 1500 pF capacitance realizes 75 μs / 50 μs de-emphasis (Switched by bus). The output impedance is as follows; NTSC : 50 kΩ		On 6 dB AMP 927 mV_{rms} 
2	Audio Output	The terminal for audio output. FM Det. signal or the signal inputted from Pin 55 is outputted (Switched by bus). And its amplitude is controlled by bus.		At ATT Max. 927 mV_{rms} 

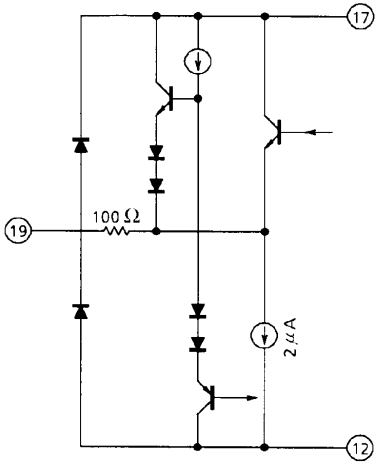
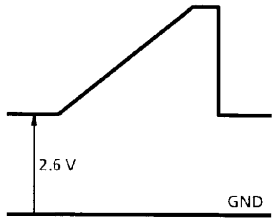
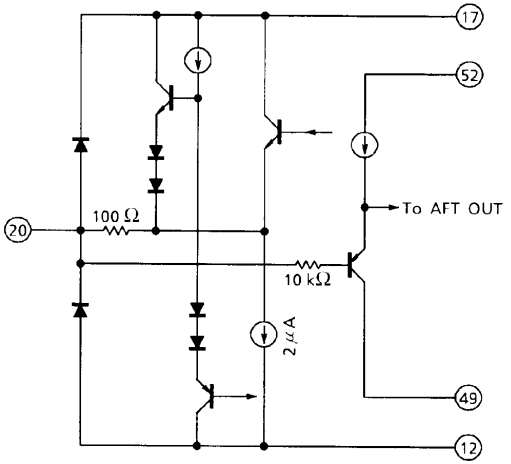
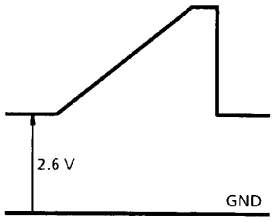
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
3	IF V_{CC} (9 V)	The terminal for V_{CC} of PIF circuit. Supply 9 V. In order to prevent leakage through V_{CC}, inserting traps for IF carrier and f_H is recommended.	—	—
4	AFT Output / Self-Adj. Output	The terminal for AFT output and Self-adj. output. AFT voltage, half of RF AGC Voltage, Red signal or Blue signal is outputted (Switched by bus) for self alignment. AFT output impedance is 50 Ω (typ.).		0.3 V~4.7 V
5	IF GND	The terminal for GND of PIF circuit. In order to realize good PIF Det. performance for low IF input, separate IF GND wiring from VCO GND (Pin 49) as far as possible.	—	—

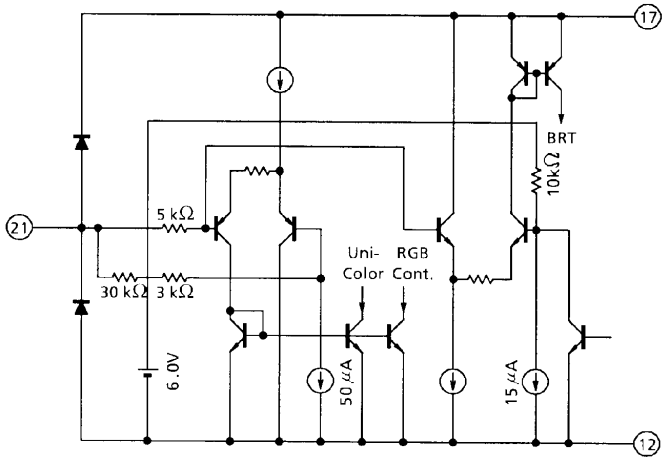
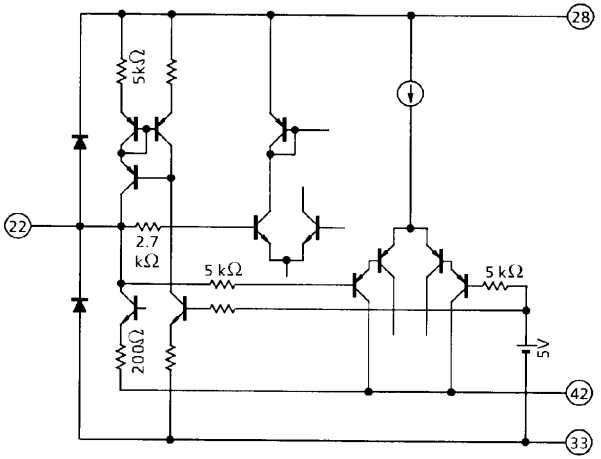
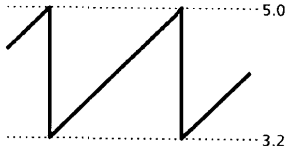
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
6 7	IF Input IF Input	The terminal for IF signal input. Pin 6 & Pin 7 are the both input poles of a differential amplifier.		Typical Input 90 dBμV
8	RF AGC	The terminal for RF AGC output (Open corrector Output). To get rid of noises, connect a capacitor to this terminal.		0 V~9 V

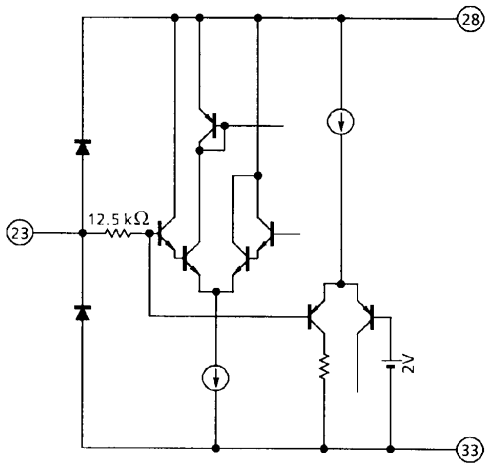
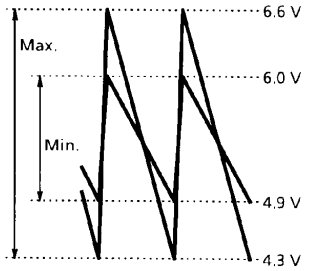
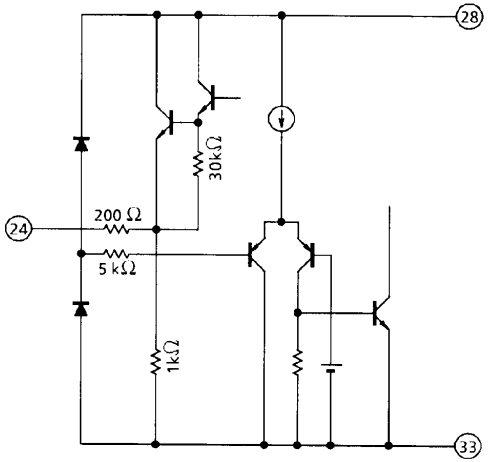
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
9	IF AGC	The terminal to be connected with an IF AGC filter. Peak AGC works n-sync. tip level of PIF Det. signal.		2 V~8 V
10	APC Filter	The terminal to be connected with APC filter for chroma demodulation. This terminal voltage controls the frequency of VCXO.		–

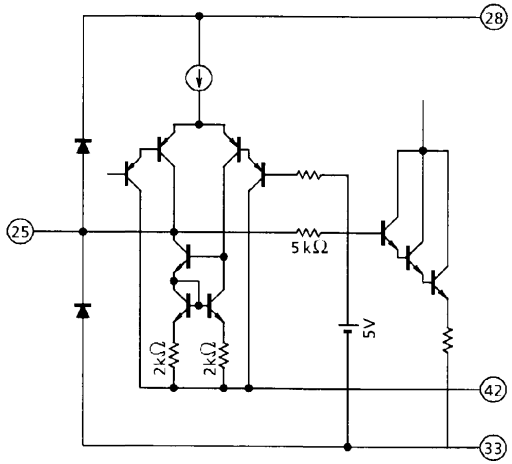
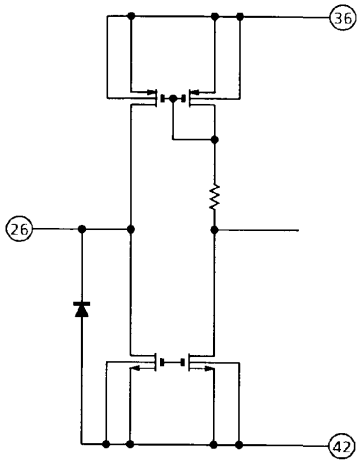
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
11	X'tal	The terminal to be connected with a 4.433619 MHz X'tal oscillator. This is the standard of both of 4.43 MHz / 3.58 MHz chroma demodulation and horizontal VCO. MIL : HC-49 / U is recommended.		—
12	Y / C GND	The terminal for GND of Y / C circuit.	—	—
13	Y_S / Y_m	The terminal for switching of Analog RGB Mode and fast Half tone. On Analog RGB Mode, the signal inputted into Pin 14, 15, 16 are outputted from Pin 18, 19, 20. For Half tone, the S16, D1 of I²C BUS SW has to be "1".		

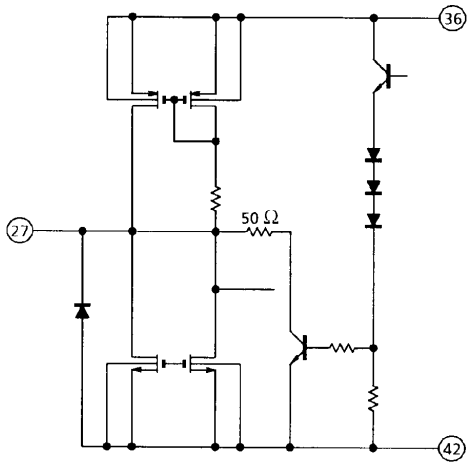
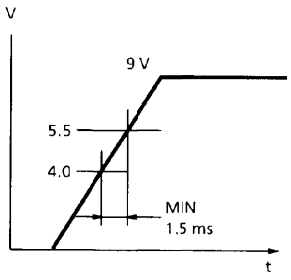
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
14 15 16	Analog R Input Analog G Input Analog B Input	The terminal for Analog RGB signals input. Input signals are clamped by charging / discharging coupling capacitors, therefore input with low impedance. 100 Ω or less is recommended.		100 IRE 0.5 V_{p-p} 
17	RGB V_{CC} (9 V)	The terminal for V_{CC} of RGB circuit (TEXT circuit). Supply 9 V.	—	—
18	R Output	The terminals for R signals output. Because of the limit of output current, set the resistance 2.0 kΩ or more to GND.		100 IRE 2.5 V_{p-p} 

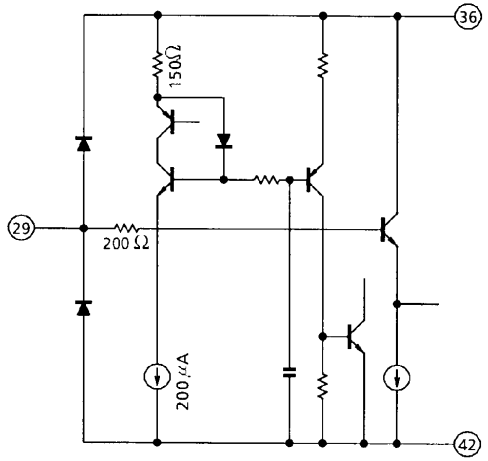
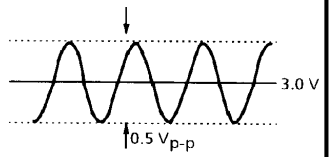
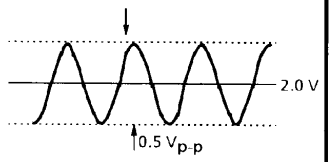
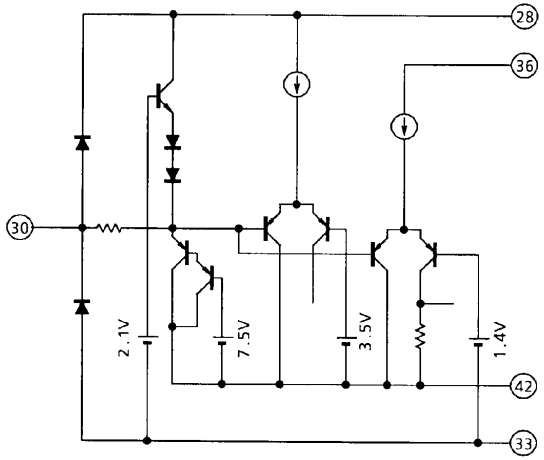
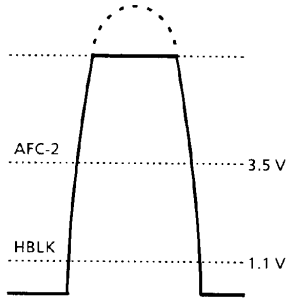
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
19	G Output	The terminals for G signals output. Because of the limit of output current, set the resistance 2.0 kΩ or more to GND.		100 IRE 2.5 V_{p-p} 
20	B Output	The terminals for B signals output. Because of the limit of output current, set the resistance 2.0 kΩ or more to GND.		100 IRE 2.5 V_{p-p} 

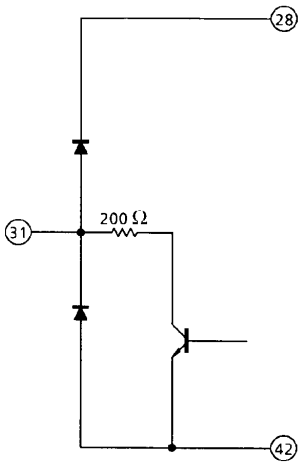
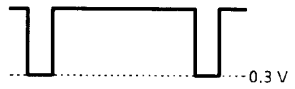
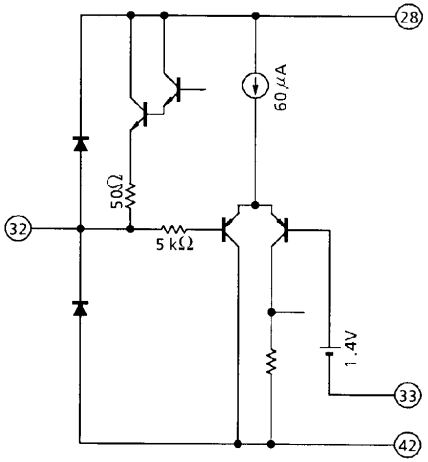
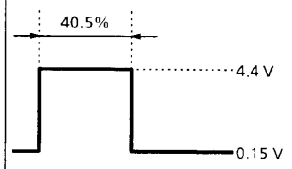
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
21	ABCL	The terminal for ABL / ACL control. Control voltage range is 5.5 V ~ 6.0 V. ABL Gain & ABL start point are selectable by bus.		At Open 6 V
22	V.Ramp	The terminal to be connected with a capacitor to make V.Ramp signal. V.Ramp amplitude is kept constant by V.AGC function.		

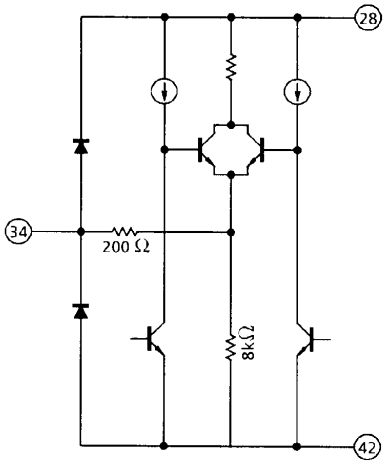
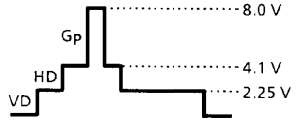
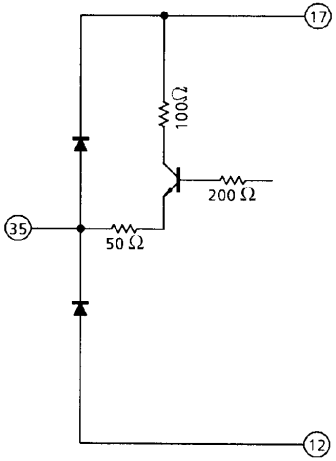
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
23	V.NFB	The terminal for input of V.sawteeth signal feedback. The amplitude of feed back signal is controlled by bus.		
24	V.Output	The terminal for output of Vertical drive signal.		—

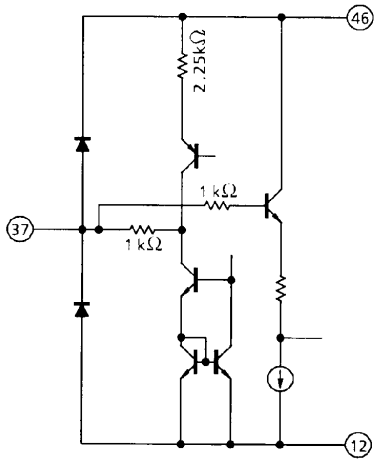
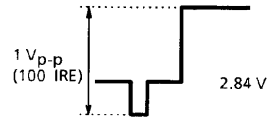
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
25	V.AGC	The terminal to be connected with a capacitor for V.AGC. V.AGC keeps V.Ramp amplitude constant.		—
26	SCL	The terminal for input of I^2C bus clock.		—

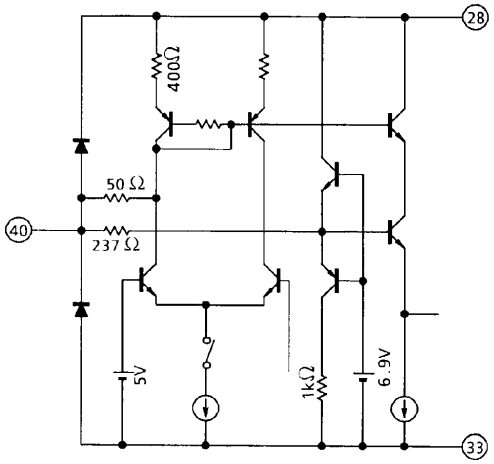
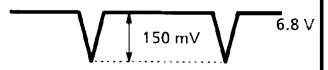
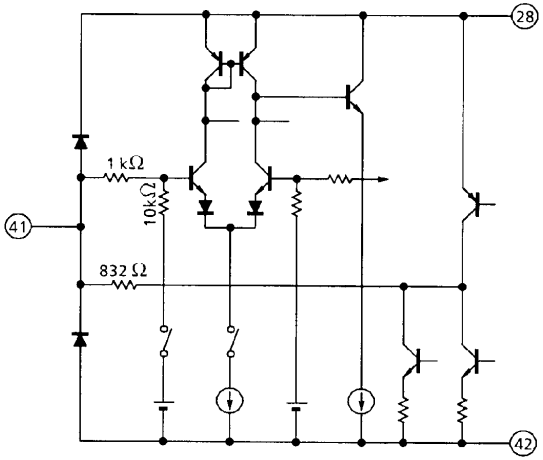
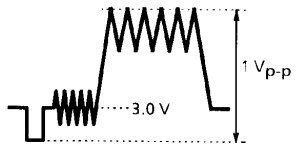
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
27	SDA	The terminal for input / output of I^2C bus data.		—
28	H. V_{CC} (9V)	The terminal for V_{CC} of deflection circuit. Supply 9 V. For 4 V or more, VCXO oscilates and for 5.5 V or more, H OUT signal is outputted. Set the raising time between 4 V and 5.5 V longer than 1.5 ms.	—	

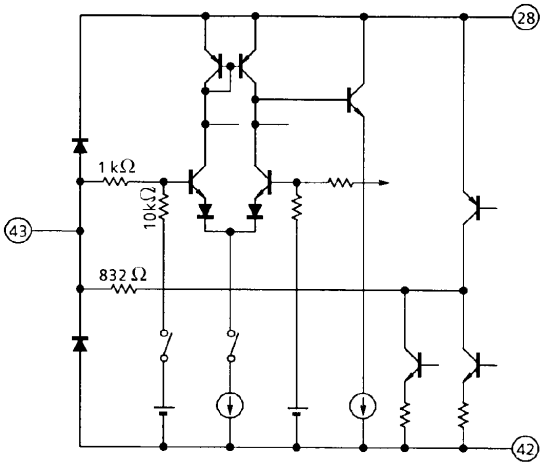
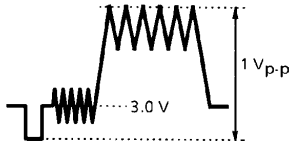
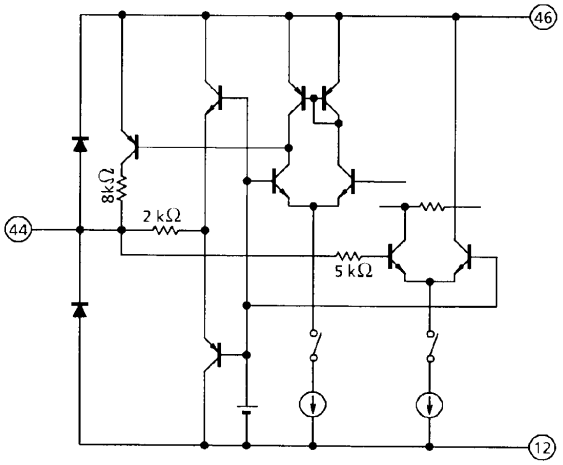
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
29	Killer status output / fsc CW Output	The terminal for the killer status output. B / W 2.0 V Color 3.0 V And the terminal for chroma sub-carrier frequency.		NTSC  B / W 
30	FBP Input	The terminal for FBP Input.		

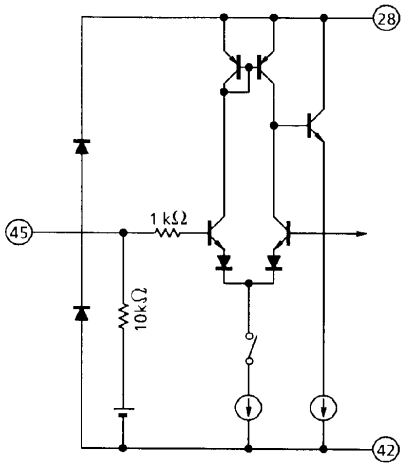
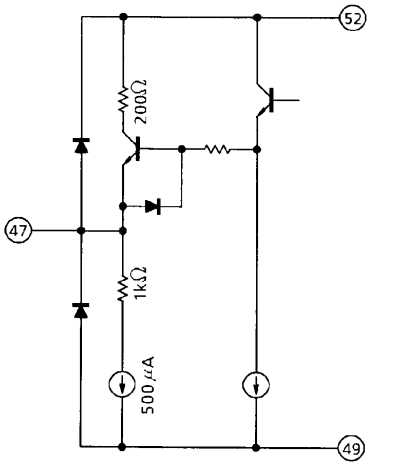
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
31	SYNC Output	The terminal for composite sync. output (Open corrector output).		
32	H.Output	The terminal for output of horizontal drive signal.		

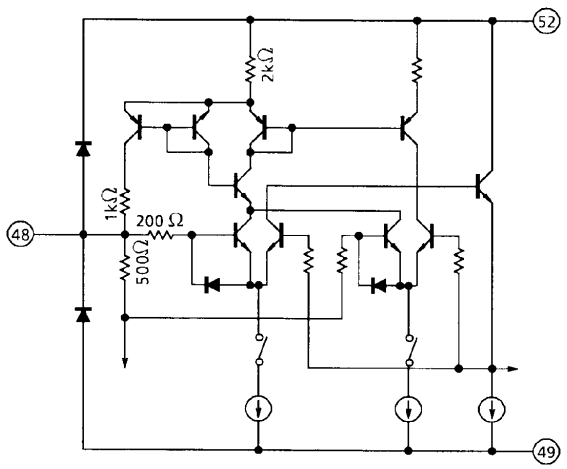
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
33	DEF GND	The terminal for GND of deflection circuit.	–	–
34	SCP Output	The terminal for Sand Castle Pulse (VD + HD + Gp) output.		
35	Video SW Output	The terminal for Video Switch output. The signal inputted into Pin 41 or 43 is outputted through 6 dB AMP.		2 V_{p-p}

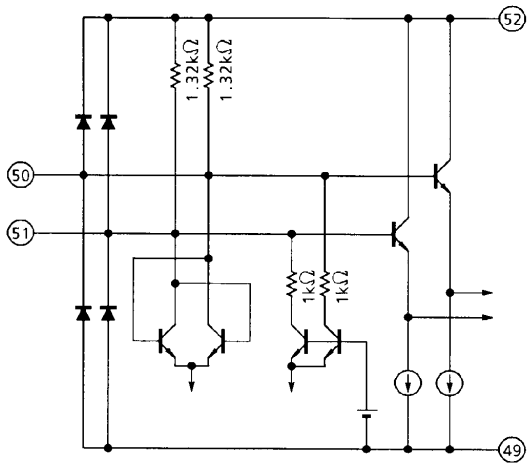
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
36	Dig. V_{DD} (5 V)	The Terminal for V_{DD} of digital block. Supply 5 V.	—	—
37	NC			
38	NC			
39	Y Input	The terminal for Y input. Input signal is clamped by charging / discharging coupling capacitor, therefore input with low impedance. 100 Ω or less is recommended. Typical input amplitude is 1.0 V_{p-p}.		

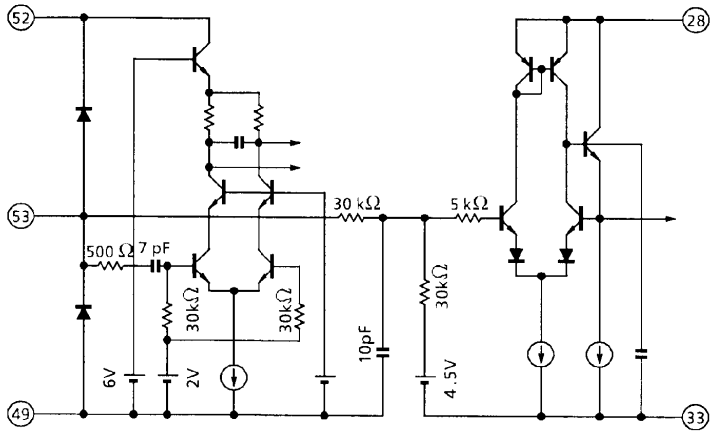
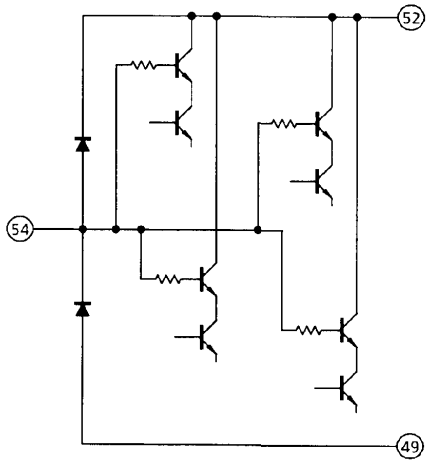
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
40	H. AFC	The terminal to be connected with H.AFC filter. This terminal voltage controls H. frequency.		
41	EXT. Video / Y Input	The terminal for input of composite signal or Y signal from TV's ext. jack. One of input terminal of video SW. This is also the terminal for sync. signal input. Input with low impedance. Typical input amplitude is 1.0 V_{p-p}.		
42	Dig. GND	The terminal for GND of digital block.	—	—

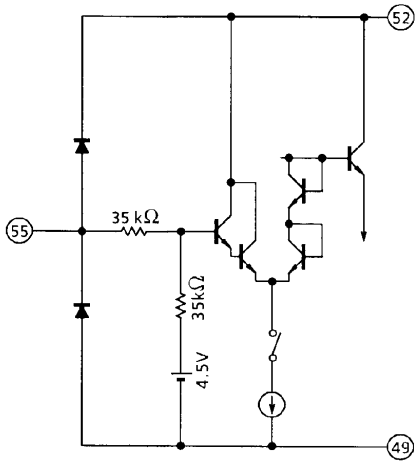
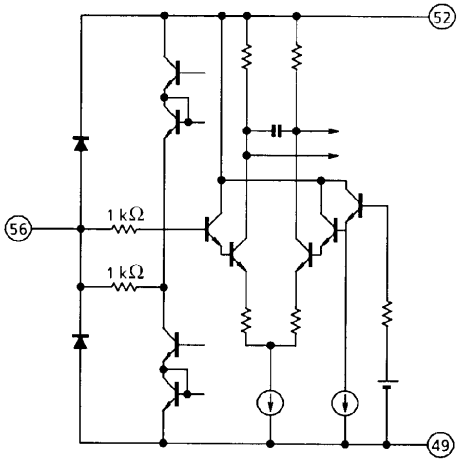
PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
43	TV Input	The terminal for input of composite video signal from PIF Det. output. One of input terminal of video SW. This is also the terminal for sync. signal input. Input with low impedance. Typical input amplitude is 1.0 V_{p-p}.		
44	Black Det.	The terminal to be connected with Black Det. filter. This terminal voltage controls Black stretching gain.		–

PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
45	EXT. C Input	The terminal for input of chroma signal from TV's ext. jack. Input through a coupling capacitor.		Burst Amplitude 286 mV_{p-p}
46	Y / C V_{CC} (5 V)	The terminal for V_{CC} of Y / C circuit. Supply 5 V.	—	—
47	IF Det. Output	The terminal for output of composite video signal and SIF signal detected in IF circuit. Typical video output amplitude is 2.2 V_{p-p}. In order to reduce 920 kHz beat, connect a emitter follower to drive audio trap and band-pass-filter.		2 V_{p-p}

PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
48	Loop Filter	The terminal to be connected with loop filter for IF PLL. This terminal voltage controls the frequency of IF VCO.		—
49	VCO GND	The terminal for GND of VCO and SIF circuit. In order to realize good PIF Det. performance for low IF input, please separate VCO GND wiring from IF GND (Pin 5) as far as possible.	—	—

PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
50 51	VCO	The terminal to be connected with a tank coil for IF VCO. IF VCO frequency is controlled by bus. For 27 pF ext. capacitance, frequency variable range is ±2 MHz.		—
52	VCO V_{CC} (9 V)	The terminal for V_{CC} of IF VCO and SIF. Supply 9 V. In order to prevent leakage through V_{CC}, inserting traps for IF carrier and f_H is recommended.	—	—

PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
53	Limiter Input / H.Correction	The terminal for SIF signal input and H.curve correction. By this terminal DC (3.5 V~5.5 V), it is possible to adjust H.phase (−1 μs~ + 1 μs). This can be used to correct horizontal curve caused by change of High-Voltage.		Typical 90 dBμV
54	Ripple Filter	The terminal to be connected with a capacitor to stabilize the performance of SIF injection-lock circuit.		—

PIN No.	PIN NAME	FUNCTION	INTERFACE CIRCUIT	INPUT / OUTPUT
55	EXT. Audio Input	The terminal for input of audio signal from TV's ext. jack. Input through a coupling capacitor. The signal inputted into this terminal is outputted from Pin 2 after passing audio SW and attenuator. Input impedance is 70 kΩ.		Center DC 4.5 V
56	FM DC NF	The terminal for FM DC Negative Feedback. Connect a capacitor to stabilize audio output DC level.		DC : 5.2 V

BUS CONTROL MAP
Write mode
Slave address : 88 HEX

SUB ADDRESS	D_7 MSB	D_6	D_5	D_4	D_3	D_2	D_1	D_0 LSB	PRESET DATA MSB	PRESET DATA LSB
00	Au Gain	WPS	Uni-Color (TV)						0000	0000
01	Mute	Brightness (TV / TEXT)							0100	0000
02		Color							1100	0000
03	V AGC	TINT							0100	0000
04	AF-G (1)	Vi Pol (0)	Sharpness						0010	0000
05	BPF / TOF SW	C-Trap	Au SW	Video SW		Half Tone	ABL Gain		0000	0000
06	Color System (101)			CW SW (0)	Sub-Contrast				0000	1000
07	R Cut Off								1000	0000
08	G Cut Off								1000	0000
09	B Cut Off								1000	0000
0A	1	G Drive Gain							0100	0000
0B	AFT M	B Drive Gain							0100	0000
0C	Vertical Position			Horizontal Position					0001	0000
0D	B.B.	Audio ATT							0000	0000
0E	V-Freq		RF AGC						0000	0000
0F	AFC Gain		Vertical Size						0010	0000
10	V Linearity				VS Correction				1000	1000
11	PIF VCO							(Fix 0)	1000	0000
12	*				*				1000	1000
13	N-Com	BLK	RGB Contrast						0000	0000
14	*	H-STP	F ID	Self Adj.		ID SW	ABL Start Point		0000	0000
15	TEST MODE								0000	0000
16	1	SE Adj (0)	IF Freq.			AFT ON	BGP P	Ym enb	0000	0000
17						TEST MODE			0000	0000

Read mode
Slave address : 89 HEX

7 MSB	6	5	4	3	2	1	0 LSB
POR	IF Lock	H Lock	IF Level	V Frq.	Color System		
Y-IN	RGB OUT	H-OUT	V-OUT	36 / 46	V Lock	AFT	

BUS CONTROL CONTENTS
Write mode

CHARACTERISTIC	DESCRIPTION	PRESET
Au Gain (Audio Gain SW)	0 : 50 kHz 1 : 25 kHz (X2 on 4.5 MHz mode)	50 kHz
WPS (White Peak Suppressor)	0 : ON 1 : OFF	ON
Uni-Color	Min : – 11.6 dB~Cen : 6.6 dB~Max : 11.6 dB	– 11.6 dB
Mute (Mute Mode)	00 : Normal 01 : Y-Mute 10 : RGB Out-Cut Off DC 11 : RGB Out-Cut Off DC + VP Out Hi (Service mode)	Y-Mute
Brightness	Min : 1.9 V~Cen : 2.6 V~Max : 3.4 V (Pedestal Level)	2.6 V
Color	Min : – 20 dB or less~Cen : 0 dB~Max : 8.15 dB	0 dB
V-AGC (Vertical AGC Speed)	0 : Normal 1 : ×3	Normal
TINT	Min : – 38°~Cen : 0°~Max : 38°	0°
AF-G (AF Gain SW)	Fix this data "1 : 75 μs (4.5 MHz)" invariably.	50 μs
Vi POL (Video Polarity)	Fix this data "0 : Normal" invariably.	Normal
Sharpness	Min : – 11 dB~Cen : 5 dB~Max : 12 dB	0 dB
BPF / TOF SW	0 : BPF 1 : TOF	BPF
C-Trap (Chroma Trap)	0 : OFF 1 : ON	OFF
AU SW (Audio SW)	0 : TV 1 : EXT.	TV
Video SW	00 : TV 01 : EXT. 10 : TV Y / C 11 : EXT Y / C	TV
Half Tone	0 : OFF 1 : ON	OFF
ABL Gain	00 : – 0.74 V 01 : – 0.64 V 10 : – 0.37 V 11 : – 0.12 V	– 0.74 V
Color System	Fix this data "101 : Fixed 358 NTSC" invariably.	Auto1
CW SW	Fix this data "0 : Auto" invariably.	Auto
Sub-Contrast	Min : – 3.5 dB~Cen : 0 dB~Max : 2.3 dB	0 dB
RGB Cut Off	Min : – 0.5 V~Cen : 0 V~Max : 0.5 V	± 0 dB
G / B Drive	Min : – 5.5 dB~Cen : 0 dB~Max : 3.5 dB	– 5 dB
AFT M (AFT Mute)	0 : Normal 1 : Mute	Normal
Vertical Position	000 : 0 H 111 : 7 H Delay / Pulse Width : 8 H	0 H
Horizontal Position	Min : – 3 μs~Cen : 0 μs~Max : 3 μs	0 μs
B.B. (Blue Back)	0 : OFF 1 : 50 IRE	OFF
Audio ATT	Min : – 85 dB~Cen : – 15 dB~Max : 0 dB	Min
V-Freq (Vertical Frequency)	01 : 60 Hz 10 : 263 H Fixed	Auto
RF AGC	000000 : IF Mute Min : 65 dBμV~Max 100 dBμV	IF Mute
AFC Gain	00 : Normal 01 : 1 / 3 10 : ×3 at VBLK 11 : AFC Off	Normal
Vertical Size	Min : – 40%~Cen : 0%~Max : 40%	0%
V Linearity	Upper Side ; Min : 16%~Cen : 0%~Max : – 14% Lower Side ; Min : – 20%~Cen : 0%~Max : 17.5%	0%
V-S Correction	Upper Side ; Min : 12%~Cen : 0%~Max : – 12% Lower Side ; Min : 15%~Cen : 0%~Max : – 15%	0%

CHARACTERISTIC	DESCRIPTION	PRESET
PIF VCO (PIF VCO f_0 Adj.)	Min : – 2 MHz~Cen : 0 MHz~Max : 2 MHz	0 MHz
N-Com (NTSC Comb SW)	0 : ON 1 : OFF	ON
BLK (Blanking SW)	0 : BLK ON 1 : BLK OFF	ON
RGB Contrast	Min : – 6.0 dB~Cen : 9.4 dB~Max : 14.0 dB	– 6.0 dB
H-STP (H-Out Stop)	0 : Normal 1 (& Mute data ; 11) : H-Out Stop & Low RGB Output	Normal
FID (Forced ID ON)	0 : Normal 1 : Killer OFF on Fixed System (This function doesn't work on Auto1 & Auto2 Mode.)	Normal
Self Adj. (AFT Output SW for Self Adj.)	00: AFT 01 : Blue 10 : Red 11 : RF AGC × 1 / 2	AFT
ID SW (ID Sensitivity Switching)	0 : Normal Mode 1 : Low Mode	Normal
ABL Start Point	00 : – 0.01 V 01 : – 0.11 V 10 : – 0.3 V 11 : – 0.45 V	– 0.01 V
TEST (TEST MODE)	For factory-TEST. Leave these bits preset data.	00 HEX
SE Adj.	Fix this data "0 : Normal" invariably.	0
IF Freq.	000 : 58.75 MHz 001 : 45.75 MHz 010 : 39.50 MHz 011 : 38.90 MHz 100 : 38.00 MHz 101 : 34.47 MHz 110 : 33.95 MHz 111 : 33.90 MHz	000
AFT ON	0 : Normal 1 : AFT-MUTE OFF	0
BGP P	0 : Normal 1 : 1.5 μs	0
Ym enb	0 : 0~0.8 V TV more than 0.8V OSD 1 : 0~0.8 V TV 0.8~2.4 V Half Tone more than 2.4 V OSD	0

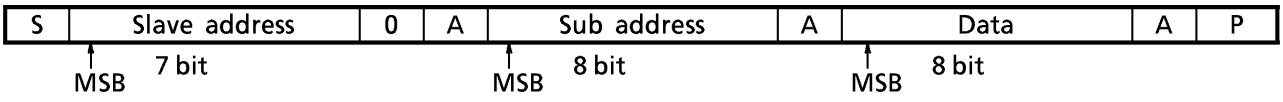
Read mode

CHARACTERISTIC	DESCRIPTION
POR (Power On Resection)	0 : Normal 1 : Resister Preset
IF Lock (IF Lock Detection)	0 : Lock Out 1 : Lock In
H-Lock (Horizontal Lock Detection)	0 : Lock Out 1 : Lock In
IF Level (IF AGC Gain Detection)	0 : High IF AGC Gain 1 : Low IF AGC Gain
V Frq (Vertical Frequency)	0 : 50 Hz 1 : 60 Hz
Color System	000 : B / W 100 : 3.58 NTSC
Y-IN (For Self-Diagnostic)	0 : No Signal 1 : OK
RGB Output (For Self-Diagnostic)	0 : No Signal 1 : OK
H-OUT (For Self-Diagnostic)	0 : No Signal 1 : OK
V-OUT (For Self-Diagnostic)	0 : No Signal 1 : OK
V-Lock (Vertical Lock Detection)	0 : Lock Out 1 : Lock In
AFT (AFT Lock Detection)	00 : Lock Out 01 : High Freq. 10 : Low Freq. 11 : Lock In
36 / 46 Recognition	0 : TB1236N 1 : TB1246N

I²C BUS CONTROLLED FORMAT SUMMARY

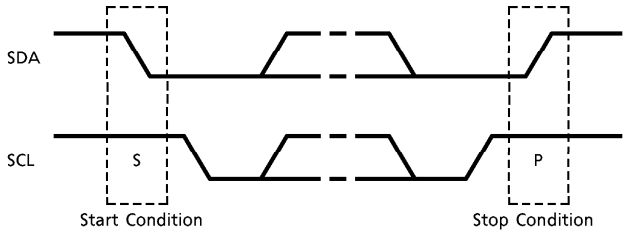
Bus controlled format of TB1246N is based on I²C Bus Control format of Philips.

Data transfer format

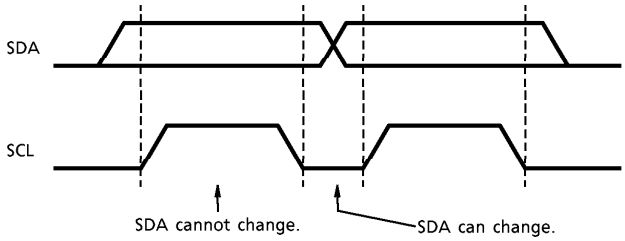


S : Start Condition
P : Stop Condition
A : Acknowledge

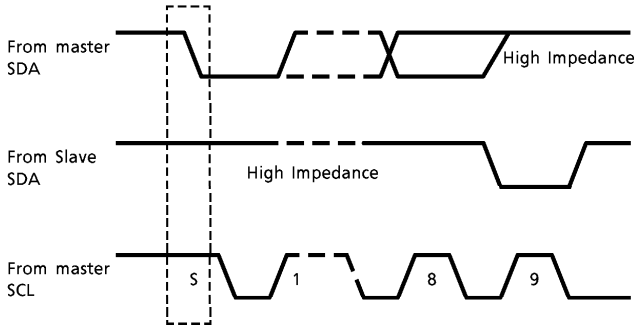
(1) Start and stop condition



(2) Bit transfer



(3) Acknowledge



(4) Slave address

A_6	A_5	A_4	A_3	A_2	A_1	A_0	R / $\overline{W}$
1	0	0	0	1	0	0	0

Purchase of TOSHIBA I²C components conveys a license under the Philips I²C Patent Rights to use these components in an I²C system, provided that the system conforms to the I²C Standard Specification as defined by Philips.

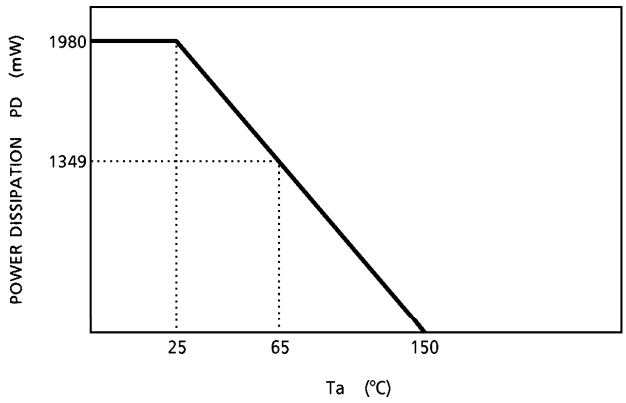
MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage (9 V V_{CC})	$V_{CC}max_9$	12	V
Supply Voltage (5 V V_{CC})	$V_{CC}max_5$	8	V
Power Dissipation	PD_{max}	1980 (*)	mW
Input Terminal Voltage	V_{in}	GND – 0.3~V_{CC} + 0.3	V
Operating Temperature	T_{opr}	– 20~65	°C
Storage Temperature	T_{stg}	– 55~150	°C

(*) When using this device at above Ta = 25°C, the power dissipation decreases by 15.9 mV per 1°C rise.

This IC is not proof enough against a strong E-M field by CRT which may cause function errors and / or poor characteristics.
Keeping the distance form CRT to the IC longer than 20 cm, or if cannot, placing shield metal over the IC, is recommended in an application.

Ta-PD CURVE



ELECTRICAL CHARACTERISTICS

DC CHARACTERISTICS

Pin voltage

PIN No.	PIN NAME	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
1	De-Emphasis	V_1	—	—	4.5	5.0	5.5	V
2	Audio Output	V_2	—	—	3.0	3.6	4.2	V
3	IF V_{CC}	V_3	—	Supply 9 V	—	9.0	—	V
4	AFT Output	V_4	—	—	2.0	2.5	3.0	V
7	IF Input	V_7	—	—	2.1	2.7	3.3	V
10	APC Filter	V_{10}	—	—	1.8	2.5	3.2	V
11	X'tal	V_{11}	—	—	37	4.0	4.3	V
13	Y_S	V_{13}	—	—	—	0.17	0.4	V
14	Analog R Input	V_{14}	—	—	1.8	2.5	3.2	V
15	Analog G Input	V_{15}	—	—	1.8	2.5	3.2	V
16	Analog B Input	V_{16}	—	—	1.8	2.5	3.2	V
17	RGB V_{CC}	V_{17}	—	Supply 9 V	—	9.0	—	V
18	R Output	V_{18}	—	—	2.30	2.65	3.00	V
19	G Output	V_{19}	—	—	2.30	2.65	3.00	V
20	B Output	V_{20}	—	—	2.30	2.65	3.00	V
21	ABCL	V_{21}	—	—	5.70	6.05	6.30	V
26	SCL	V_{26}	—	—	4.5	5.0	5.5	V
27	SDA	V_{27}	—	—	4.5	5.0	5.5	V
28	H.V_{CC}	V_{26}	—	Supply 9 V	—	9.0	—	V
29	Killer Status Output / fsc CW Output	V_{29}	—	—	1.40	1.75	2.00	V
35	Video SW Output	V_{35}	—	—	1.90	2.15	2.50	V
36	Digital V_{DD}	V_{36}	—	Supply 5 V	—	5.0	—	V
39	Y Input	V_{39}	—	—	2.5	2.8	3.2	V
40	H.AFC	V_{40}	—	—	6.0	6.8	7.5	V
41	Ext. Video / Y Input	V_{41}	—	Video SW : 01	2.7	3.0	3.4	V
43	TV Video Input	V_{43}	—	Video SW : 00	2.7	3.0	3.4	V
44	Black Detection	V_{44}	—	—	2.00	2.25	2.60	V
45	Ext. C Input	V_{45}	—	—	2.7	3.0	3.4	V
46	Y / C V_{CC}	V_{46}	—	Supply 5 V	—	5.0	—	V
47	PIF Det. Output	V_{47}	—	—	4.8	5.3	5.8	V
48	Loop Filter	V_{48}	—	—	4.1	4.6	5.1	V

PIN No.	PIN NAME	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
50	PIF VCO	V_{50}	—	—	7.4	8.0	8.6	V
51	PIF VCO	V_{51}	—	—	7.4	8.0	8.6	V
52	VCO V_{CC}	V_{52}	—	Supply 9 V	—	9.0	—	V
53	Limiter Input / Curre Correction	V_{53}	—	—	3.9	4.5	5.1	V
54	Ripple Filter	V_{54}	—	—	5.2	5.9	6.6	V
55	Ext. Audio Input	V_{55}	—	—	3.8	4.4	5.0	V

Current dissipation

PIN No.	PIN NAME	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
3	IF V_{CC}	I_{CC3}	—	Supply 9 V	8.5	15	19	mA
17	RGB V_{CC}	I_{CC17}	—	Supply 9 V	8.5	12	14	mA
28	H.V_{CC}	I_{CC26}	—	Supply 9 V	12	16	22	mA
36	Digital V_{CC}	I_{CC36}	—	Supply 5 V	7	12	15	mA
46	Y / C V_{CC}	I_{CC46}	—	Supply 5 V	45	65	76	mA
52	VCO V_{CC}	I_{CC52}	—	Supply 9 V	15.5	23	29	mA

RECOMMENDED OPERATING POWER SUPPLY VOLTAGE

PIN No.	PIN NAME	MIN.	TYP.	MAX.	UNIT	NOTE
3	IF V_{CC}	8.5	9	9.5	V	—
17	RGB V_{CC}	8.5	9	9.5	V	—
28	H.V_{CC}	8.5	9	9.5	V	—
36	Digital V_{CC}	4.5	5	5.5	V	—
46	Y / C V_{CC}	4.5	5	5.5	V	The thermal drift of the Y / C V_{CC} should be less than 50 mV. Because the amplitude of V-Ramp depends on this DC Voltage.
52	VCO V_{CC}	8.5	9	9.5	V	—

AC CHARACTERISTIC

PIF stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
PIF Det. Output Level	87.5%	V_{DET875}	—	P_1	2.0	2.2	2.4	V_{p-p}
	110%	V_{DET110}			2.0	2.5	3.0	
PIF Input Sensitivity	MIN.	$E_{PIFINMIN}$	—	P_2	—	37	—	dBμV
	MAX.	$E_{PIFINMAX}$			100	107	—	
IF AGC Range		ΔE_{IFAGC}			65	70	—	dB
PIF Det. Sync. Tip Level		V_{SYNC}	—	P_3	2.6	2.9	3.2	V
Output Level for No Input		V_{NOIF}	—	P_4	4.8	5.2	5.6	V
Differential Gain		DG	—	P_5	—	2	5	%
Differential Phase		DP			—	2	5	°
PIF Output Freq. Response		FR_{DET}	—	P_6	5	7	—	MHz
S / N		S / N_{PIF}	—	P_7	52	55	—	dB
Intermodulation		I_{107}	—	P_8	42	45	—	dB
I_F AGC Voltage	MAX.	$V_{IFAGCMAX}$	—	P_9	7.3	7.5	—	V
	MIN.	$V_{FAGCMIN}$			—	3.8	—	
R_F AGC Voltage	MAX.	$V_{RFAGCMAX}$	—	P_{10}	—	9	—	V
	MIN.	$V_{RFAGCMIN}$			—	0.2	0.5	
R_F AGC Control Range		ΔE_{RFAGC}	—	P_{11}	35	—	—	dB
AFT Center Voltage		V_{AFTCEN}	—	P_{12}	—	2.5	—	V
AFT Voltage	MAX.	V_{AFTMAX}	—	P_{13}	4.4	4.8	5.2	V
	MIN.	V_{AFTMIN}			—	0.2	0.5	
AFT Sensitivity		μ_{AFT}	—	P_{14}	—	40	—	kHz / V
PIF VCO Control Sensitivity		β_{IFVCO}	—	P_{15}	—	1.5	—	MHz / V
PIF VCO Pull-In Range	High	F_{PIFINH}	—	P_{16}	1	1.5	—	MHz
	Low	F_{PIFINL}			1	1.5	—	

SIF & audio stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
FM Det. Output Level	4.5 MHz / N	V_{AUAC4N}	—	S_1	350	500	700	mV_{rms}
Audio Distortion	4.5 MHz / N	$D_{AUDIO}N$	—	S_2	—	0.3	1	%
Audio S / N	4.5 MHz / N	$S / N_{SIF}N$	—	S_3	52	58	—	dB
AMR		AMR	—	S_4	50	60	—	dB
Limiting Sensitivity		E_{LIM}	—	S_5	—	35	—	dBμV
Band Width (4.5 MHz / NTSC)	High	F_{AUH4N}	—	S_7	4.9	6.4	—	MHz
	Low	F_{AUL4N}			—	2.8	4	
Attenuator Gain	MAX.	G_{ATTMAX}	—	S_8	—	0	—	dB
	CEN.	G_{ATTCEN}			—	− 15	—	
	MIN.	G_{ATTMIN}			—	− 85	− 75	
Offset between TV / Ext		$V_{AUOFFSET}$	—	S_9	− 30	0	30	mV
DC Change by Volume		ΔV_{VOLDC}	—	S_{10}	—	—	100	mV

Video stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
TV Input Dynamic Range	DR_{TV}	—	V_1	1.2	1.4	—	V_{p-p}
External Input Dynamic Range	DR_{EXT}			1.2	1.4	—	V_{p-p}
TV Mode Gain	G_{TV}	—	V_2	5.2	6.0	6.4	dB
External Mode Gain	G_{EXT}			5.2	6.0	6.4	dB
AV SW Cross-Talk	CT_{SWTE}	—	V_3	—	−55	−50	dB
	CT_{SWET}			—	−55	−50	dB
Y Input Dynamic Range	DR_Y	—	V_4	1.1	1.3	—	V_{p-p}
Y Input Pedestal Clamp Voltage	V_{YCLP}	—	V_5	2.5	2.7	2.9	V
Y Delay Time	t_{YDEL}	—	V_6	500	550	600	ns
Brightness Chara.	V_{BRTMAX}	—	V_7	3.0	3.4	3.7	V
	V_{BRTCEN}			2.3	2.6	2.8	
	V_{BRTMIN}			1.6	1.9	2.1	
Brightness Data Sensitivity	ΔV_{BRT}			9.4	13.6	16.3	mV / bit
Uni-Color Chara. for Y	G_{UCYMAX}	—	V_8	10.2	11.6	13.2	dB
	G_{UCYCEN}			5.1	6.6	8.3	
	G_{UCYMIN}			−9.1	−6.9	−5.2	
Sub-Contrast Chara.	$G_{SCONMAX}$	—	V_9	1.8	2.3	2.8	dB
	$G_{SCONMIN}$			−3.0	−3.5	−4.0	
Sharpness Peaking Frequency	F_{SHP}	—	V_{10}	3.0	3.3	3.6	MHz
Sharpness Control Characteristics	G_{SHMAX}	—	V_{11}	7.0	12.0	15.0	dB
	G_{SHCEN}			2.0	5.0	7.0	
	G_{SHMIN}			−14.0	−11.0	−8.0	
Y Frequency Response	FR_Y	—	V_{12}	5.5	—	—	MHz
Black Expansion AMP Gain	G_{BLEX}	—	V_{13}	1.2	1.4	1.6	—
Black Expansion Start Point	V_{BLEX}			0.79	0.96	1.14	V
Black Peak Detection Level	V_{BLPD}	—	V_{14}	−50	0	50	mV
WPS Level	V_{WPS}	—	V_{15}	2.5	2.8	3.2	V_{p-p}
Chrome Trap Gain	G_{TRAP} 358	—	V_{16}	—	—	−20	dB
Half Tone Chara. for Y	G_{HTY}	—	V_{17}	−6.9	−6.0	−5.1	dB

Chroma stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
ACC Chara.	V_{ACCL}	—	C_1	—	20	30	mV_{p-p}
	V_{ACCH}			600	—	—	
TOF Chara. (3.58 MHz)	F_{0T358}	—	C_2	—	4.28	—	MHz
	Q_{T358}			—	2.0	—	—
BPF Chara. (3.58 MHz)	F_{0B358}			—	3.58	—	MHz
	Q_{B358}			—	2.0	—	—
C Delay Time	t_{CDEL}	—	C_3	550	600	650	ns
Delay Time Difference between Y / C	$\Delta t_{Y/C}$			– 60	0	60	ns
Color Chara.	G_{COLMAX}	—	C_4	6.93	8.15	9.37	dB
	G_{COLMIN}			—	—	– 20	
Uni-Color Chara. for C	G_{UCCMIN}	—	C_5	– 21.5	– 18.8	– 16.0	dB
Tint Chara. (3.58 MHz)	$\Delta\theta_{358MAX}$	—	C_6	30	38	46	deg
	$\Delta\theta_{358MIN}$			– 46	– 38	– 30	
Relative Amplitude (NTSC)	$V_{PR/B}$	—	C_7	0.6	0.7	0.8	—
	$V_{PG/B}$			0.25	0.31	0.37	
Relative Phase (NTSC)	θ_{PR-B}	—	C_8	86	91	96	deg
	θ_{PG-B}			232	240	245	
APC Pull-In Range (3.58 MHz)	F_{3APCP+}	—	C_9	350	500	—	Hz
	F_{3APCP-}			350	500	—	
APC Hold Range (3.58 MHz)	F_{3APCH+}			350	500	—	Hz
	F_{3APCH-}			350	500	—	
APC Control Sensitivity (3.58 MHz)	β_{358}	—	C_{10}	0.7	0.9	1.1	Hz / mV

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
NTSC ID Sensitivity (Normal Mode)	V_{NTIDON}	—	C_{11}	0.4	0.8	1.2	mV_{p-p}
	$V_{NTIDOFF}$			0.4	0.8	1.2	
NTSC ID Sensitivity (Low Mode)	$V_{NTIDLON}$			2	4	6	mV_{p-p}
	$V_{NTIDLOFF}$			2	4	6	
ID Output Level	V_{IDH}	—	C_{12}	2.9	3.2	3.5	V
	V_{IDL}			1.5	1.8	2.1	
fsc Continuous Wave Output Level	V_{CW}	—	C_{15}	0.35	0.50	0.70	V_{p-p}
Sub-Carrier Remain on RGB Output	V_{SCR}	—	C_{16}	0	20	40	mV_{p-p}
	V_{SCG}			0	20	40	
	V_{SCB}			0	20	40	
Half Tone Chara. for C	G_{HTC}	—	C_{17}	−6.9	−6.0	−5.1	dB
Freerun Frequency	f_{03}	—	C_{18}	−200	0	200	Hz

Text stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
V-BLK Pulse Output Level		V_{VBLK}	—	T_1	0.5	1.0	1.5	V
H-BLK Pulse Output Level		V_{HBLK}			0.5	1.0	1.5	V
RGB Output Black Level (0 IRE DC)		V_{BLACK}	—	T_2	2.35	2.60	2.85	V
RGB Output White Level (100 IRE AC)		V_{WHITE}	—	T_3	—	2.50	—	V_{p-p}
Cut-Off Voltage Variable Range		ΔV_{CUT+}	—	T_4	0.58	0.65	0.72	V
		ΔV_{CUT-}			− 0.72	− 0.65	− 0.58	
Drive Control Variable Range		G_{DR+}	—	T_5	3.0	3.5	4.0	dB
		G_{DR-}			− 6.0	− 5.5	− 5.0	
ABCL Control Voltage Range		V_{ABCLH}	—	T_6	5.9	6.0	6.1	V
		V_{ABCLL}			5.4	5.5	5.6	
ACL Gain		G_{ACL}			− 16.5	− 15	− 13.5	dB
ABL Point		V_{ABLP1}	—	T_7	− 0.06	− 0.01	0.04	V
		V_{ABLP2}			− 0.16	− 0.11	− 0.06	
		V_{ABLP3}			− 0.35	− 0.30	− 0.25	
		V_{ABLP4}			− 0.47	− 0.42	− 0.37	
ABL Gain		V_{ABLG1}	—	T_8	− 0.17	− 0.12	− 0.07	V
		V_{ABLG2}			− 0.42	− 0.37	− 0.32	
		V_{ABLG3}			− 0.69	− 0.64	− 0.59	
		V_{ABLG4}			− 0.79	− 0.74	− 0.69	
Analog RGB Dynamic Range		DR_{TX}	—	T_9	0.5	—	—	V_{p-p}
Analog RGB Contrast Control Characteristic	MAX.	G_{TXCMAX}	—	T_{10}	0.85	1.00	1.20	V_{p-p}
	CEN.	G_{TXCCEN}			0.50	0.59	0.71	
	MIN.	G_{TXCMIN}			0.08	0.10	0.12	
Analog RGB Brightness Control Characteristic	MAX.	$V_{TXBRMAX}$	—	T_{11}	3.0	3.4	3.7	V
	CEN.	$V_{TXBRCEN}$			2.3	2.6	2.8	
	MIN.	$V_{TXBRMIN}$			1.6	1.9	2.1	
Analog RGB Mode Switching Level		V_{YS}	—	T_{12}	0.6	0.8	1.0	V
Analog RGB Mode Transfer Characteristic		τR_{YS}	—	T_{13}	—	25	100	ns
		tPR_{YS}			—	30	100	
		τF_{YS}			—	10	100	
		tPF_{YS}			—	25	100	
Cross Talk from Analog RGB to TV		CT_{TX-TV}	—	T_{14}	—	− 55	− 50	dB
Cross Talk from TV to Analog RGB		CT_{TV-TX}	—	T_{15}	—	− 55	− 50	dB

CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
RGB Output Amplitude	R	V_{ROUT}	—	T_{19}	1.0	1.2	1.4	V_{p-p}
	G	V_{GOUT}			0.45	0.60	0.75	
	B	V_{BOUT}			2.0	2.2	2.4	
Half Tone Mode SW Level		V_{YM1}	—	T_{23}	0.6	0.8	1.0	V
Half Tone → Analog RGB Mode SW Level		V_{YM2}			2.2	2.4	2.6	
Half Tone Mode Transfer Characteristic		τR_{YM1}	—	T_{24}	—	25	100	ns
		tPR_{YM1}			—	30	100	
		τF_{YM1}			—	10	100	
		tPF_{YM1}			—	25	100	
Half Tone → Analog RGB Mode Transfer Characteristic		τR_{YM2}	—		—	25	100	ns
		tPR_{YM2}			—	30	100	
		τF_{YM2}			—	10	100	
		tPF_{YM2}			—	25	100	
RGB Output Voltage Axes Difference		ΔV_{bct}	—	T_{25}	—	0	40	mV
RGB Output Amplitude Axes Difference		ΔV_a	—	T_{26}	—	0	10	mV

1 H DL stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
1 H DL Dynamic Range (Direct)	DR_{BDR}	—	Pin 37~Pin 20	0.8	1.2	—	V
	DR_{RDR}		Pin 38~Pin 18	0.8	1.2	—	
1 H DL Dynamic Range (Delay)	DR_{BDL}	—	Pin 37~Pin 20	0.8	1.2	—	V
	DR_{RDL}		Pin 38~Pin 18	0.8	1.2	—	
1 H DL Dynamic Range (Direct + Delay)	DR_{BDRDL}	—	Pin 37~Pin 20	0.9	1.2	—	V
	DR_{RDRDL}		Pin 38~Pin 18	0.9	1.2	—	
Frequency Response (Direct)	FR_{BDR}	—	At 700 kHz	−3.0	−2.0	0.5	dB
	FR_{RDR}		At 700 kHz	−3.0	−2.0	0.5	
Frequency Response (Delay)	FR_{BDL}	—	At 700 kHz	−8.2	−6.5	−4.3	dB
	FR_{RDL}		At 700 kHz	−8.2	−6.5	−4.3	
AC Gain (Direct)	G_{BDR}	—	Pin 37~Pin 20	−2.0	−0.5	2.0	dB
	G_{RDR}		Pin 38~Pin 18	−2.0	−0.5	2.0	
AC Gain (Delay)	G_{BDL}	—	Pin 37~Pin 20	−2.4	−0.5	1.1	dB
	G_{RDL}		Pin 38~Pin 18	−2.4	−0.5	1.1	
Direct-Delay AC Gain Difference	$\Delta G_{BDR/DL}$	—	G_{BDR}-G_{BDL}	−1.0	0.0	1.0	dB
	$\Delta G_{RDR/DL}$		G_{RDR}-G_{RDL}	−1.0	0.0	1.0	
1 H Delay Time	T_{BDL}	—	Pin 37~Pin 20	63.7	64.0	64.4	μs
	T_{RDL}		Pin 38~Pin 18	63.7	64.0	64.4	

DEF stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
AFC Inactive Period	60 Hz	$T_{60AFCOFF}$	—	D_1	—	262-10	—	H
H-OUT Start Voltage		V_{HON}	—	D_2	4.8	5.3	5.9	V
H-OUT Pulse Duty		W_{HOUT}	—	D_3	38.5	40.5	42.5	%
H-OUT Freq. on AFC Stop Mode		$F_{HAFCOFF}$	—	D_4	15.585	15.734	15.885	kHz
Horizontal Free-Run Frequency	60 Hz	F_{H60FR}	—	D_5	15.585	15.734	15.885	kHz
Horizontal Freq. Variable Range	MAX.	F_{HMAX}	—	D_6	16.500	16.700	16.900	kHz
	MIN.	F_{HMIN}			14.700	15.000	15.300	
Horizontal Freq. Control Sensitivity		β_{HAFC}	—	D_7	2.0	2.5	3.0	Hz / mV
Horizontal Pull-In Range		F_{HPH}	—	D_8	500	—	—	Hz
		F_{HPL}			500	—	—	
H-OUT Voltage		V_{HOUTH}	—	D_9	4.0	4.4	4.8	V
		V_{HOUTL}			—	0.15	0.30	
Horizontal Freq. Dependence on V_{CC}		ΔF_{HVCC}	—	D_{10}	– 20	0	20	Hz / V
FBP Phase		PH_{FBP}	—	D_{11}	2.3	2.5	2.7	μs
H-Sync. Phase		PH_{HSYNC}			0.2	0.3	0.4	
Horizontal Position Variable Range		ΔPH_{HPOS}	—	D_{12}	5.5	6.0	6.5	μs
AFC-2 Pulse Threshold Level		V_{AFC2}	—	D_{13}	3.1	3.5	3.9	V
H-BLK Pulse Threshold Level		V_{HBLK}		D_{14}	0.8	1.1	1.4	
Black Peak Det. Stop Period (H)		PH_{BPDET}	—	D_{15}	7.5	8.0	8.5	μs
		W_{BPDET}			13.0	13.5	14.0	
Clamp Pulse Start Phase		PH_{CP}	—	D_{16}	2.8	3.0	3.2	μs
Clamp Pulse Width		W_{CP}			5.6	5.8	6.0	
Gate Pulse Start Phase		PH_{GP}	—	D_{17}	2.7	2.9	3.1	μs
Gate Pulse Width		W_{GP}			1.8	2.0	2.2	
Sync. Output Low Level		V_{SYNCL}	—	D_{18}	0.0	0.3	0.5	V
Vertical Oscillation Start Voltage		V_{VON}	—	D_{19}	4.7	5.0	5.3	V
Vertical Free-Run Frequency	60 Hz	F_{V60FR}	—	D_{20}	48	53	58	Hz
Gate Pulse V-Masking Period	60 Hz	T_{60GPM}	—	D_{21}	—	261-10	—	H
V.Ramp DC on Service Mode		$V_{NOVRAMP}$	—	D_{22}	3.0	3.2	3.4	V
Vertical Pull-In Range (60 Hz)		F_{VP60L}	—	D_{23}	—	224.5	—	H
		F_{VP60H}			—	297	—	

CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Vertical Period on Fixed Mode		T_{V263}	—	D_{24}	—	263	—	H
V-BLK Start Phase	60 Hz	PH_{60VBLK}	—	D_{25}	44	46	48	μs
V-BLK Width	60 Hz	W_{60VBLK}			—	21	—	H
Picture Mute Period	60 Hz	W_{60PM}	—	D_{26}	—	257-28	—	H
Sand Castle Pulse Level		V_{SCPH}	—	D_{27}	7.70	8.00	8.30	V
		V_{SCPM}			4.00	4.30	4.60	
		V_{SCPL}			2.25	2.55	2.85	
Vertical Ramp Amplitude		V_{VRAMP}	—	D_{28}	1.50	1.67	1.83	V_{p-p}
Vertical AMP Gain		G_{VAMP}	—	D_{29}	22	25	28	dB
Vertical AMP Max. Output Level		V_{VOMAX}			2.5	3.0	3.5	V
Vertical AMP Min. Output Level		V_{VOMIN}			—	0.0	0.3	V
Vertical AMP Min. Output Current		I_{VOMAX}	—	D_{30}	11	14	17	mA
Vertical NFB Amplitude		V_{NFB}	—	D_{31}	1.50	1.67	1.83	V_{p-p}
Vertical Amplitude Variable Range		ΔV_{VRAMPH}			36	40	44	%
		ΔV_{VRAMPL}			−44	−40	−36	
Vertical Linearity Variable Range		ΔV_{LIN1+}	—	D_{32}	−17	−14	−11	%
		ΔV_{LIN1-}			13	16	19	
		ΔV_{LIN2+}			14.5	17.5	20.5	
		ΔV_{LIN2-}			−23	−20	−17	
Vertical S Correction Variable Range		ΔV_{S1+}	—	D_{33}	−14	−12	−10	%
		ΔV_{S1-}			10	12	14	
		ΔV_{S2+}			−18	−15	−12	
		ΔV_{S2-}			12	15	18	
V-AGC Current		I_{VAGCH}	—	D_{34}	440	550	660	μA
		I_{VAGCL}			100	120	140	μA
Vertical Guard Voltage		V_{VG}	—	D_{35}	1.80	2.00	2.20	V
BGP Phase		ΔBGP	—	D_{36}	1.45	1.50	1.55	μs

TEST CONDITION

PIF stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
P1	PIF Det. Output Level / V_{DET875} / V_{DET110}	RF AGC : except 0 PIF VCO : adjust V_i Pol : 0 Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, 87.5% modulated while signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Measure the amplitude of PIF det. output at Pin 47 (V_i Pol : 0), that is "V_{DET875}". (4) Input a 38.9 MHz, 90 dB μV, 87.5% modulated L-SECAM white signal into Pin 6. (5) Input a 38.9 MHz, 90 dBμV, 110% modulated white signal into Pin 6. (6) Measure the amplitude of PIF det. output at Pin 47 (V_i Pol : 0), that is "V_{DET110}".
P2	PIF Input Sensitivity / $E_{PIFINMIN}$ / $E_{PIFINMAX}$	RF AGC : except 0 PIF VCO : adjust Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, 87.5% modulated white signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Decreasing the IF input level, measure the input level at which PIF det. output amplitude turns to be – 3 dB against V_{DET875} that is "$E_{PIFINMIN}$". (4) Increasing the IF input level, measure the input level at which PIF det. output amplitude turns to be – 0.5 dB against V_{DET875} that is "$E_{PIFINMAX}$". (5) Calculate ; "ΔE_{IFAGC}" = $E_{PIFINMAX}$ – $E_{PIFINMIN}$
	IF AGC Range / ΔE_{IFAGC}		
P3	PIF Det. Sync. Tip Level / V_{SYNC}	RF AGC : except 0 PIF VCO : adjust V_i Pol : 0 Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, non-modulation signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Measure the DC level at Pin 47 (V_i Pol : 0), that is "V_{SYNC}".

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
P4	Output Level for No Input / V_{NOIF}	RF AGC : except 0 V_i Pol : 0 Others : Preset	(1) Connect Pin 6 / 7 to GND. (2) Supply 3.0 V to Pin 9. (3) Measure the DC level at Pin 47 (V_i Pol : 0), that is "V_{NOIF}".
P5	Differential Gain / DG Differential Phase / DP	RF AGC : except 0 PIF VCO : adjust Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, 87.5% modulated video signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Measure "DG" & "DP" for Pin 47 output.
P6	PIF Output Freq. Response / FR_{DET}	RF AGC : except 0 PIF VCO : adjust Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, 87.5% modulated sweep video signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Measure the Pin 9 DC level and fix it on that value. (4) For PIF det. output signal, measure the frequency at which the amplitude (Without sync) turns to be – 3 dB against the one for 10 kHz, that is "FR_{DET}"
P7	S / N / S/N_{PIF}	RF AGC : except 0 PIF VCO : adjust Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, non- modulation signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Measure the amplitude of PIF det. output, that is V_N. (4) Calculate ; "S/N_{PIF}" = 20*ℓog (V_{DET875} / V_N)
P8	Intermodulation / I_{107}	RF AGC : except 0 PIF VCO : adjust Others : Preset	(1) Input a signal composed of following 3 signals into Pin 6; 38.9 MHz / 90 dBμV, 34.47 MHz / 84 dBμV 33.4 MHz / 84 dBμV (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Adjust Pin 9 voltage so that the bottom of PIF det. output is equal to V_{SYNC}. (4) Measure the 1.07 MHz level against the 4.43 MHz level (= 0 dB), that is "I_{107}"

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
P9	IF AGC Voltage / $V_{IFAGCMAX}$ / $V_{IFAGCMIN}$	RF AGC : except 0 PIF VCO : adjust Others : Preset	(1) Connect Pin 6 / 7 to GND. (2) Measure the Pin 9 voltage, that is "$V_{IFAGCMAX}$". (3) Input a 38.9 MHz, 107 dBμV, non-modulation signal into Pin 6. (4) Adjust PIF VCO so that the AFT voltage is 2.5 V. (5) Measure the Pin 9 voltage, that is "$V_{IFAGCMIN}$".
P10	RF AGC Voltage / $V_{RFAGCMIN}$ / $V_{RFAGCMAX}$	RF AGC : adjust PIF VCO : adjust Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, non-modulation signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Adjust RF AGC so that the Pin 9 voltage is 4.5 V. (4) Increase the IF input level to 107 dBμV. (5) Measure the Pin 8 voltage, that is "$V_{RFAGCMIN}$" (6) Connect Pin 6 / 7 to GND. (7) Measure the Pin 8 voltage, that is "$V_{RFAGCMAX}$"
P11	RF AGC Control Range / ΔE_{RFAGC}	RF AGC : 1 / 63 PIF VCO : adjust Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, non-modulation signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Set RF AGC to 1. (4) Decreasing the IF input level, measure the input level at which the Pin 8 voltage is 4.5 V, that is $E_{RFAGCMIN}$. (5) Set RF AGC to 63. (6) Increasing the IF input level, measure the input level at which the Pin 8 voltage is 4.5 V, that is $E_{RFAGCMAX}$. (7) Calculate ; "ΔE_{RFAGC}" = $E_{RFAGCMAX}$ − $E_{RFAGCMIN}$
P12	AFT Center Voltage / V_{AFTCEN}	RF AGC : except 0 Others : Preset	(1) Connect Pin 6 / 7 to GND. (2) Supply 3 V to Pin 9. (3) Measure the Pin 4 voltage, that is "V_{AFTCEN}".

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
P13	AFT Voltage / V_{AFTMAX} / V_{AFTMIN}	RF AGC : except 0 PIF VCO : adjust Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, 87.5% modulated video signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Input a 37.9 MHz, 90 dBμV, 87.5% modulated video signal into Pin 6. (4) Measure the Pin 4 voltage, that is "V_{AFTMAX}" (5) Input a 39.9 MHz, 90 dBμV, 87.5% modulated video signal into Pin 6. (6) Measure the Pin 4 voltage, that is "V_{AFTMIN}"
P14	AFT Sensitivity / μ_{AFT}	RF AGC : except 0 PIF VCO : adjust Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, non-modulation signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) When changing the input frequency to $\pm$20 kHz, measure the change of Pin 4 voltage, that is ΔV_{AFT}. (4) Calculate ; "μ_{AFT}" = 40 / ΔV_{AFT}
P15	PIF VCO Control Sensitivity / β_{IFVCO}	RF AGC : except 0 PIF VCO : adjust Others : Preset	(1) Input a 38.9 MHz, 90 dBμV, non- modulation signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Measure the Pin 48 voltage, that is $V_{LOOP389}$. (4) Input a 38.7 MHz, 90 dBμV, non-modulation signal into Pin 6. (5) Measure the Pin 48 voltage, that is $V_{LOOP387}$. (6) Calculate ; "β_{IFVCO}" = 0.2 / ($V_{LOOP387}$ – $V_{LOOP389}$)
P16	PIF VCO Pull-In Range / F_{PIFINH} / F_{PIFINL}	RF AGC : except 0 PIF VCO : adjust Others : Preset	(1) Input a 45 MHz, 90 dBμV, 87.5% modulated video signal into Pin 6. (2) Adjust PIF VCO so that the AFT voltage is 2.5 V. (3) Dcreasing the input frequency, measure the frequency at which detected video signal appears on Pin 47, that is "F_{PIFINH}" (4) Input a 30 MHz, 90 dBμV, 87.5% modulated video signal into Pin 6. (5) Increasing the input frequency, measure the frequency at which detected video signal appears on Pin 47, that is "F_{PIFINL}"

SIF & audio stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
S1	FM Det. Output Level / V_{AUAC4N}	Audio ATT : 127 Au Gain : 1 AF-G : 1 Others : Preset	(1) Input a 4.5 MHz, 90 dBμV FM signal (Modulate 400 Hz with 25 kHz deviation) into Pin 53. (2) Measure the output amplitude at Pin 2, that is "V_{AUAC4N}". (Au Gain : 1, AF-G : 1)
S2	Audio Distortion / D_{AUDIO}	Audio ATT : 127 Others : Preset	(1) Input a 4.5 MHz, 90 dBμV FM signal (Modulate 400 Hz with 25 kHz deviation) into Pin 53. (2) Measure the distortion of Pin 2 output, that is "D_{AUDIO}N".
S3	Audio S / N / S/N_{SIF}	Audio ATT : 127 Others : Preset	(1) Input a 4.5 MHz, 90 dBμV non-modulation signal into Pin 53. (2) Measure the output amplitude at Pin 2, that is $V_{NOAUACN}$. (3) Calculate ; "S/N_{SIF}N" = 20*ℓog (V_{AUAC5P} / $V_{NOAUACN}$)

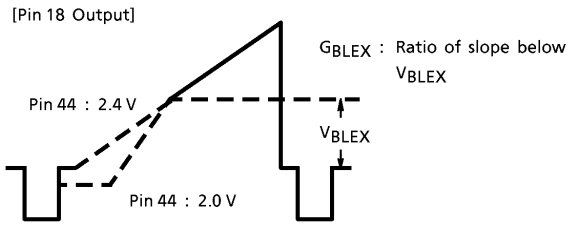
NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
S4	AMR / AMR	Audio ATT : 127 Others : Preset	(1) Input a 4.5 MHz, 90 dBμV AM signal (Modulate 400 Hz with 30%) into Pin 53. (2) Measure the output amplitude at Pin 2, that is V_{AMAU}. (3) Calculate ; "AMR" = 20*ℓog (V_{AUAC5N} / V_{AMAU})
S5	Limiting Sensitivity / E_{LIM}	Audio ATT : 127 Others : Preset	(1) Input a 4.5 MHz, 90 dBμV FM signal (Modulate 400 Hz with 50 kHz deviation) into Pin 53. (2) Decreasing the input level, measure the input level at which Pin 2 output amplitude turns to be – 3 dB against V_{AUAC5N}, that is "E_{LIM}".
S7	Band Width (4.5 MHz / NTSC) / F_{AUH4N} / F_{AUL4N}	Audio ATT : 127 Au Gain : 1 AF-G : 1 Others : Preset	(1) Input a 4.5 MHz, 90 dBμV FM signal (Modulate 400 Hz with 25 kHz deviation) into Pin 53. (2) Increasing the input frequency, measure the frequency at which Pin 2 output turns to be – 3 dB against V_{AUAC4N}, that is "F_{AUH4N}". (3) Decreasing the input frequency, measure the frequency at which Pin 2 output turns to be – 3 dB against V_{AUAC4N}, that is "F_{AUL4N}".

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
S8	Attenuator Gain / G_{ATTMAX} / G_{ATTCEN} / G_{ATTMIN}	Audio ATT : 0 / 64 / 127 Au SW : 1 Others : Preset	(1) Input a 1 MHz, 500 mV_{rms} signal into Pin 55. (2) Set Audio ATT to 0 / 64 / 127 and measure the Pin 2 output amplitude for each bus data, that is V_{ATTMAX} / V_{ATTCEN} / V_{ATTMIN}. (3) Calculate ; "G_{ATTMAX}" = 20*ℓog (V_{ATTMAX} / 500) "G_{ATTCEN}" = 20*ℓog (V_{ATTCEN} / 500) "G_{ATTMIN}" = 20*ℓog (V_{ATTMIN} / 500)
S9	Offset between TV / Ext. / $V_{AUOFFSET}$	Audio ATT : 127 Au SW : 0 / 1 Others : Preset	(1) Input a 4.5 MHz, 90 dBμV non-modulation signal into Pin 53. (2) Connect Pin 55 to GND via a 4.7 μF capacitor. (3) Switching Au SW to 0 / 1 and measure the change of Pin 2 DC level, that is "$V_{AUOFFSET}$".
S10	DC Change by Volume / ΔV_{VOLDC}	Audio ATT : 0 / 127 Au SW : 1 Others : Preset	(1) Connect Pin 55 to GND via a 4.7 μF capacitor. (2) Switching Audio ATT to 0 / 127 and measure the change of Pin 2 DC level, that is "ΔV_{VOLDC}".

Video stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
V1	TV Input Dynamic Range / DR_{TV} External Input Dynamic Range / DR_{EXT}	Video SW : 00 / 01 Others : Preset	(1) Input a white signal with sync into Pin 41 & 43. (2) Increasing the input amplitude, measure the amplitude (Include sync) at which the Pin 35 output is clipped, that is "DR_{TV}" (Video SW : 00) / "DR_{EXT}" (Video SW : 01)
V2	TV Mode Gain / G_{TV} Ext. Mode Gain / G_{EXT}	Video SW : 00 / 01 Others : Preset	(1) Input a 1 V_{p-p}, white signal with sync into Pin 41 & 43. (2) Set Video SW to 00 and measure the gain between Pin 43 and Pin 35, that is "G_{TV}" (3) Set Video SW to 01 and measure the gain between Pin 41 and Pin 35, that is "G_{EXT}"
V3	AV SW Cross-Talk / CT_{SWTE} / CT_{SWET}	Video SW : 00 / 01 Others : Preset	(1) Input a NTSC red signal with sync into Pin 43 and connect Pin 41 to GND via a 1 μF capacitor. (2) Set Video SW 01, measure the amplitude of 3.58 MHz signal at Pin 35 and calculate the cross-talk, that is "CT_{SWTE}". (3) Input a red signal into Pin 41 and connect Pin 43 to GND via a 1 μF capacitor. (4) Set Video SW 00, measure the amplitude of 3.58 MHz signal at Pin 35 and calculate the cross-talk, that is "CT_{SWET}".
V4	Y Input Dynamic Range / DR_Y	Uni-Color : 32 Brightness : 0 Color : 0 Others : Preset	(1) Input a white signal with sync into Pin 43 & 39. (2) Increasing the Pin 39 input amplitude, measure the amplitude (include sync) at which the Pin 18 output is clipped, that is "DR_Y".
V5	Y Input Pedestal Clamp Voltage / V_{YCLP}	All : Preset	(1) Input a composite sync signal into Pin 43. (2) Connect Pin 39 to GND via a 1 μF capacitor. (3) Measure the DC Voltage at Pin 39, that is "V_{YCLP}".
V6	Y Delay Time / t_{YDEL}	Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 2T pulse with sync into Pin 43 & 39. (2) Observe the Pin 18 output, measure the delay time between Pin 39 and Pin 18, that is "t_{YDEL}".

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
V7	Brightness Characteristics / V_{BRTMAX} / V_{BRTCEN} / V_{BRTMIN} Brightness Data Sensitivity / ΔV_{BRT}	Brightness : 0 / 64 / 127 Color : 0 Others : Preset	(1) Input a 0 IRE black signal with sync into Pin 43 & 39. (2) Measure the DC level of picture period at Pin 18 for Brightness : 127 / 64 / 0, that is "V_{BRTMAX}" / "V_{BRTCEN}" / "V_{BRTMIN}". (3) Calculate ; "ΔV_{BRT}" = ($V_{BRTMAX} - V_{BRTMIN}$) / 127
V8	Uni-Color Characteristics for Y / G_{UCYMAX} / G_{UCYCEN} / G_{UCYMIN}	Uni-Color : 0 / 32 / 63 Color : 0 Others : Preset	(1) Input a 50 IRE (0.357V) white signal with sync into Pin 43 & 39. (2) Measure the output picture amplitude at Pin 18 for Uni-Color 63 / 32 / 0, that is V_{UCYMAX} / V_{UCYCEN} \ V_{UCYMIN}. (3) Calculate ; "G_{UCYMAX}" = 20*ℓog (V_{UCYMAX} / 0.357) "G_{UCYCEN}" = 20*ℓog (V_{UCYCEN} / 0.357) "G_{UCYMIN}" = 20*ℓog (V_{UCYMIN} / 0.357)
V9	Sub-Contrast Characteristics / $G_{SCONMAX}$ / $G_{SCONMIN}$	Sub-Contrast : 0 / 8 / 15 Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 50 IRE white signal with sync into Pin 43 & 39. (2) Measure the output picture amplitude at Pin 18 for Sub-Contrast 15 / 8 / 0, that is $V_{SCONMAX}$ / $V_{SCONCEN}$ / $V_{SCONMIN}$. (3) Calculate ; "$G_{SCONMAX}$" = 20*ℓog ($V_{SCONMAX}$ / $V_{SCONCEN}$) "$G_{SCONMIN}$" = 20*ℓog ($V_{SCONMIN}$ / $V_{SCONCEN}$)
V10	Sharpness Peaking Frequency / F_{SHP}	Sharpness : 63 Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 0.5 V_{p-p} sweep signal with sync into Pin 43 & 39. (2) Measure the frequency at which the Pin 18 output amplitude is Max., that is "F_{SHP}".
V11	Sharpness Control Characteristics / G_{SHMAX} / G_{SHCEN} / G_{SHMIN}	Sharpness : 0 / 32 / 63 Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 0.5 V_{p-p} sweep signal with sync into Pin 43 & 39. (2) Measure the output picture amplitude for 100 kHz at Pin 18, that is V_{SH100k}. (3) Measure the output picture amplitude for F_{SHP} when Sharpness is max., center and min., that is V_{SHMAX}, V_{SHCEN} and V_{SHMIN}. (4) Calculate ; "G_{SHMAX}" = 20*ℓog (V_{SHMAX} / V_{SH100k}) "G_{SHCEN}" = 20*ℓog (V_{SHCEN} / V_{SH100k}) "G_{SHMIN}" = 20*ℓog (V_{SHMIN} / V_{SH100k})

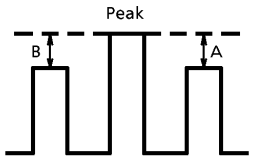
NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
V12	Y Frequency Response / FR_Y	Uni-Color : 63 Sharpness : Adjust Color : 0 Others : Preset	(1) Input a 0.5 V_{p-p} sweep signal with sync into Pin 43 & 39. (2) Adjust Sharpness so that the output amplitude for F_{SHP} equals V_{SH100k}. (3) Measure the frequency at which the output amplitude is 3 dB down against V_{SH100k}, which is "FR_Y".
V13	Black Expansion Start Point / V_{BLEX} Black Expansion AMP Gain / G_{BLEX}	Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 100 IRE ramp signal with sync into Pin 43 & 39. (2) Supply 2.4 V / 2.0 V to Pin 44 and observe the Pin 18 output. (3) Measure "V_{BLEX}" and "G_{BLEX}". 
V14	Black Peak Detection Level / ΔV_{BLPD}	Others : Preset	(1) Input a composite sync signal into Pin 43. (2) Increasing the Pin 39 DC level, measure the level at which the Pin 44 voltage drops down, that is V_{BLPD}. (3) Calculate ; "ΔV_{BLPD}" = V_{BLPD} – V39
V15	WPS Level / V_{WPS}	Uni-Color : 63 Brightness : 127 Color : 0 Others : Preset	(1) Input a 100 IRE ramp signal with sync into Pin 43 & 39. (2) Measure the amplitude from cut-off level to peak (At which output signal is clipped), that is "V_{WPS}".
V16	Chroma Trap Gain / G_{TRAP} 358	C-Trap : 0 / 1 Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 0.5 V_{p-p}, 3.58 MHz signal with sync into Pin 43 & 39. (2) Measure the 3.58 MHz amplitude at Pin 18 for Chroma Trap : 1 / 0, that is V_{TRAPON} / $V_{TRAPOFF}$. (3) Calculate ; "$G_{TRAP358}$" = 20*ℓog (V_{TRAPON} / $V_{TRAPOFF}$)

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
V17	Half Tone Characteristics for Y / G_{HTY}	Half Tone : 0 / 1 Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 100 IRE white signal with sync into Pin 43 & 39. (2) Measure the output picture amplitude at Pin 18 for Half Tone : 1 / 0, that is V_{HTYON} / V_{HTYOFF}. (3) Calculate ; "G_{HTY}" = 20*ℓog (V_{HTYON} / V_{HTYOFF})

Chrome stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
C1	ACC Characteristics / V_{ACCH} / V_{ACCL}	Mute : 01 Uni-Color : 63 Others : Preset	(1) Input a 3.58 MHz NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Changing the amplitude of burst and chroma, measure the input amplitude at which Pin 20 output amplitude is + 1 dB / – 1 dB against the one for 300 mVp-p input, that is "V_{ACCH}" / "V_{ACCL}".
C2	TOF Characteristics (3.58 MHz) / F_{0T358} / Q_{T358} BPF Characteristics (3.58 MHz) / F_{0B358} / Q_{B358}	TEST : 01000111 C-BPF : 0 / 1 Color System : 101 Others : Preset	(1) Set C-BPF to 1 and Color System to 101. (2) Input a sweep signal into Pin 43. (3) Observe the frequency response at Pin 18 and measure the Peaking Frequency / Q of chroma filter, that is "F_{0T358}" / "Q_{T358}". (4) Set C-BPF to 0 and Color System to 101 and repeat (2) & (3), that is "F_{0B358}" / "Q_{B358}".
C3	C Delay Time / t_{CDEL} Delay Time Difference between Y / C / $\Delta t_{Y/C}$	Mute : 01 Uni-Color : 63 Others : Preset	(1) Input a 3.58 MHz, NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Observe the Pin 18 output, measure the delay time between Pin 43 and Pin 18, that is "t_{CDEL}". (3) Calculate ; "$\Delta t_{Y/C}$" = $t_{YDEL} - t_{CDEL}$
C4	Color Characteristics / G_{COLMAX} / G_{COLMIN}	Color : 0 / 64 / 127 Mute : 01 Uni-Color : 63 Others : Preset	(1) Input a 3.58 MHz NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Measure the Pin 18 amplitude for Color 127 / 64 / 0, that is V_{COLMAX} / V_{COLCEN} / V_{COLMIN}. (3) Calculate ; "G_{COLMAX}" = 20*ℓog (V_{COLMAX} / V_{COLCEN}) "G_{COLMIN}" = 20*ℓog (V_{COLMIN} / V_{COLCEN})

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
C5	Uni-Color Characteristics for C / G_{UCC}	Uni-Color : 0 / 63 Mute : 01 Others : Preset	(1) Input a 3.58 MHz, NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Measure the Pin 18 amplitude for Uni-Color 63 / 0, that is V_{UCCMAX} and V_{UCCMIN}. (3) Calculate ; "G_{UCC}" = 20*ℓog (V_{UCCMIN} / V_{UCCMAX})
C6	Tint Characteristics (3.58 MHz) / $\Delta\theta_{358MAX}$ / $\Delta\theta_{358MIN}$	Tint : 0 / 64 / 127 Mute : 01 Uni-Color : 63 Others : Preset	(1) Input a 3.58 MHz NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Set Tint to 64 and adjust the burst phase so that the 6th bar of Pin 20 output is maximum, that is θ_{358CEN}. (3) Change Tint to 127 / 0 and adjust the burst phase so that the 6th bar of Pin 20 output is maximum, that is θ_{358MAX} / θ_{358MIN}. (4) Calculate ; "$\Delta\theta_{358MAX}$" = $-(\theta_{358MAX} - \theta_{358CEN})$ "$\Delta\theta_{358MIN}$" = $-(\theta_{358MIN} - \theta_{358CEN})$
C7	Relative Amplitude (NTSC) / $V_{NR / B}$ / $V_{NG / B}$	Mute : 01 Uni-Color : 63 Others : Preset	(1) Input a 3.58 MHz NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Measure the amplitude of Pin 18 / 19 / 20 output, that is "V_{NROUT}" / "V_{NGOUT}" / "V_{NBOUT}" (3) Calculate ; "$V_{NR / B}$" = V_{NROUT} / V_{NBOUT} "$V_{NG / B}$" = V_{NGOUT} / V_{NBOUT}

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
C8	Relative Phase (NTSC) / θ_{NR-B} / θ_{NG-B}	Mute : 01 Uni-Color : 63 Others : Preset	(1) Input a 3.58 MHz NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Observe the Pin 18 / 19 / 20 output, measure the R / G / B modulation angle (θ_{NR} / θ_{NG} / θ_{NB}) accoeding following figure and equality. $\theta_{N*} = \theta_{0*} - \left\{ \tan^{-1} \left(\frac{1}{\frac{2A}{B} + \sqrt{3}} \right) - 15 \right\}$  For θ_{NR} ; Peak : 3rd bar, θ_{0R} = 90 For θ_{NG} ; Peak (Negative) : 4th bar, θ_{0G} = 240 For θ_{NB} ; Peak : 6th bar, θ_{0B} = 0 (3) Calculate ; "θ_{NR-B}" = θ_{NR} − θ_{NB} "θ_{NG-B}" = θ_{NG} − θ_{NB}

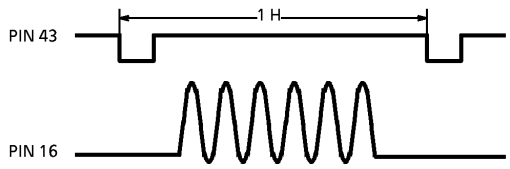
NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
C9	APC Pull-In Range (3.58 MHz) / ΔF_{3APCP+} / ΔF_{3APCP-}	Color System : 101 Others : Preset	(1) Input a 3.58 MHz NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Set Color System to 101 (358 NTSC). (3) For higher frequency than 3.58 MHz, measure the burst frequency at which Pin 29 DC level changes from low to high / from high to low, that is F_{3APCP+} / F_{3APCH+}. (4) For lower frequency than 3.58MHz, repeat (3), that is F_{3APCP-} / F_{3APCH-}. (5) Calculate ; "ΔF_{3APCP+}" = F_{3APCP+} − 3579545 "ΔF_{3APCP-}" = 3579545 − F_{3APCP-} "ΔF_{3APCH+}" = F_{3APCH+} − 3579545 "ΔF_{3APCH-}" = 3579545 − F_{3APCH-}
	APC Hold Range (3.58 MHz) / ΔF_{3APCH+} / ΔF_{3APCH-}		

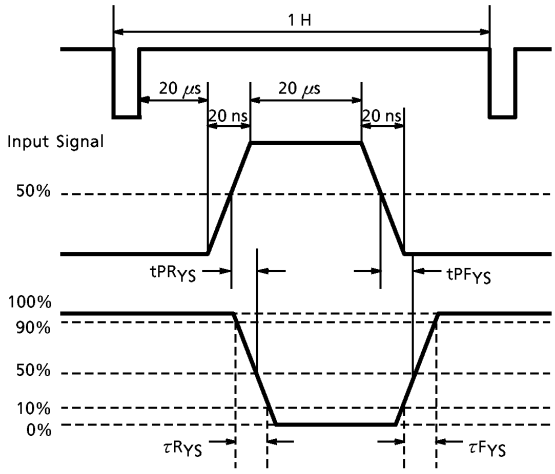
NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
C10	APC Control Sensitivity (3.58 MHz) / β_{358}	Color System : 101 Others : Preset	(1) Connect Pin 43 to GND via a 1 μF capacitor. (2) Set Color System to 101 (358NTSC). (3) Adjust Pin 10 voltage so that the Pin 29 output frequency is 3.579545MHz, that is $V_{3APCCEN}$. (4) Measure the Pin 29 output frequency when Pin 10 voltage is $V_{3APCCEN}$ + 100 mV / $V_{3APCCEN}$ − 100 mV, that is F_{3APC+} / F_{3APC-}. (5) Calculate ; "β_{358}" = (F_{3APC+} − F_{3APC-}) / 200
C11	NTSC ID Sensitivity (Normal Mode) / V_{NTIDON} / $V_{NTIDOFF}$ NTSC ID Sensitivity (Low Mode) / $V_{NTIDLON}$ / $V_{NTIDLOFF}$	ID SW : 0 / 1 Color System : 101 Mute : 01 Uni-Color : 63 Others : Preset	(1) Set ID SW to 0. (2) Set Color System to 101 (358NTSC). (3) Input a 3.58 MHz NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (4) Measure the burst amplitude at which Pin 29 DC level changes from low to high / from high to low, that is "V_{NTIDON}" / "$V_{NTIDOFF}$". (5) Set ID SW to 1, repeat (2)~(4), that is "$V_{NTIDLON}$", "$V_{NTIDLOFF}$".
C12	ID Output Level / V_{IDH} / V_{IDL}	All : Preset	(1) Input a 3.58 MHz NTSC color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Measure the center DC level of Pin 29 output, that is "V_{IDH}". (3) Connect Pin 43 to GND via a 1 μF capacitor and repeat (2), that is "V_{IDL}".

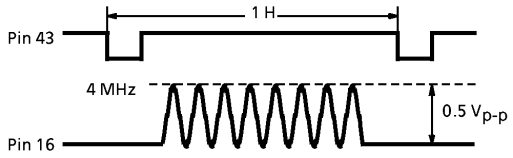
NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
C15	fsc Continuous Wave Output Level / V_{CW}	CW SW : 1 Others : Preset	Measure the amplitude of Pin 29 output, that is "V_{CW}".
C16	Sub-Carrier Remain on RGB Output / V_{SCR} / V_{SCG} / V_{SCB}	Mute : 01 Uni-Color : 63 Others : Preset	(1) Input a 3.58 MHz NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Measure the amplitude of 3.58 MHz signal at Pin 18 / 19 / 20, that is "V_{SCR}" / "V_{SCG}" / "V_{SCB}".
C17	Half Tone Characteristics for C / G_{HTC}	Half Tone : 1 Mute : 01 Uni-Color : 63 Others : Preset	(1) Input a 3.58 MHz NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into Pin 43. (2) Set Half Tone to 1 and measure the amplitude of Pin 20 output, that is V_{PBHTC}. (3) Calculate ; "G_{HTC}" = 20*ℓog (V_{PBHTC} / V_{PBOUT})
C18	Freerun Frequency / f_{03}	Color system : 101 Others : Preset	(1) Measure the output frequency at the Pin 29 for the mode of the color system : 3.58NTSC (101), that is "f_{03}".

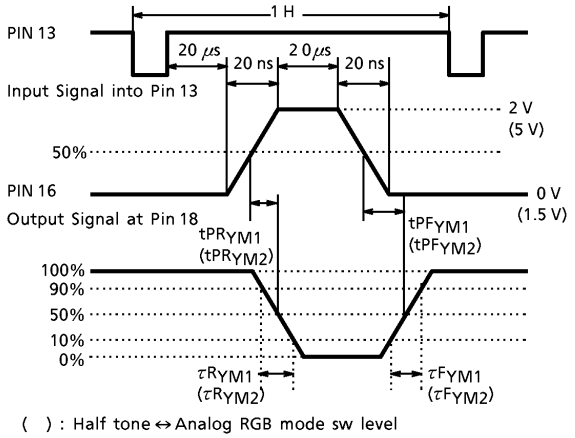
Text stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
T1	V-BLK Pulse Output Level / V_{VBLK} H-BLK Pulse Output Level / V_{HBLK}	All : Preset	(1) Input a composite sync signal into Pin 43. (2) Measure the DC level of V / H blanking period at Pin 20, that is "V_{VBLK}" / "V_{HBLK}".
T2	RGB Output Black Level (0 IRE DC) / V_{BLACK}	Color : 0 Others : Preset	(1) Input a 0 IRE Y signal with sync into Pin 43 & 39. (2) Measure the DC level of picture period at Pin 20, that is "V_{BLACK}".
T3	RGB Output White Level (100 IRE AC) / V_{WHITE}	Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 100 IRE Y signal with sync into Pin 43 & 39. (2) Measure the amplitude from 0 to 100 IRE at Pin 20, that is "V_{WHITE}".
T4	Cut-Off Voltage Variable Range / ΔV_{CUT+} / ΔV_{CUT-}	B Cut Off : 0 / 255 Color : 0 Others : Preset	(1) Input a 0 IRE Y signal with sync into Pin 43 & 39. (2) Measure the DC level of picture period at Pin 20 for B Cut-off : 255 / 0, that is V_{CUTMAX} / V_{CUTMIN}. (3) Calculate ; "ΔV_{CUT+}" = $V_{CUTMAX} - V_{BLACK}$ "ΔV_{CUT-}" = $V_{CUTMIN} - V_{BLACK}$
T5	Drive Control Variable Range / G_{DR+} / G_{DR-}	B Drive : 0 / 127 Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 100 IRE Y signal with sync into Pin 43 & 39. (2) Measure the amplitude from 0 to 100 IRE at Pin 20 for B drive 127 / 0, that is V_{DRMAX} / V_{DRMIN}. (3) Calculate ; "G_{DR+}" = 20*ℓog (V_{DRMAX} / V_{WHITE}) "G_{DR-}" = 20*ℓog (V_{DRMIN} / V_{WHITE})
T6	ABCL Control Voltage Range / V_{ABCLH} / V_{ABCLL} ACL Gain / G_{ACL}	ABL Gain : 11 Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 100 IRE Y signal with sync into Pin 43 & 39. (2) Decreasing the Pin 21 voltage, measure the voltage at which Pin 20 output begins / stops decreasing, that is V_{ABCLH}" / "V_{ABCLL}". (3) Measure the minimum amplitude of Pin 20 output, that is V_{ACLMIN}. (4) Calculate ; "G_{ACL}" = 20*ℓog (V_{ACLMIN} / V_{WHITE})

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
T7	ABL Start Point / V_{ABLP0} / V_{ABLP1} / V_{ABLP2} / V_{ABLP3}	ABL Start Point : 00 / 01 / 10 / 11 ABL Gain : 11 Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 0 IRE Y signal with sync into Pin 43 & 39. (2) For ABL Point 00 / 01 / 10 / 11, decreasing the Pin 21 voltage, measure the voltage the voltage at which Pin 20 output begins decreasing, that is V_{ABL1} / V_{ABL2} / V_{ABL3} / V_{ABL4}. (3) Calculate ; "V_{ABLP0}" = V_{ABL1} − V_{ABCLH} "V_{ABLP1}" = V_{ABL2} − V_{ABCLH} "V_{ABLP2}" = V_{ABL3} − V_{ABCLH} "V_{ABLP3}" = V_{ABL4} − V_{ABCLH}
T8	ABL Gain / V_{ABLG0} / V_{ABLG1} / V_{ABLG2} / V_{ABLG3}	ABL Gain : 00 / 01 / 10 / 11 Uni-Color : 63 Color : 0 Others : Preset	(1) Input a 0 IRE Y signal with sync into Pin 43 & 39. (2) For ABL Gain 00 / 01 / 10 / 11, measure the DC level of picture period at Pin 20 when Pin 21 voltage is V_{ABCLL}, that is V_{ABL5} / V_{ABL6} / V_{ABL7} / V_{ABL8}. (3) Calculate ; "V_{ABLG0}" = V_{ABL5} − V_{BLACK} "V_{ABLG1}" = V_{ABL6} − V_{BLACK} "V_{ABLG2}" = V_{ABL7} − V_{BLACK} "V_{ABLG3}" = V_{ABL8} − V_{BLACK}
T9	Analog RGB Dynamic Range / DR_{TX}	RGB Contrast : 32 Others : Preset	(1) Input a composite sync signal into Pin 43. (2) Supply 2 V to Pin 13. (3) Input a signal of following figure into Pin 16.  (4) Increasing the amplitude of Pin 16 input, measure the amplitude at which the Pin 20 amplitude stops increasing, that is "DR_{TX}".

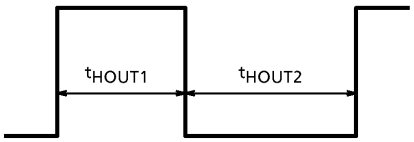
NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
T10	Analog RGB Contrast Control Characteristic / G_{TXCMAX} / G_{TXCCEN} / G_{TXCMIN}	RGB Contrast : 32 Others : Preset	(1) Input a composite sync signal into Pin 43. (2) Supply 2 V to Pin 13. (3) Input a signal of NOTE : T9 figure into Pin 16. (4) For RGB Contrast 63 / 32 / 0, measure the amplitude of Pin 20 output, that is V_{TXCMAX} / V_{TXCCEN} / V_{TXCMIN}. (5) Calculate ; "G_{TXCMAX}" = 20*ℓog (V_{TXCMAX} / 0.2) "G_{TXCCEN}" = 20*ℓog (V_{TXCCEN} / 0.2) "G_{TXCMIN}" = 20*ℓog (V_{TXCMIN} / 0.2)
T11	Analog RGB Brightness Control Characteristic / $V_{TXBRMAX}$ / $V_{TXBRCEN}$ / $V_{TXBRMIN}$	Brightness : 0 / 64 / 127 Others : Preset	(1) Supply 2 V to Pin 13. (2) Connect Pin 16 to GND via a 0.1μF capacitor. (3) For Brightness 127 / 64 / 0, measure the DC level of picture period at Pin 20, that is "$V_{TXBRMAX}$" / "$V_{TXBRCEN}$" / "$V_{TXBRMIN}$".
T12	Analog RGB Mode Switching Level / V_{YS}	RGB Contrast : 32 Others : Preset	(1) Input a composite sync signal into Pin 43. (2) Input a signal of NOTE : T9 figure into Pin 16. (3) Increasing the Pin 13 voltage, measure the voltage at which the signal inputted into Pin 16 appears at Pin 20, that is "V_{YS}".
T13	Analog RGB Mode Transfer Characteristic / τR_{YS} / tPR_{YS} / τF_{YS} / tPF_{YS}	All : Preset	(1) Input a 50 IRE Y signal with sync into Pin 43 & 39. (2) Connect Pin 16 to GND via a 0.1 μF capacitor. (3) According to following figure, measure the Analog RGB Mode Transfer Characteristic 

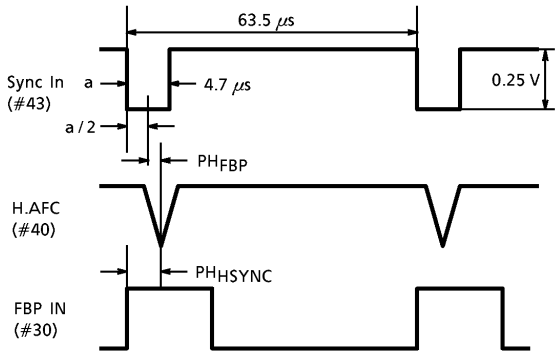
NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
T14	Cross Talk from Analog RGB to TV / CT_{TX-TV}	Uni-Color : 63 RGB Contrast : 63 Others : Preset	(1) Input a composite sync signal into Pin 43. (2) Connect Pin 39 to GND via a 1 μF capacitor. (3) Input a signal of following figure into Pin 16.  (4) Measure the amplitude of 4 MHz signal at Pin 20, that is V_{TX-TV}. (5) Calculate ; "CT_{TX-TV}" = 20*ℓog (V_{TX-TV} / 0.5)
T15	Cross Talk from TV to Analog RGB / CT_{TV-TX}	Uni-Color : 63 RGB Contrast : 63 Others : Preset	(1) Input a 4 MHz, 0.5 V_{p-p} Y signal with sync into Pin 43 & 39. (2) Connect Pin 16 to GND via a 0.1 μF capacitor. (3) Supply 2 V to Pin 13. (4) Measure the amplitude of 4 MHz signal at Pin 20, that is V_{TV-TX}. (5) Calculate ; "CT_{TV-TX}" = 20*ℓog (V_{TV-TX} / 0.5)
T19	RGB OUTPUT AMPLITUDE / V_{ROUT} / V_{GOUT} / V_{BOUT}	Mute : 01 Uni-Color : 63 Others : Preset	(1) Input a 3.58 MHz, NTSC rainbow color-bar (286 mV_{p-p}, burst : chroma = 1 : 1) with sync into pin 43. (2) Measure the amplitude of pin 18 / 19 / 20 output, that is "/ V_{ROUT} / V_{GOUT} / V_{BOUT}".

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
T23	Half tone mode SW level / V_{YM1} Half tone ↔ Analog RGB mode SW level / V_{YM2}	Half tone : 0 RGB Contrast : 32 Ym enb : 1 Others : Preset	(1) Input a composite sync signal into Pin 43. (2) Increasing the Pin 13 voltage, measure the voltage at which the picture portion amplitude at Pin 20 starts changing, that is "V_{YM1}". (3) Input a signal of Note : T9 figure into Pin 16. (4) Increasing the Pin 13 voltage further, measure the voltage at which the signal inputted into Pin 16 appears at Pin 20, that is "V_{YM2}".
T24	Half tone mode transfer characteristic / τR_{YM1} / tPR_{YM1} / τF_{YM1} / tPF_{YM1} Half tone ↔ Analog RGB mode transfer characteristic / τR_{YM2} / tPR_{YM2} / τF_{YM2} / tPF_{YM2}	Half tone : 0 Ym enb : 1 Others : Preset	(1) Input a 50 IRE Y signal with sync into pin 43 & 39, a signal as the figure below into Pin 13. (2) Connect Pin 16 to GND via a 0.1 μF capacitor. (3) According to following figure, measure the Half tone Mode transfer characteristic / Half tone ↔ Analog RGB mode transfer characteristic.  ( ) : Half tone ↔ Analog RGB mode sw level

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
T25	RGB output voltage Axes difference / ΔV_{bct}	Brightness : 32 Uni-color : 63 Color : 0 Others : Preset	(1) Input a 0 IRE black signal with sync into Pin 43 & 39. (2) Measure the DC level of picture period at Pin 18 / 19 / 20. (3) Find maximum axes difference, that is "ΔV_{bct}".
T26	RGB output amplitude Axes difference / ΔV_a	Mute : 00	(1) Input a sin wave (0.3 V_{p-p}, setup = 0.3 V) with sync into Pin 43 & 39. (2) Measure the amplitude of Pin 18 / 19 / 20 output. (3) Find maximum axes difference, that is "ΔV_a".

DEF stage (Unless otherwise specified, V_{CC} = 9 V (3, 17, 28 & 52 pin) / 5 V (36 & 46 pin), Ta = 25°C)

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
D1	AFC Inactive Period / $T_{60AFCOFF}$	All : Preset	(1) Input a 60 Hz composite sync signal into Pin 43. (2) Measure "$T_{60AFCOFF}$" at Pin 40. (cf. Fig.D1)
D2	H-OUT Start Voltage / V_{HON}	All : Preset	(1) Let Pin 3 / 17 / 52 / 36 / 46 be open. (2) Increasing Pin 28 voltage, measure the voltage at which H OUT pulse appears at Pin 32, that is "V_{HON}".
D3	H-OUT Pulse Duty / W_{HOUT}	All : Preset	(1) Measure t_{HOUT1} & t_{HOUT2} at Pin 32.  (2) Calculate ; "W_{HOUT}" = t_{HOUT1} / (t_{HOUT1} + t_{HOUT2}) *100
D4	H-OUT Freq. on AFC Stop Mode / $F_{HAFCOFF}$	AFC Gain : 11 Others : Preset	(1) Input a 60 Hz composite sync signal into Pin 43. (2) Measure the H OUT frequency at Pin 32, that is "$F_{HAFCOFF}$".
D5	Horizontal Free-Run Frequency / F_{H60FR}	V-Freq : 01 Others : Preset	For V-Freq 01, measure the H OUT frequency at Pin 32, that is "F_{H60FR}"
D6	Horizontal Freq. Variable Range / F_{HMAX} / F_{HMIN}	All : Preset	(1) Connect Pin 40 to V_{CC} via a 10 kΩ and measure the H OUT frequency at Pin 32, that is "F_{HMAX}". (2) Connect Pin 40 to GND via a 68 kΩ and measure the H OUT frequency at Pin 32, that is "F_{HMIN}".
D7	Horizontal Freq. Control Sensitivity / β_{HAFC}	All : Preset	(1) Measure the Pin 40 voltage at which H OUT frequency is 15.734 kHz, that is V_{H15734}. (2) Measure the H OUT frequency when Pin 40 voltage is V_{H15734} + 50 mV / V_{H15734} − 50 mV, that is F_{HHIGH} / F_{HLOW}. (3) Calculate ; "β_{HAFC}" = (F_{HHIGH} − F_{HLOW}) / 100

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
D8	Horizontal Pull-in Range / ΔF_{HPH} / ΔF_{HPL}	All : Preset	(1) Input a composite sync signal into Pin 43. (2) Decreasing the horizontal frequency from 17 kHz, measure the frequency at which H OUT synchronized with Sync Out (Pin 31), that is F_{HPH}. (3) Increasing the horizontal frequency from 14 kHz, measure the frequency at which H OUT synchronized with Sync Out (Pin 31), that is F_{HPL}. (4) Calculate ; "ΔF_{HPH}" = F_{HPH} − 15734 "ΔF_{HPL}" = 15625 − F_{HPL}
D9	H-OUT Voltage / V_{HOUTH} / V_{HOUTL}	All : Preset	(1) Measure the high level of H OUT at Pin 32, that is "V_{HOUTH}". (2) Measure the Low level of H OUT at Pin 32, that is "V_{HOUTL}".
D10	Horizontal Freq. Dependence on V_{CC} / ΔF_{HVCC}	All : Preset	(1) Measure the H OUT frequency when H V_{CC} is 8.5 V / 9.5 V, that is F_{HVCCH} / F_{HVCCL}. (2) Calculate ; "ΔF_{HVCC}" = (F_{HVCCH} − F_{HVCCL}) / 1
D11	FBP Phase / PH_{FBP} H-Sync. Phase / PH_{HSYNC}	All : Preset	(1) Input a composite sync signal into Pin 43. (2) According to the following figure, measure "PH_{FBP}" & "PH_{HSYNC}". 

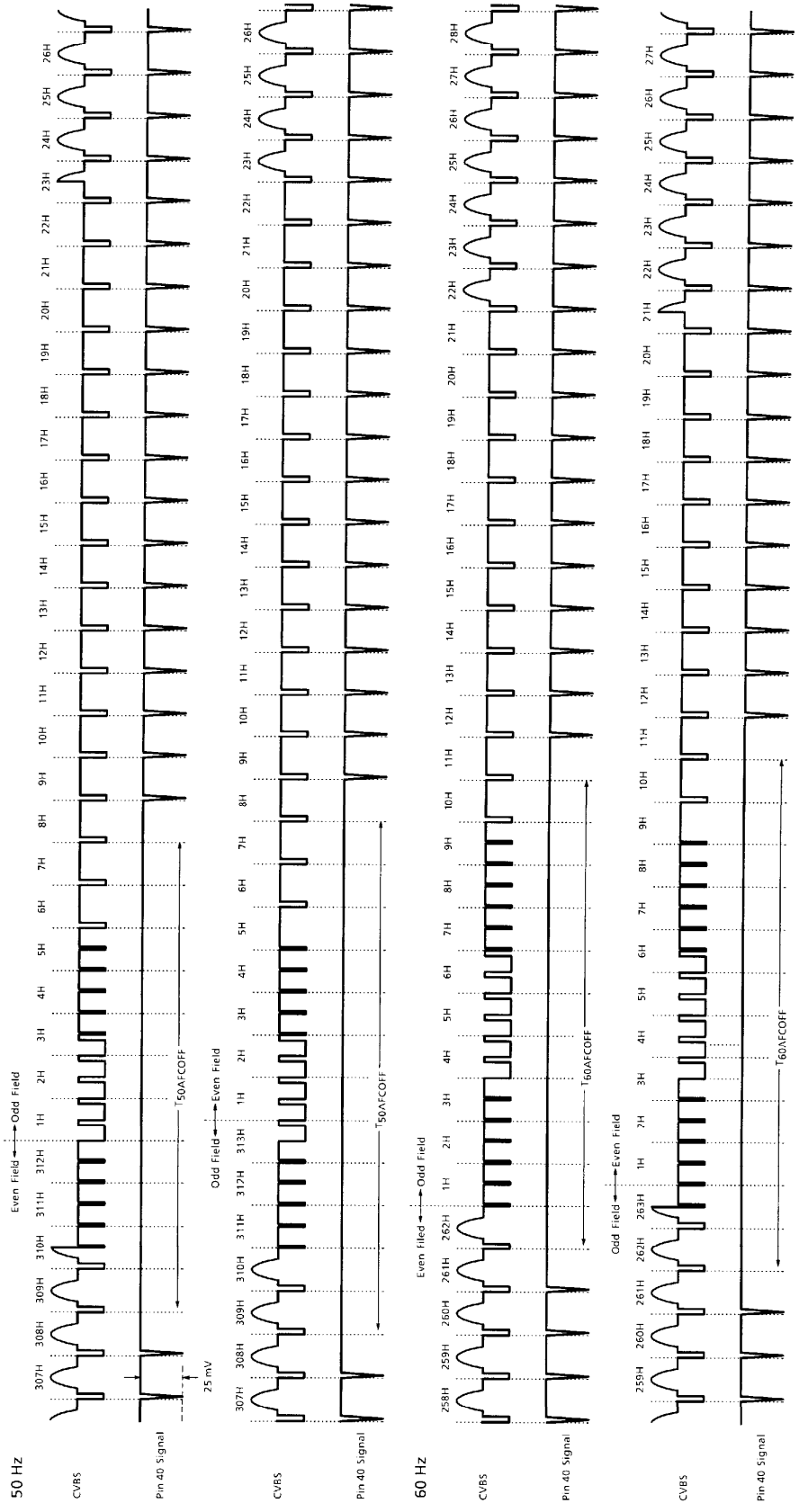
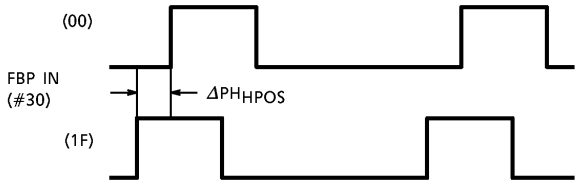
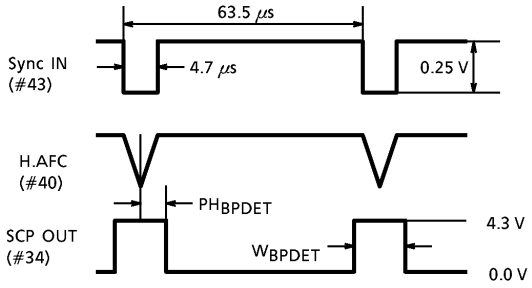
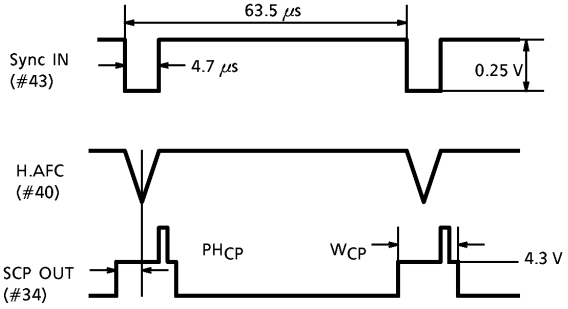
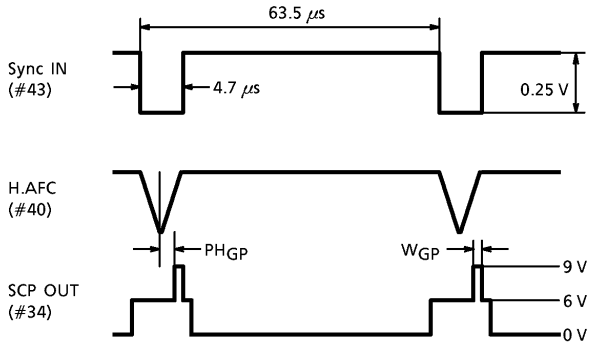
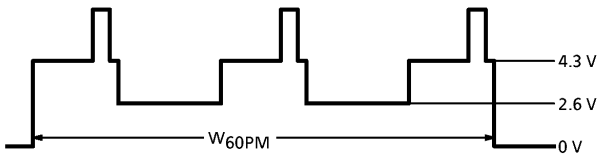

50 Hz
CVBS
Pin 40 Signal
Even Field
Odd Field
25 mV
T50AFCOFF
60 Hz
T60AFCOFF
TA1246AN—69

Fig. D1

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
D12	Horizontal Position Variable Range / ΔPH_{HPOS}	H Position : 0 / 31 Others : Preset	(1) Input a composite sync signal into Pin 43. (2) Changing Horizontal Position from 0 to 31, measure "ΔPH_{HPOS}" according to the following figure. 
D13	AFC-2 Pulse Threshold Level / V_{AFC2}	All : Preset	(1) Input a composite sync signal into Pin 43. (2) Decreasing the FBP high level, measure the DC level at which H OUT phase changes against Sync Out phase, that is "V_{AFC2}".
D14	H-BLK Pulse Threshold Level / V_{HBLK}	All : Preset	(1) Input a composite sync signal into Pin 43. (2) Increasing the FBP high level, measure the DC level at which H blanking begins to work, that is "V_{HBLK}".
D15	Black Peak Det. Stop Period (H) / PH_{BPDET} / W_{BPDET}	TEST : 00001000 Others : Preset	(1) Input a composite sync signal into Pin 43. (2) According to the following figure, measure "PH_{BPDET}" & "W_{BPDET}". 
D16	Clamp Pulse Start Phase / PH_{CP} Clamp Pulse Width / W_{CP}	TEST : 00001000 V Position : 001 Others : Preset	(1) Input a composite sync signal into Pin 43. (2) According to the following figure, measure "PH_{CP}" & "W_{CP}". 

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
D17	Gate Pulse Start Phase / PH_{GP} Gate Pulse Width / W_{GP}	All : Preset	(1) Input a composite sync signal into Pin 43. (2) According to the following figure, measure "PH_{GP}" & "W_{GP}". 
D18	Sync. Output Low Level / V_{SYNCL}	All : Preset	(1) Input a composite sync signal into Pin 43. (2) Measure the DC voltage of Sync Out low level, that is "V_{SYNCL}".
D19	Vertical Oscillation Start Voltage / V_{VON}	All : Preset	(1) Let Pin 3 / 17 / 52 / 36 / 46 be open. (2) Increasing Pin 28 voltage, measure the voltage at which V Ramp signal appears at Pin 22, that is "V_{VON}".
D20	Vertical Free-Run Frequency / F_{V60FR}	V-Freq : 01 Others : Preset	For V-Freq 01, measure the frequency of V Ramp at Pin 22, that is "F_{V60FR}".
D21	Gate Pulse V-Masking Period / T_{60GPM}	All : Preset	(1) Input a 60 Hz composite sync signal into Pin 43. (2) Measure "T_{60GPM}" at Pin 34. (cf. Fig.D_{21})
D22	V.Ramp DC on Service Mode / $V_{NOVRAMP}$	MUTE : 11 Others : Preset	Measure the DC level of Pin 22, that is "$V_{NOVRAMP}$".

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
D23	Vertical Pull-In Range (60 Hz) / F_{VP60L} / F_{VP60H}	V-Freq : 01 Others : Preset	(1) Input a composite sync signal into Pin 43. (2) For V-Freq 01, increasing the input vertical period from 220 H by 0.5 H step, measure the period at which V OUT signal synchronized with Sync out, that is "F_{VP60L}". (3) For V-Freq 01, decreasing the input vertical period from 360 H by 0.5 H step, measure the period at which V OUT signal synchronized with Sync out, that is "F_{VP60L}".
D24	Vertical Period on Fixed Mode / T_{V263}	V-Freq : 10 Others : Preset	For V-Freq 10, measure the vertical period at Pin 34, that is "T_{V263}".
D25	V-BLK Start Phase / PH_{60VBLK} V-BLK Width / W_{60VBLK}	All : Preset	(1) Input a 60 Hz composite sync signal into Pin 43. (2) Measure "$T_{60AFCOFF}$" at Pin 40. (cf. Fig.D25)
D26	Picture Mute Period / W_{60PM}	TEST : 00001000 Others : Preset	(1) Input a 60 Hz composite sync signal into Pin 43. (2) According to a following figure, measure "W_{60PM}". 

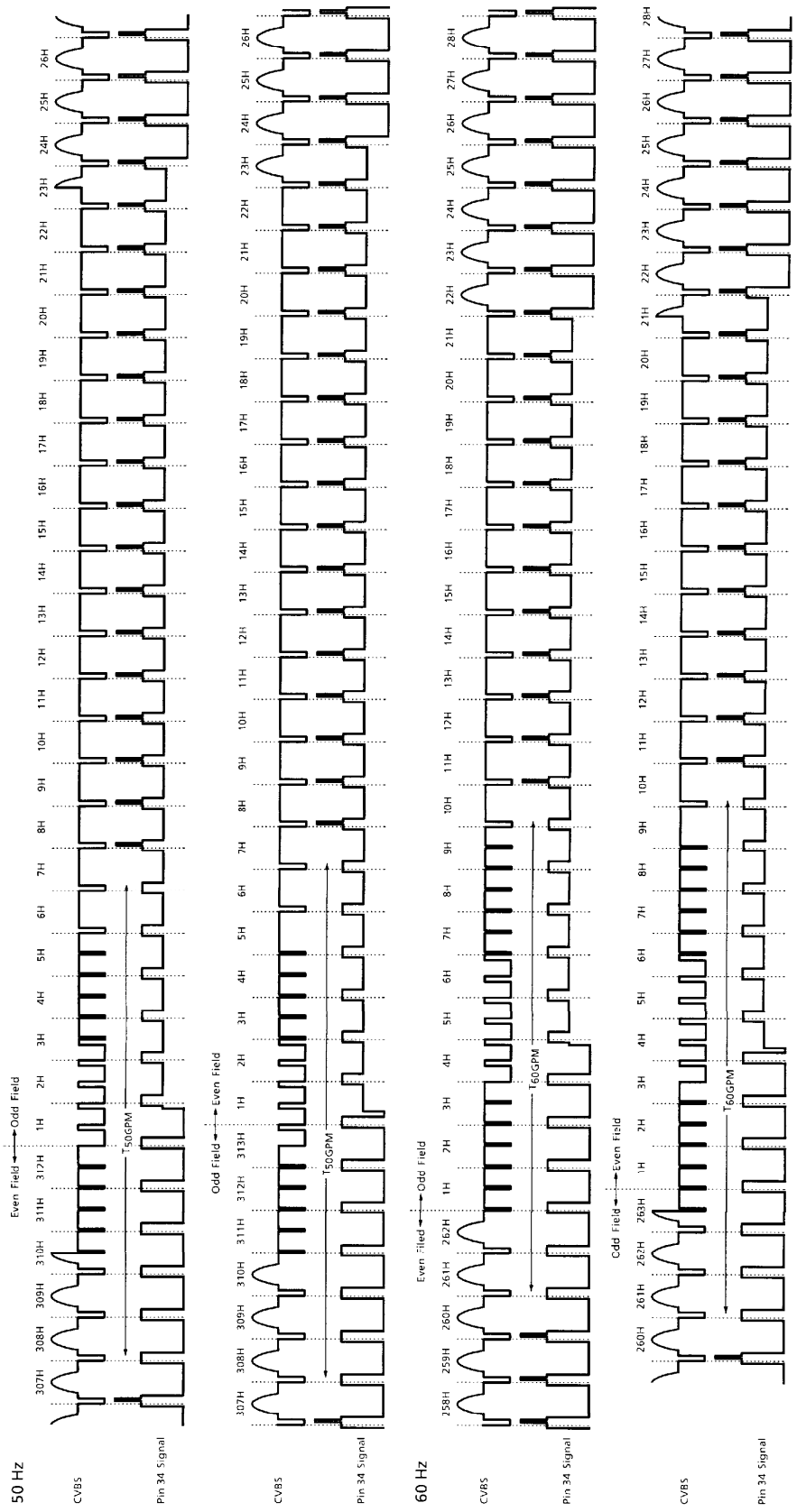

50 Hz
CVBS
Pin 34 Signal
Even Field → Odd Field
Odd Field → Even Field
T50GPM
60 Hz
Even Filed → Odd Field
Odd Field → Even Field
T60GPM
TA1246AN—73

Fig. D21

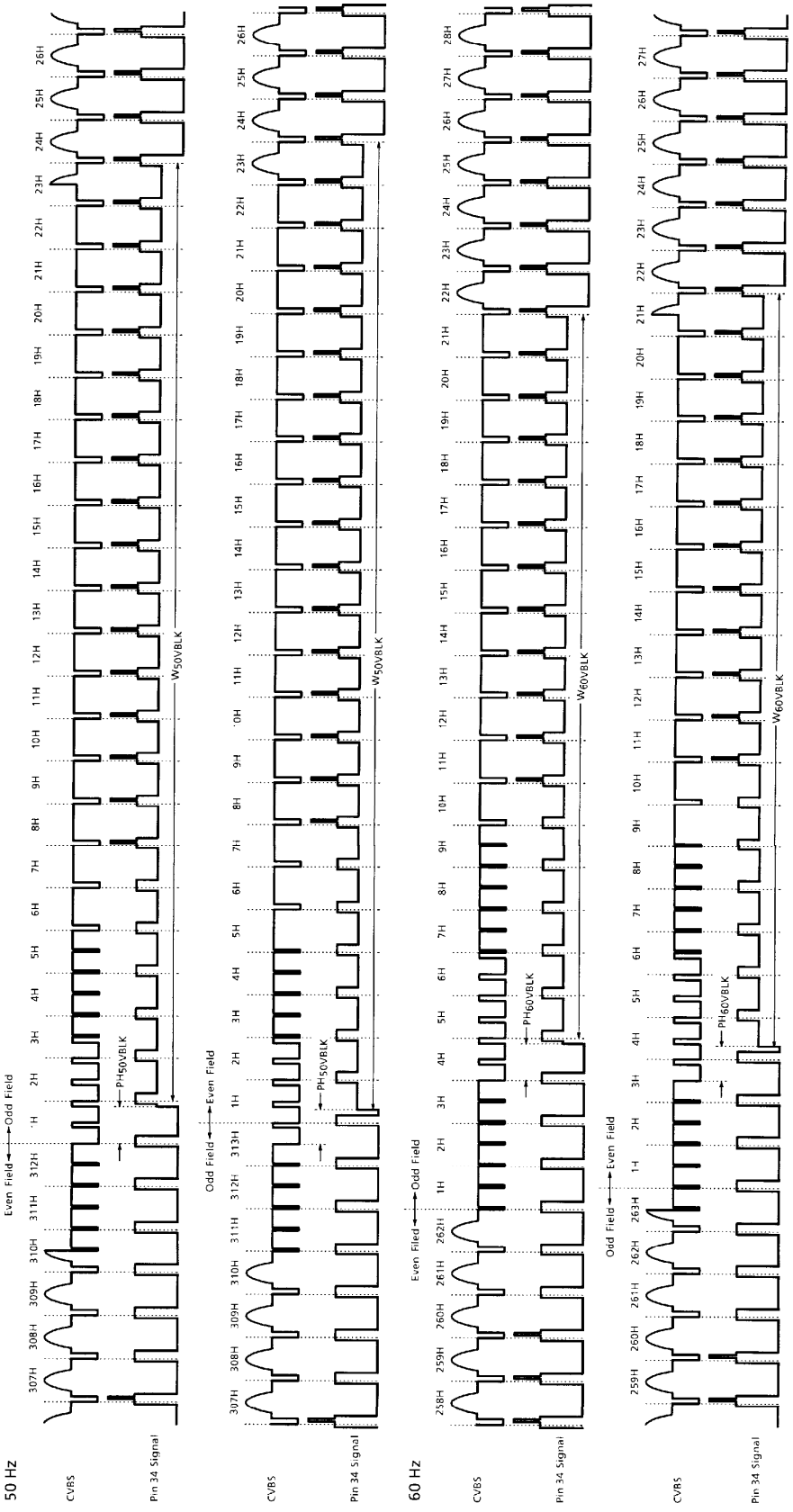
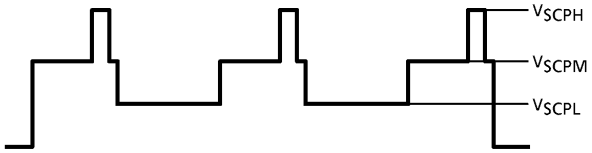
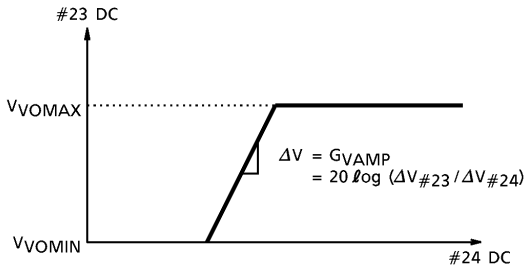
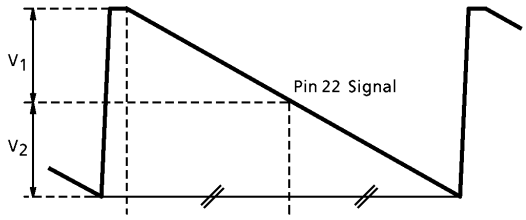
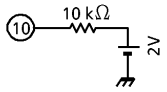
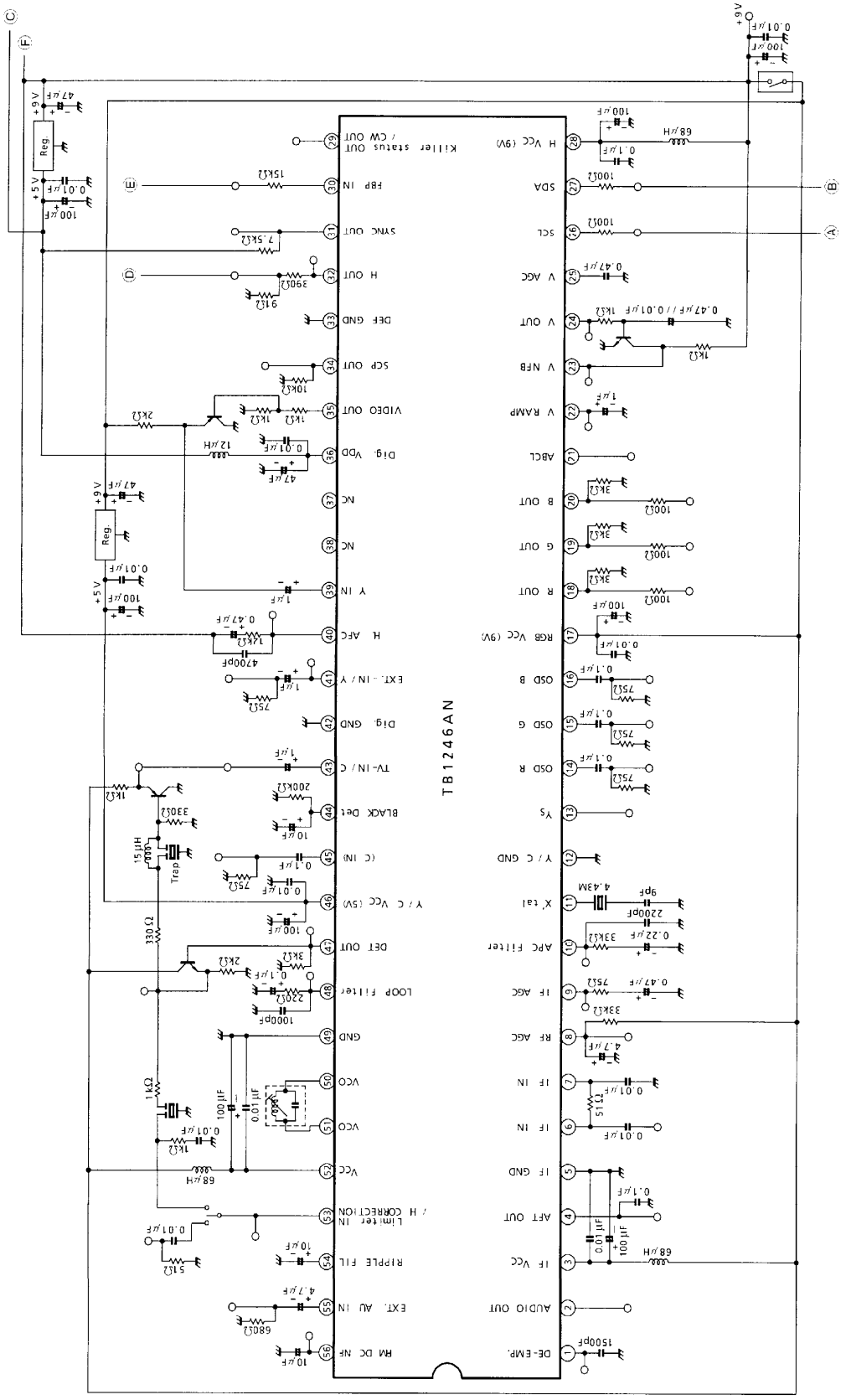

50 Hz
CVBS
Pin 34 Signal
Even Field
Odd Field
PH50VBLK
W50VBLK
60 Hz
PH60VBLK
W60VBLK
TA1246AN—74

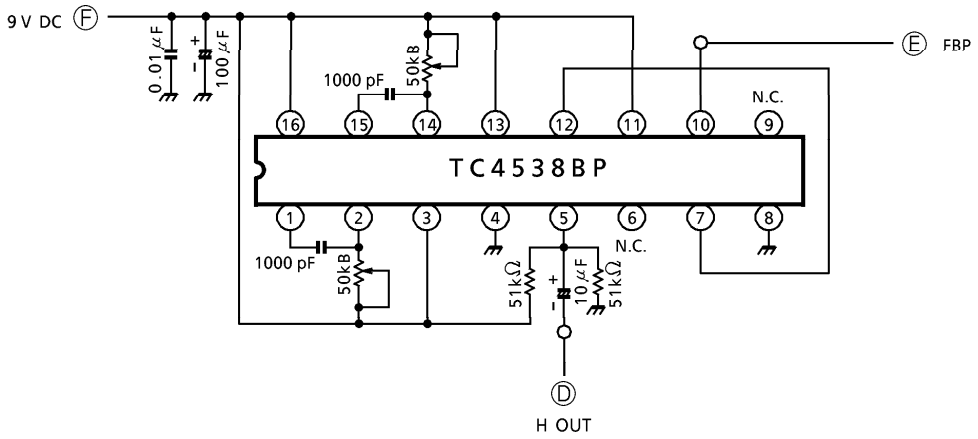
Fig. D25

NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
D27	Sand Castle Pulse Level / V_{SCPH} / V_{SCPM} / V_{SCPL}	All : Preset	Measure "V_{SCPH}" / "V_{SCPM}" / "V_{SCPL}" at Pin 34. 
D28	Vertical Ramp Amplitude / V_{VRAMP}	All : Preset	Measure the V Ramp amplitude at Pin 22, that is "V_{VRAMP}".
D29	Vertical AMP Gain / G_{VAMP}	All : Preset	(1) Let Pin 24 be open. (2) Changing the Pin 23 DC voltage, measure "V_{VOMAX}" / "V_{VOMIN}" / "G_{VAMP}" according to a following figure. 
	Vertical AMP Max. Output Level / V_{VOMAX}		
	Vertical AMP Min. Output Level / V_{VOMIN}		
D30	Vertical AMP Max. Output Current / I_{VOMAX}	All : Preset	(1) Supply 7 V to Pin 23. (2) Measure the Current from Pin 24 to GND, that is "I_{VOMAX}".
D31	Vertical NFB Amplitude / V_{NFB}	V Size : 0 / 32 / 63 Others : Preset	(1) Measure the amplitude of NFB V Ramp at Pin 23, that is "V_{NFB}". (2) Measure the amplitude of NFB V Ramp at Pin 23 for V-Size 0 / 63, that is V_{NFBMIN} / V_{NFBMAX}. (3) Calculate ; "ΔV_{VRAMPH}" = (V_{NFBMAX} − V_{NFB}) / V_{NFB}*100 "ΔV_{VRAMPL}" = (V_{NFBMIN} − V_{NFB}) / V_{NFB}*100
	Vertical Amplitude Variable Range / ΔV_{VRAMPH} / ΔV_{VRAMPL}		

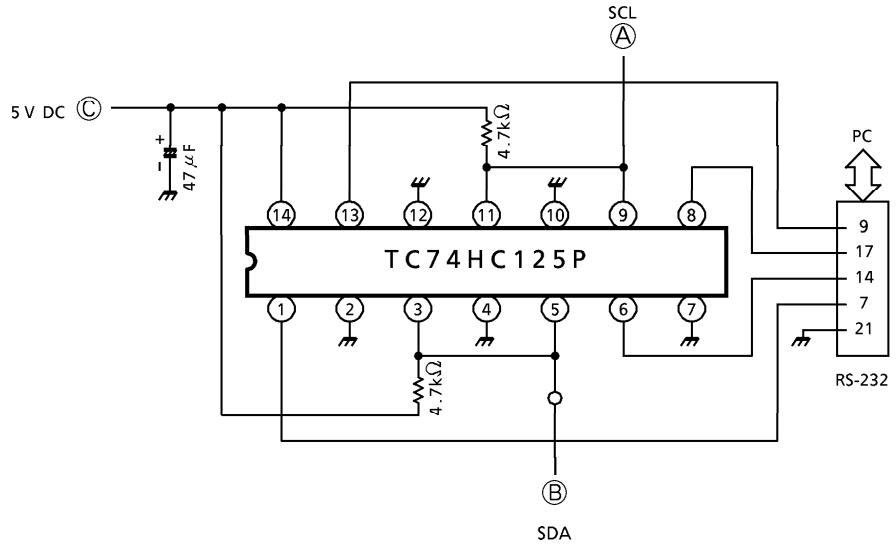
NOTE	ITEM / SYMBOL	BUS CONDITION	MEASUREMENT METHOD
D32	Vertical Linearity Variable Range / ΔV_{LIN1+} / ΔV_{LIN1-} / ΔV_{LIN2+} / ΔV_{LIN2-}	V Linearity : 0 / 8 / 15 Others : Preset	(1) For V linearity 8, Measure V_1 (From center to max.) and V_2 (From center to min.) at Pin 22 according to a following figure.  (2) For V linearity 15 / 0, measure V_{LIN1+} / V_{LIN1-} and V_{LIN2+} / V_{LIN2-}. (3) Calculate ; "ΔV_{LIN1+}" = $(V_{LIN1+} - V_1) / V_1 * 100$ "ΔV_{LIN1-}" = $(V_{LIN1-} - V_1) / V_1 * 100$ "ΔV_{LIN2+}" = $(V_{LIN2+} - V_2) / V_2 * 100$ "ΔV_{LIN2-}" = $(V_{LIN2-} - V_2) / V_2 * 100$
D33	Vertical S Correction Variable Range / ΔV_{S1+} / ΔV_{S1-} / ΔV_{S2+} / ΔV_{S2-}	V S Corr. : 0 / 8 / 15 Others : Preset	(1) For V S Correction : 8, measure V_1 and V_2 at Pin 22 according to a figure of NOTE : D_{32}. (2) For V S Correction : 15 / 0, measure V_{S1+} / V_{S1-} and V_{S2+} / V_{S2-}. (3) Calculate ; "ΔV_{S1+}" = $(V_{S1+} - V_1) / V_1 * 100$ "ΔV_{S1-}" = $(V_{S1-} - V_1) / V_1 * 100$ "ΔV_{S2+}" = $(V_{S2+} - V_2) / V_2 * 100$ "ΔV_{S2-}" = $(V_{S2-} - V_2) / V_2 * 100$
D34	V-AGC Current / I_{VAGCH} / I_{VAGCL}	V-AGC : 0 / 1 Others : Preset	(1) Connect Pin 25 to GND via a 200 resistor. (2) For V-AGC : 0 / 1, measure V_{VAGCL} / V_{VAGCH} at Pin 25 according to a following figure. (3) Calculate ; "I_{VAGCL}" = $V_{VAGCL} / 200$ "I_{VAGCH}" = $V_{VAGCH} / 200$
D35	Vertical Guard Voltage / V_{VG}	All : Preset	Decreasing the Pin 23 voltage from 5 V, measure the voltage at which Pin 20 output drops to blanking level, that is "V_{VG}".
D36	BGP Phase / ΔBGP	BGP P : 0 / 1 Others : Preset	(1) Input a composite sync signal into Pin 43. (2) Connect Pin 10 as the figure.  (3) Measure the start point difference of BGP at Pin 10 for BGP P : 0 / 1, that is "ΔBGP".


TB1246AN
DE-EMP.
AUDIO OUT
IF VCC
AFT OUT
IF GND
IF IN
IF IN
RF AGC
IF AGC
APC Filter
X' tal
Y / C GND
Y S
OSD B
OSD G
OSD R
RGB VCC (9V)
R OUT
G OUT
B OUT
ABCL
V RAMP
V OUT
V AGC
SCL
SDA
H VCC (9V)
Killer status OUT / CW OUT
FBP IN
SYNC OUT
H OUT
DEF GND
SCP OUT
VIDEO OUT
Dig. VDD
NC
NC
Y IN
H. AFC
EXT. IN / Y
Dig. GND
TV-IN / C
BLACK Det
(C IN)
Y / C VCC (5V)
DET OUT
LOOP Filter
GND
VCO
VCO
VCC
Limiter IN / H CORRECTION
RIPPLE FIL
EXT. AU IN
RM DC NF
TEST CIRCUIT 1
TA1246AN-77

TEST CIRCUIT 2

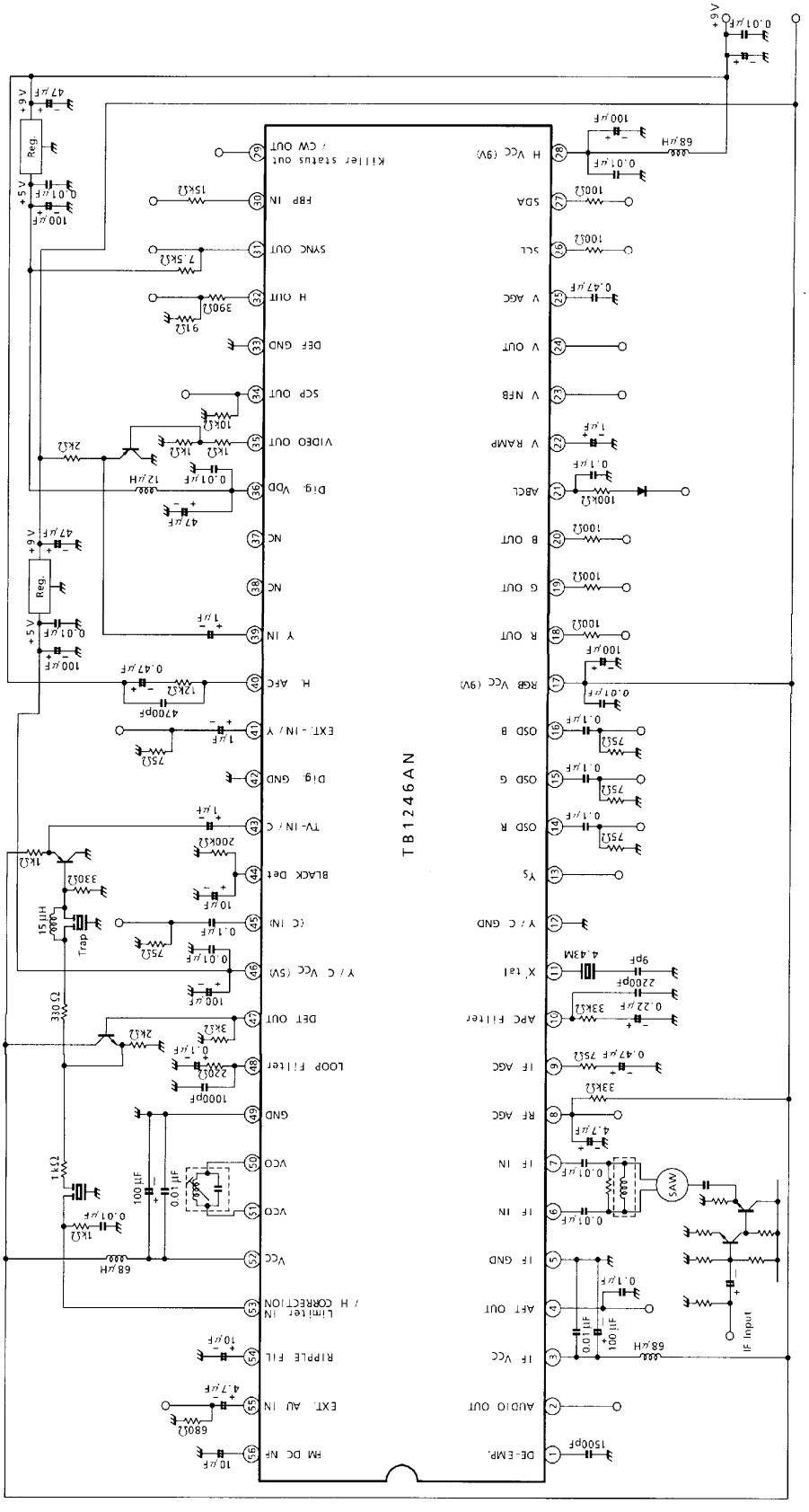


Mono Multi Vibrator



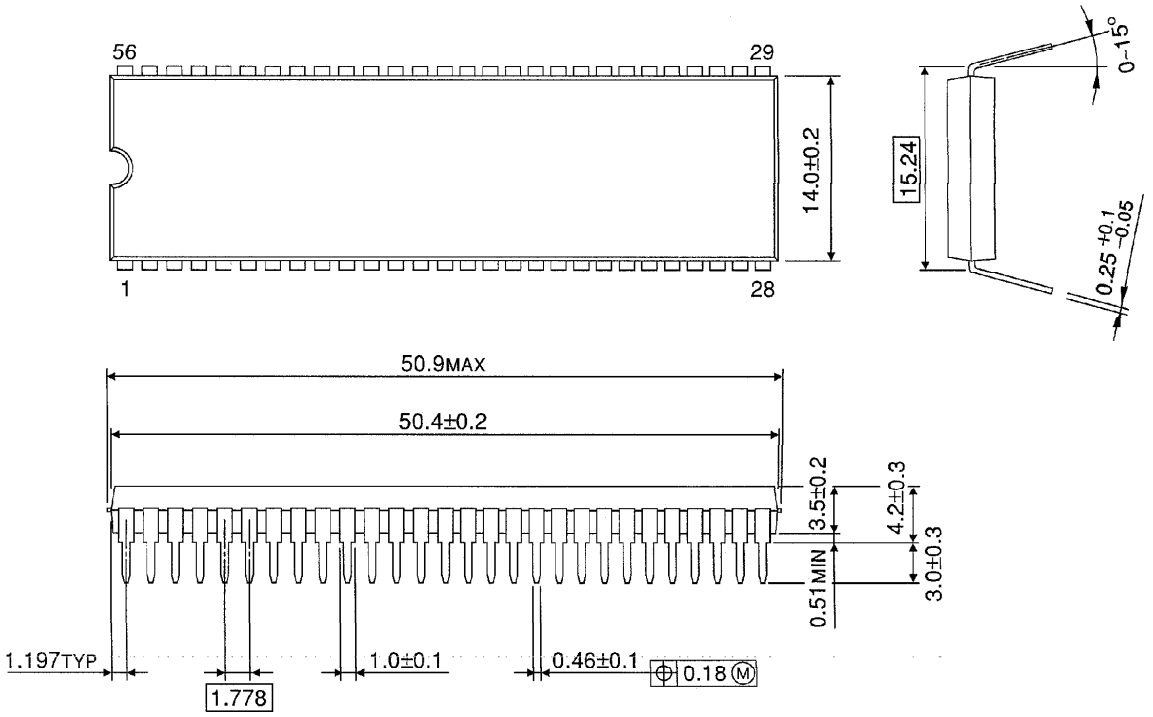
I^2C BUS Interface

EXAMPLE OF APPLICATION CIRCUIT



OUTLINE DRAWING

SDIP56-P-600-1.78 Unit : mm



Weight : 5.55 g (Typ.)