

TBA810SH/AS

7W AUDIO POWER AMPLIFIER

■ FEATURES

- Low Noise

1.7 μ V typ, 3.3 μ V max, total input noise.
($V_{CC} = 15V$, $R_g = 7.7 k\Omega$, see test circuit)
where R_g : Signal Source Resistance for IC

- High Output Power

7W typ ($V_{CC} = 16V$, $R_L = 4\Omega$

T.H.D = 10%)

where

6W typ ($V_{CC} = 14.4V$, $R_L = 4\Omega$, V_{CC} : Supply

T.H.D. = 10%) Voltage

2.5W typ ($V_{CC} = 9V$, $R_L = 4\Omega$, R_L : Load Resistance

T.H.D. = 10%) T.H.D.: Total Harmonic

1W typ ($V_{CC} = 6V$, $R_L = 4\Omega$,

T.H.D = 10%) Distortion

- Wide Range of Supply Voltage from 4 to 20V

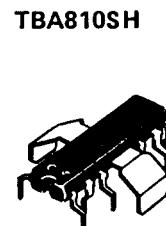
- High Output Current up to 2.5A

- High Efficiency 75% at 6W Output

- Very Low Harmonic and Crossover Distortion

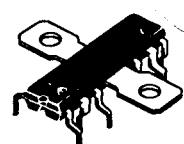
- TBA810S·H is provided with a thermal limiting circuit which fundamentally changes the criteria normally used in determining size of the heat sink.

TBA810SH



(QP-12T)

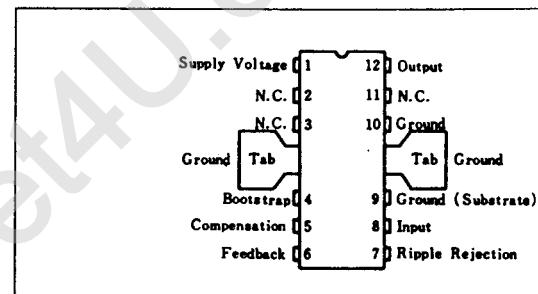
TBA810AS



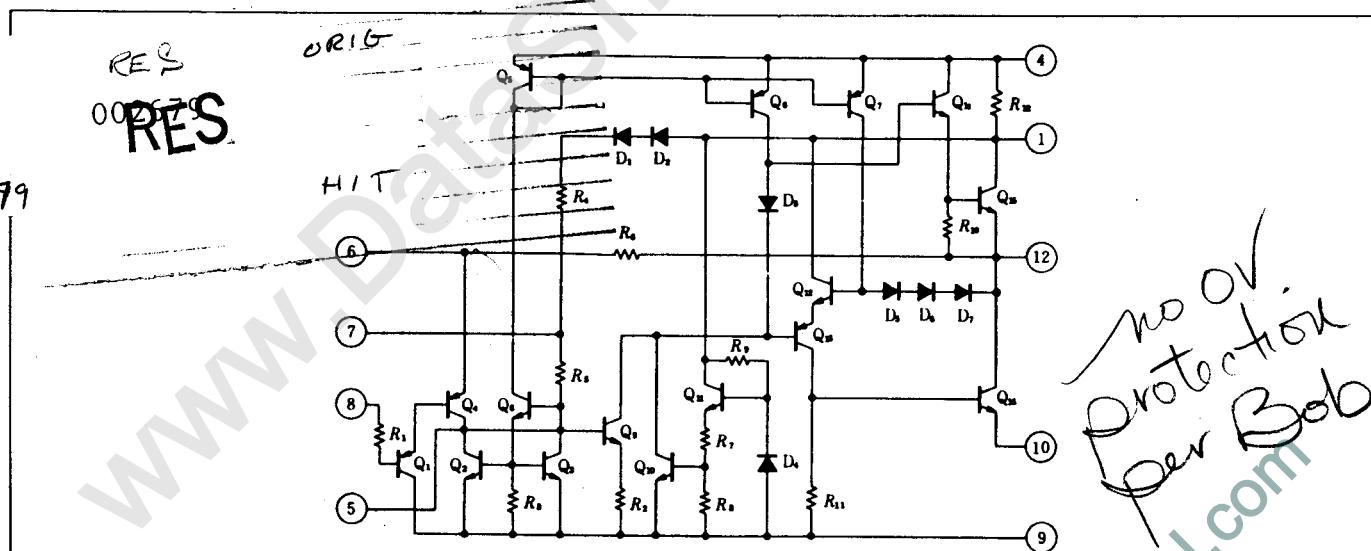
65.7

(QP-12TA)

■ PIN CONNECTION (Top View)



■ CIRCUIT SCHEMATIC



■ ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$)

Item	Symbol	Rating	Unit
Supply Voltage	V_{CC}	20	V
Output Peak Current (nonrepetitive)	$I_{O,peak}$	3.5	A
Output Current (repetitive)	I_O	2.5	A
Power Dissipation	when $T_A = 80^\circ C$	1	W
	when $T_{J,ab} = 100^\circ C$	5	W
Storage and Junction Temperature	T_{Stg}, T_J	-40 to +150	°C
Thermal Resistance (Junction-to-tab(max))	θ_{J-tab}	10	°C/W
Thermal Resistance (Junction-to-ambient(max))	θ_{J-a}^*	70	°C/W

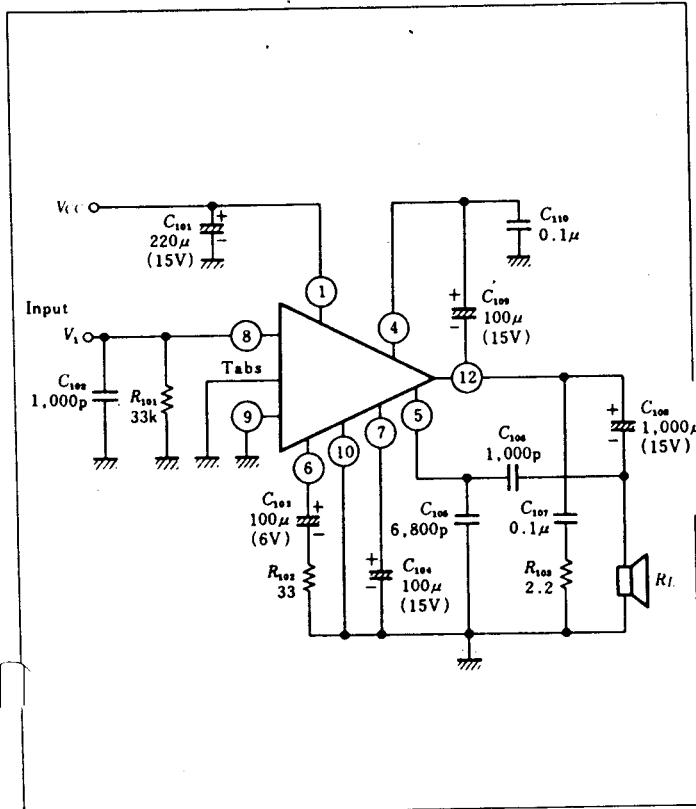
* Obtained with tabs soldered to printed circuit with minimized copper area.

ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Test Condition	min	typ	max	Unit
Supply Voltage (pin 1)	V_{cc}	$V_{cc} = 14.4\text{V}$	4	—	20	V
Quiescent Output Voltage (pin 12)	V_{out}		6.4	7.2	8.0	V
Quiescent Total Current Drain	I_T		—	12	20	mA
Bias Current (pin 8)	I_b		—	0.4	—	μA
Output Power	P_{out}	$T.H.D = 10\%$ $R_L = 4\Omega$ $f = 1\text{kHz}$	$V_{cc} = 16\text{V}$	—	7	—
			$V_{cc} = 14.4\text{V}$	—	6	—
			$V_{cc} = 12\text{V}$	3.6	4.2	—
			$V_{cc} = 9\text{V}$	—	2.5	—
			$V_{cc} = 6\text{V}$	—	1	—
Input Voltage	V_{in}					220 mV_{rms}
Input Sensitivity	V_i	$P_{out} = 6\text{W}, V_{cc} = 14.4\text{V}$ $R_L = 4\Omega, f = 1\text{kHz}$	$R_f = 56\Omega$	—	80	—
			$R_f = 22\Omega$	—	35	—
Input Resistance (pin 8)	R_{in}					$\text{M}\Omega$
Frequency Response (-3dB)	B	$V_{cc} = 14.4\text{V}, R_L = 4\Omega, R_f = 33\Omega, C_3 = 1000\text{pF}$				Hz
Total Harmonic Distortion	$T.H.D$	$P_{out} = 0.5\text{W}, V_{cc} = 14.4\text{V}, R_L = 4\Omega, f = 1\text{kHz}$				%
Voltage Gain (open loop)	G_V	$V_{cc} = 14.4\text{V}, R_L = 4\Omega, f = 1\text{kHz}$				dB
Voltage Gain (closed loop)	G_V	$V_{cc} = 14.4\text{V}, R_L = 4\Omega, f = 1\text{kHz}$				dB
Input Noise Voltage	e_N	$V_{cc} = 14.4\text{V}, R_g = 0\Omega$				μV
Output Noise Voltage		$V_{cc} = 15.0\text{V}, R_g = 7.7\text{k}\Omega$				μV
Efficiency	η	$P_{out} = 5\text{W}, V_{cc} = 14.4\text{V}, R_L = 4\Omega, f = 1\text{kHz}$				%
Hum Rejection	HR	$V_{cc} = 14.4\text{V}, R_L = 4\Omega, f = 100\text{Hz}$				dB

Notes : 1. When $C_2 = 820\text{pF}$, $C_1 = 500\text{nF}$, $R_f = 56\Omega$, $B = 40$ to $20,000\text{Hz}$
 2. $B(-3\text{dB})$ of IC : 40 to $20,000\text{Hz}$, $B(-3\text{dB})$ of Test equipment : 20 to $20,000\text{Hz}$
 3. $B(-3\text{dB})$ of IC : 50 to $10,000\text{Hz}$, $B(-3\text{dB})$ of Test equipment : 10 to $100,000\text{Hz}$, $10\text{k}\Omega$ is additionally connected at input terminals.

TEST CIRCUIT



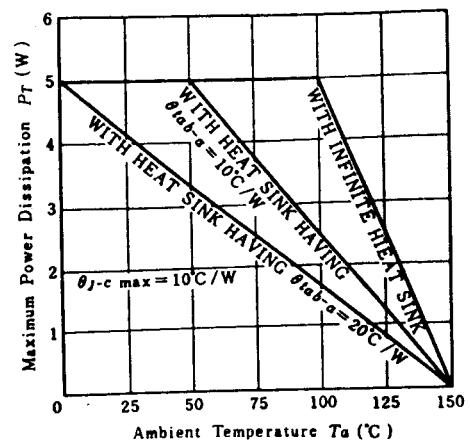
MOUNTING INSTRUCTIONS

The tabs on the TBA810 can be used to detract the heat generated in the integrated circuit so that the junction temperature does not exceed the permissible maximum (150°C).

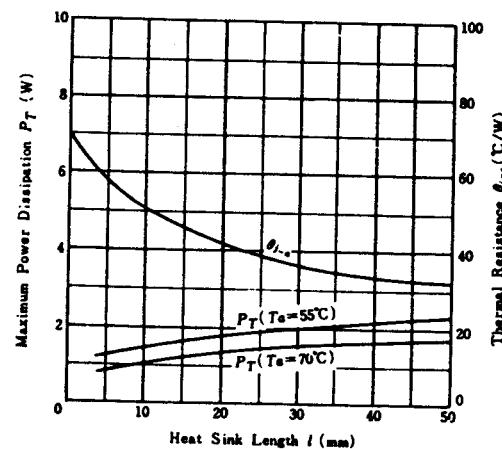
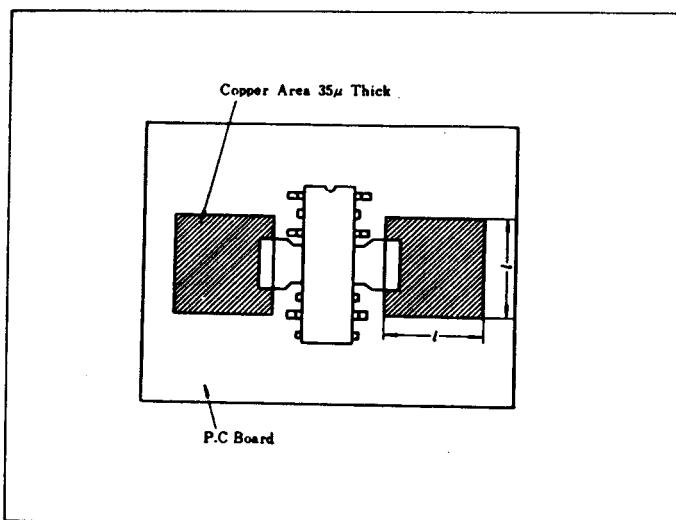
This may be done by connecting tabs to an external heat sink, or by soldering them to a suitable copper area of the printed circuit board.

External heat sink or printed circuit copper area must be connected to electrical ground.

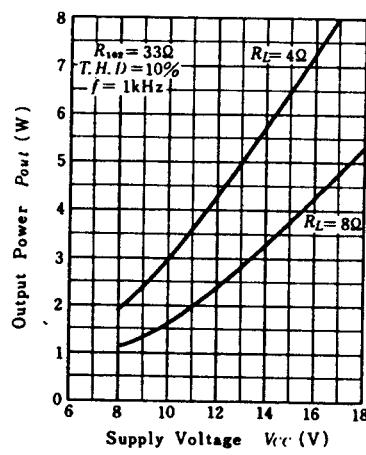
POWER RATING CHARACTERISTICS



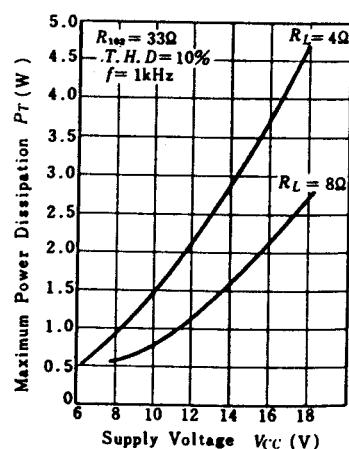
MAXIMUM POWER DISSIPATION VS. COPPER AREA OF P.C. BOARD



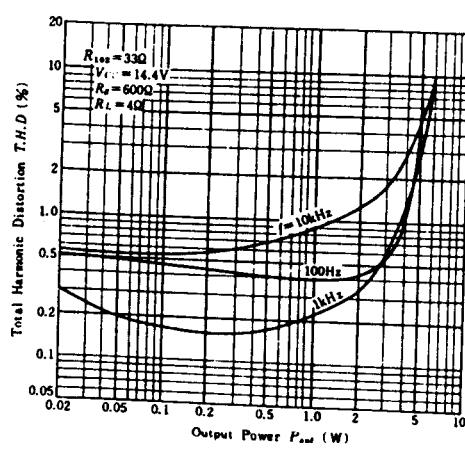
OUTPUT POWER VS. SUPPLY VOLTAGE



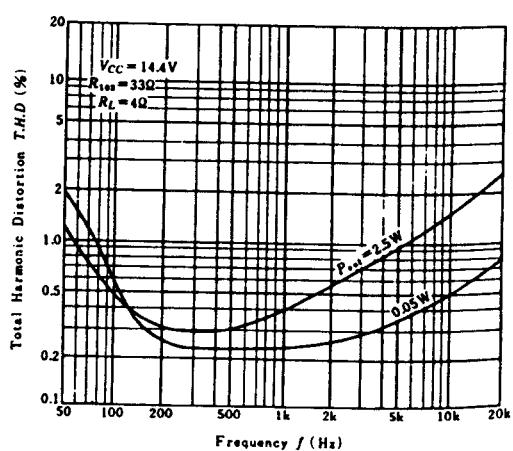
MAXIMUM POWER DISSIPATION VS. SUPPLY VOLTAGE



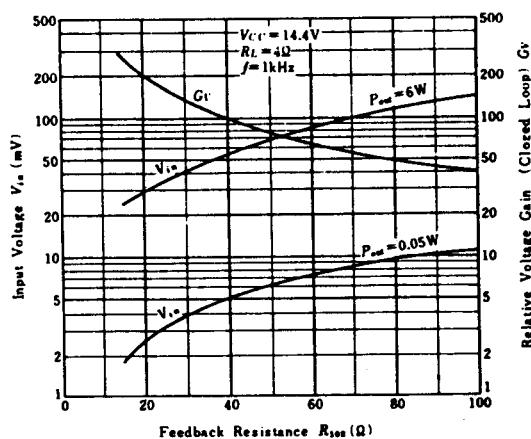
TOTAL HARMONIC DISTORTION VS. OUTPUT POWER



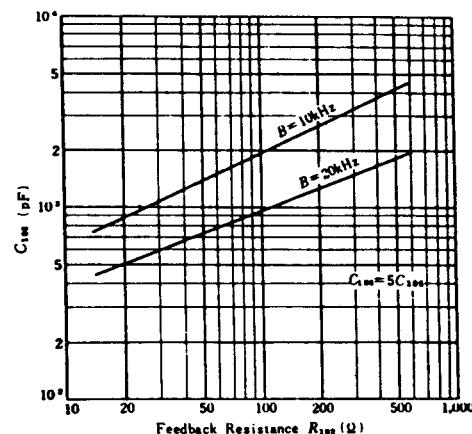
TOTAL HARMONIC DISTORTION VS. FREQUENCY



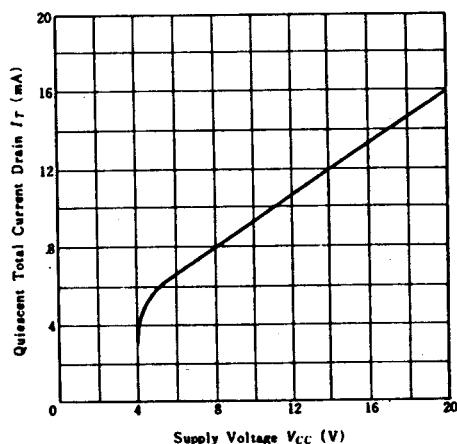
RELATIVE VOLTAGE GAIN AND INPUT VOLTAGE VS. FEEDBACK RESISTANCE



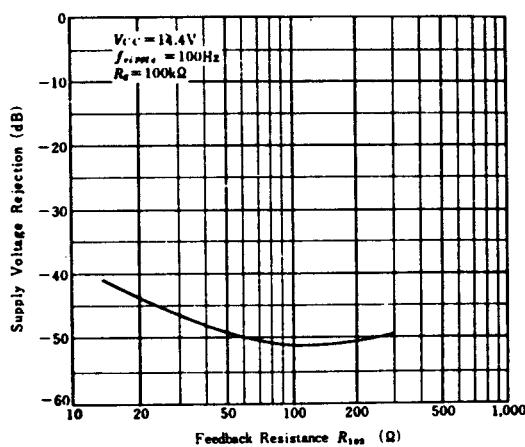
VALUE OF C_{106} VS. R_{102} FOR VARIOUS VALUE OF B



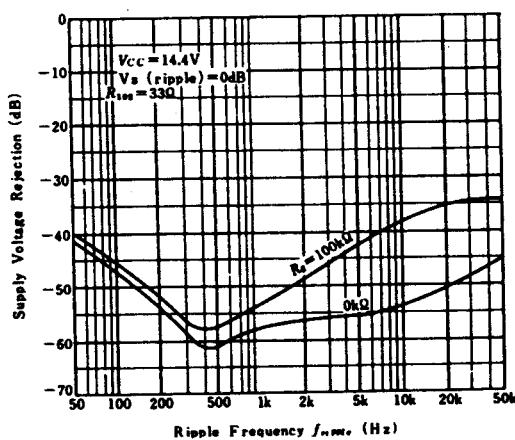
QUIESCENT TOTAL CURRENT DRAIN VS. SUPPLY VOLTAGE



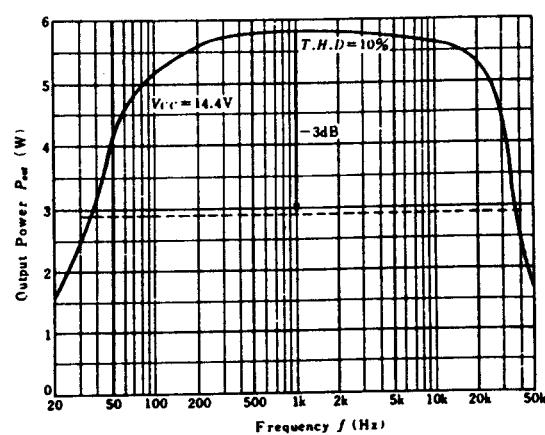
TYPICAL SUPPLY VOLTAGE REJECTION VS. FEEDBACK RESISTANCE



SUPPLY VOLTAGE REJECTION VS. RIPPLE FREQUENCY



POWER BANDWIDTH CHARACTERISTICS



■ EXTERNAL COMPONENTS

Parts No.	Recommended Value	Purpose	Influence		Remarks
			Larger than recommended Value	Smaller than recommended Value	
R ₁₀₁	33kΩ	Determination of Input impedance	—	—	—
R ₁₀₂	33Ω	Determination of G _V	All Characteristics are influenced	Oscillation	$G_V = \frac{4000}{R_{102} (\Omega)}$
R ₁₀₃	2.2Ω (When C ₁₀₇ =0.1μF)	Prevention of Oscillation	Oscillation	Oscillation	—
C ₁₀₃	100μF (When R ₁₀₂ =33Ω)	Feedback Capacitor	f _L decreases	f _L increases	$f_L = \frac{1}{2\pi C_{103} R_{102}}$ (Low cut-off frequency)
C ₁₀₅ C ₁₀₆ C ₁₀₇ C ₁₁₀	6,800pF 1,000pF 0.1μF 0.1μF	Prevention of Oscillation	Oscillation	Oscillation	—
C ₁₀₈	1,000μF	Output Coupling Capacitor	Breakdown at Overload	Poor low-frequency characteristics	$f_L = \frac{1}{2\pi C_{108} R_L}$

The following figure illustrates a method of mounting the TBA810 that is satisfactory both from the heat dissipation viewpoint and from mechanical consideration.

