

Dual Operational Amplifiers

TBB 1458 -1458

TBB 1458 B-1458

TBC 1458 -1558

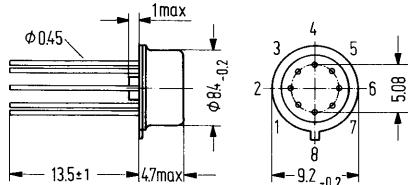
TBB 1458 and TBC 1458 are monolithic integrated dual operational amplifiers in packages similar to 5 G 8 DIN 41873. They are outstanding by reason of their large common-mode and differential voltage range and short-circuit protection. No external components for frequency compensation are required. TBB 1458 B (8 pins) in plastic plug-in package. For single amplifier performance, see the TBA 221 data sheet.

Type	Ordering codes
TBB 1458	Q67000-A1035
TBB 1458 B	Q67000-A1036
TBC 1458	Q67000-A1037

TBB 1458 B

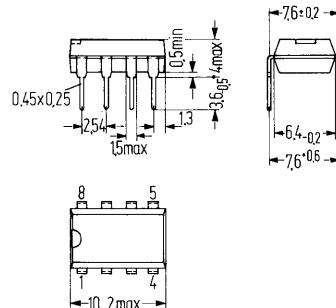
Package outlines

TBB 1458, TBC 1458



Case similar to 5 G 8 DIN 41873 (TO-99)
Weight approx. 1.2 g

Dimensions in mm



Plastic plug-in package, 8 pins
20 A 8 DIN 41866
Weight approx. 0.7 g

Maximum ratings

	TBB 1458 TBB 1458 B	TBC 1458	
Supply voltage	± 18	± 22	V
Input voltage ¹⁾	± 15	± 15	V
Differential input voltage ²⁾	± 30	± 30	V
Short circuit duration ³⁾	∞	∞	
Storage temperature	T_s -65 to +150	-65 to +150	°C
Junction temperature	T_j 150	150	°C
Thermal resistance:			
System-case (TBB 1458/TBC 1458)	$R_{thScase}$ 80	80	K/W
System-ambient air (TBB 1458, TBC 1458)	R_{thSamb} 190	190	K/W
System-ambient air (TBB 1458 B)	R_{thSamb} 140		K/W

Range of operation

Supply voltage	V_{cc}	± 4 to ± 18	± 4 to ± 22	V
Ambient temperature in operation	T_{amb}	0 to +70	-55 to +125	°C

¹⁾ For supply voltage less than ± 15 V the maximum input voltage is equal to the supply voltage

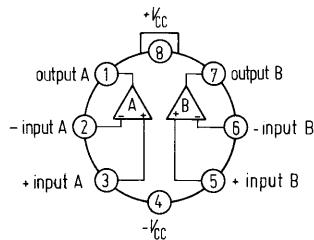
²⁾ For supply less than ± 15 V the maximum differential input voltage is equal to $\pm (V_{cc} + |V_{cc}|)$

³⁾ Short circuit may be ground or $\pm V_{cc}$.

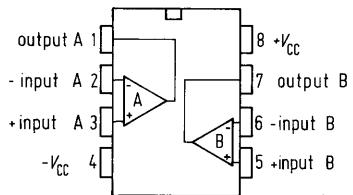
TBB 1458 -1458
TBB 1458 B-1458
TBC 1458 -1558

Pin connection

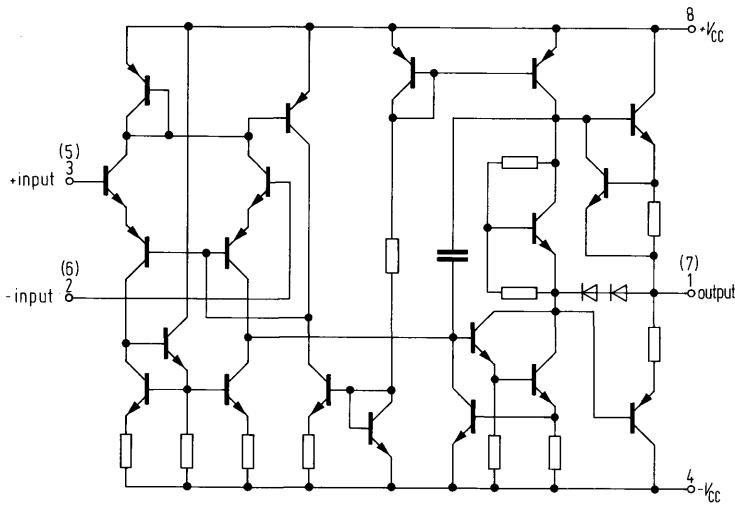
TBB 1458
TBC 1458



TBB 1458 B



Equivalent circuit (each side)

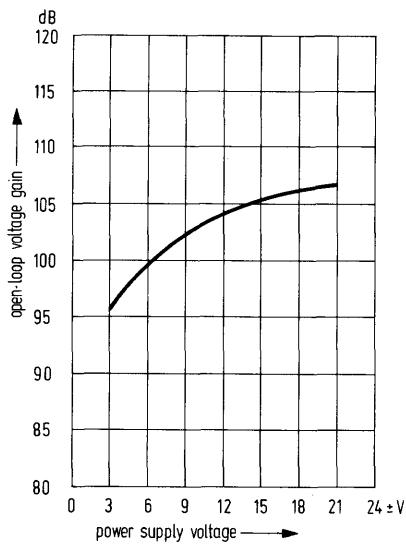


Operating characteristics

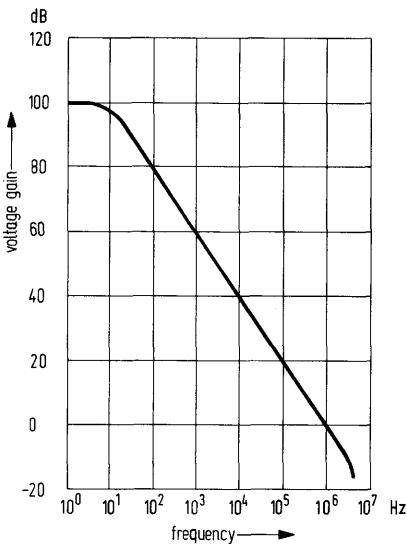
($V_{CC} = \pm 15$ V, $T_{amb} = 25^\circ C$
when not otherwise stated)

	TBB 1458			TBC 1458			mV	
	Tbb 1458 B	min	typ	max	min	typ	max	
Input offset voltage ($R_G \leq 10$ kΩ) ($T_{amb} = 0$ to $70^\circ C$) ($T_{amb} = -55$ to $+125^\circ C$)	V_{io}	-6		6	-4		4	mV
	V_{io}	-7.5		7.5	-6		6	mV
	V_{io}							mV
Input offset current ($T_{amb} = 0$ to $70^\circ C$) ($T_{amb} = -55$ to $+125^\circ C$)	I_{io}	-200	± 20	200	-100	± 20	100	nA
	I_{io}	-300		300				nA
	I_{io}							nA
Input current ($T_{amb} = 0$ to $70^\circ C$) ($T_{amb} = -55$ to $+125^\circ C$)	I_i	80	500		80	350		nA
	I_i		800					nA
	I_i							μA
Current supply	I_{cc}	3.4	5.6		3.4	5.6		mA
Output short circuit current	I_{osc}	± 18			± 18			mA
Input resistance	R_i	300	1000		300	1000		k Ω
Input capacitance	C_i	6			6			pF
Output resistance	R_q	75			75			Ω
Output voltage ($R_L \geq 10$ kΩ) ($R_L \geq 2$ k Ω)	V_{app}	12	± 14	-12	13	± 14	-12.5	V
	V_{app}	10	± 13	-10	11	± 13	-11	V
	V_i	12	± 13	-12	12	± 13	-12	V
Input voltage range								
Voltage gain ($V_{app} = \pm 10$ V, $R_L \geq 2$ k Ω) ($T_{amb} = 0$ to $70^\circ C$) ($T_{amb} = -55$ to $+125^\circ C$)	G_v	86	100		94	106		dB
	G_v	84						dB
	G_v				88			dB
Common-mode rejection ratio ($R_G \leq 10$ k Ω)	$CMRR$	70	90		80	90		dB
Sensitivity to supply voltage variations	$\frac{\Delta V_{io}}{\Delta V_{cc}}$	30	150		30	150		$\mu V/V$
Leading edge slope ($R_L \geq 2$ k Ω)	$\frac{dV_{app}}{dt}$.5			.5			V/ μs
Temperature coefficient of V_{io}	α_{vio}				3			$\mu V/K$
Temperature coefficient of I_{io}	α_{lio}				.4			nA/K

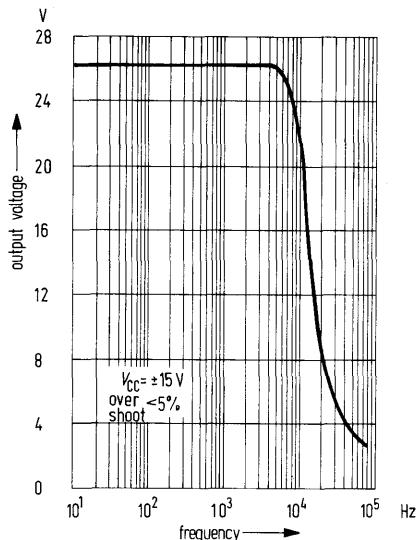
Open-loop voltage gain versus power-supply voltage



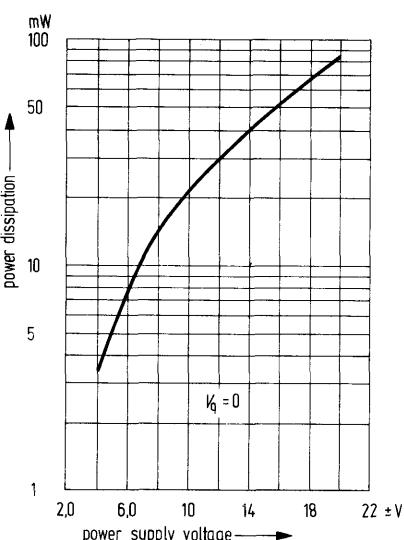
Open-loop frequency response



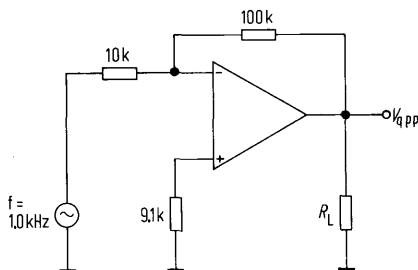
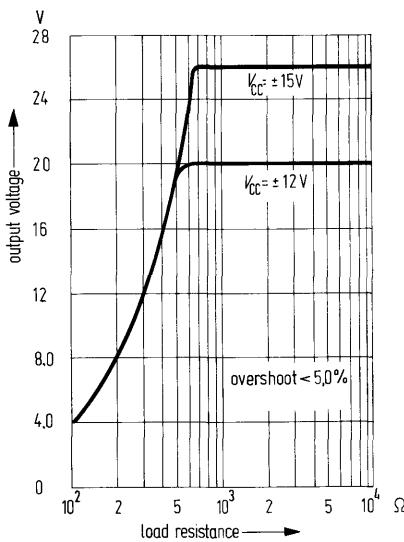
**Power bandwidth
(large signal swing versus frequency)**



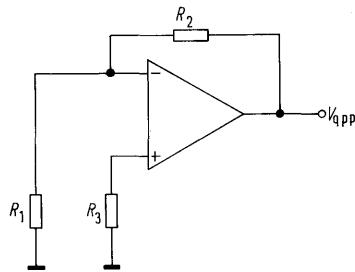
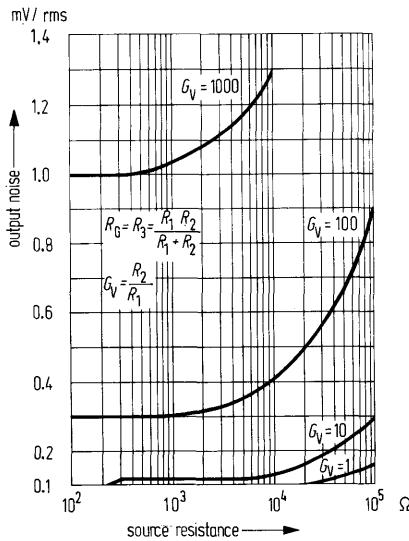
**Power dissipation
versus power supply voltage**



Output voltage swing versus load resistance



Output noise versus source resistance



For further performance curves, see TBA 221 data sheet