

TBR8

High Input Voltage, Adjustable 3-Terminal Linear Regulator

Features

13.2 - 450V input voltage range Adjustable 1.20 - 440V output regulation 5% output voltage tolerance Output current limiting 10µA typical ADJ current Internal junction temperature limiting

Applications

Off-line SMPS startup circuits
Adjustable high voltage constant current source
Industrial controls
Motor controls
Battery chargers
Power supplies

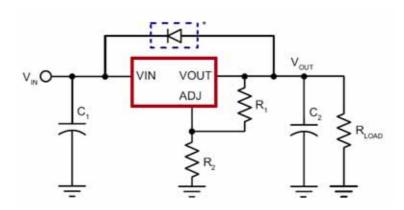
General Description

The Top-Best IC TBR8 is a high voltage, low output current, adjustable linear regulator. It has a wide operating input voltage range of 13.2 - 450V. The output voltage can be adjusted from 1.20 - 440V provided that the input voltage is at least 12V greater than the output voltage. The output voltage can be adjusted by means of two external resistors R1 and R2 as shown in the typical application circuits. The TBR8 regulates the voltage difference between $V_{\rm OUT}$ and ADJ pins to a nominal value of 1.20V. The 1.20V is amplified by the external resistor ratio R1 and R2. An internal constant bias current of typically $10\mu A$ is connected to the ADJ pin. This increases $V_{\rm OUT}$ by a constant voltage of $10\mu A$ times R2.

The TBR8 has current limiting and temperature limiting. The output current limit is typically 20mA and the minimum temperature limit is 125°C. An output short circuit current will therefore be limited to 20mA. When the junction temperature reaches its temperature limit, the output current and/or output voltage will decrease to keep the junction temperature from exceeding its temperature limit. For SMPS start-up circuit applications, the TBR8 turns off when an external voltage greater than the output voltage of the TBR8 is applied to Vout of the TBR8. to maintain stability, a bypass capacitor of $1.0\mu F$ or larger and a minimum DC output current of $500\mu A$ are required.

The device is available in SOT-89, TO-252 (D-PAK), and TO-92 packages.

Typical Application Circuit



Required for conditions where V_{IN} is less than V_{OUT}



Absolute Maximum Ratings

Parameter	Value
V _{IN} input voltage (voltages ref to ADJ)	-0.5V to +480V
Output voltage range	-0.5V to +470V
Operating ambient temperature range	-40°C to +85°C
Operating junction temperature range	-40°C to +125°C
Storage temperature range	-65°C to +150°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Pin Configuration



Electrical Characteristics

(Test conditions unless otherwise specified: -40°C < T_A< 85°C.)

Sym	Parameter	Min	Тур	Max	Units	Conditions
V _{IN} - V _{OUT}	Input to output voltage difference	12	-	450	V	
Vout	Overall output voltage regulation	1.14	1.20	1.26	V	$\begin{aligned} &13.2V < V_{\text{IN}} < 400V, \\ &R1 = 2.4K , R2 = 0 \end{aligned}$
V_{OUT}	Overall output voltage regulation	375	400	426	V	R1 = 2.4K , $R2 = 782K$
Vout	Line regulation	-	0.003	0.01	%/V	$17V < V_{\rm IN} < 400V, \ V_{\rm OUT} = 5V, \label{eq:voltage}$ Iout = 0.5mA
Vout	Load regulation	ı	1.4	3.0	V/0	$V_{IN} = 17V$, $V_{OUT} = 5V$, $0.5mA < I_{OUT} < 10mA$
Vout	Temperature regulation	-1	-	+1	%	$V_{IN} = 17V$, $V_{OUT} = 5V$, $I_{OUT} = 10mA$, $-40 < T_A < 85$
Iout	Output current limit	10	-	30	mA	$T_{\text{J}} < 85$, $V_{\text{IN}} - V_{\text{OUT}} = 12V$
Iout	Output current limit	-	-	0.5	mA	$T_{\rm J} < 125$, $V_{\rm IN} - V_{\rm OUT} = 450V$

Electrical Characteristics (cont.)

(Test conditions unless otherwise specified: -40°C < T_A < 85°C.)

Sym	Parameter	Min	Тур	Max	Units	Conditions
Iout	Minimum output current	ı	0.3	0.5	mA	Includes R1 and load current
I_{ADJ}	Adjust output current	5.0	10	15	uA	
C2	Minimum output load capacitance	1.0	-	-	uF	
DVout/DVin	Ripple rejection ratio	50	60	-	dB	120Hz, V _{ОUТ} = 5V
TLIMIT	Junction temperature limit	125	-	-		

Thermal Characteristics

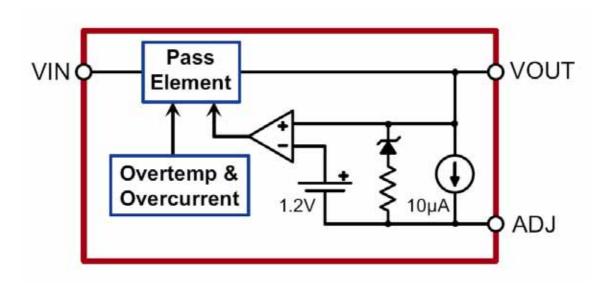
Package	Power Dissipation @ T _A = 25°C	Θ _{jc} °C/W	Θ ja °C/W
TO-92	0.74W	125	170
TO-89	1.6W	15	78†
TO-252	2.5W	6.25	50†

Note:

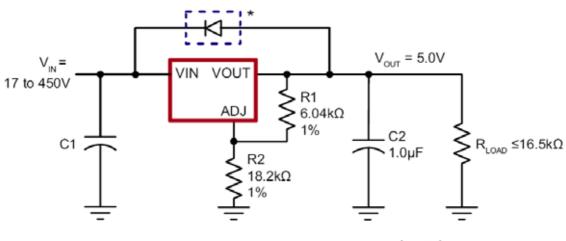
† Mounted on FR4 board, 25mm x 25mm x 1.57mm.



Functional Block Diagram



Typical Application Circuits
Figure 1: High Input Voltage, 5.0V Output Linear Regulator



 $V_{out} = 1.20V \left(1 + \frac{R2}{R1}\right) + I_{AOJ}R2$ Required for conditions where $V_{\text{IN}}\hspace{0.5mm} is \hspace{0.5mm} less \hspace{0.5mm} than \hspace{0.5mm} V_{\text{OUT}}$



Figure 2: SMPS Start-Up Circuit

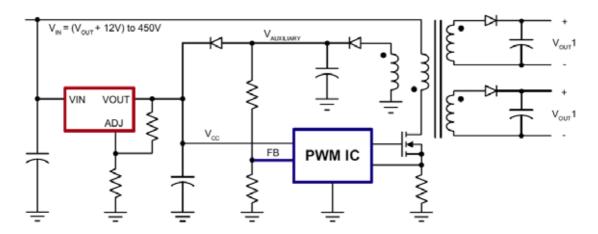
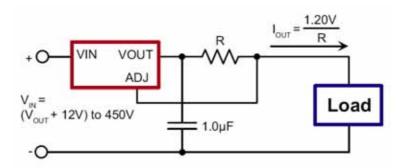
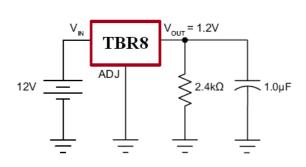


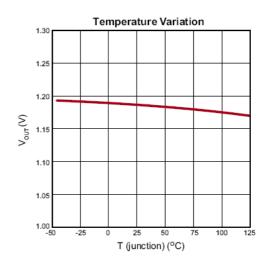
Figure 3: High Voltage Adjustable Constant Current Source

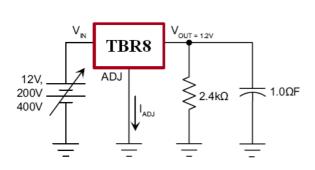


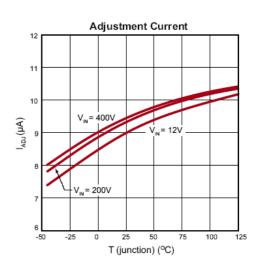


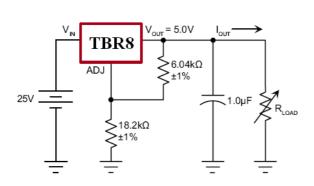
Typical Performance Curves

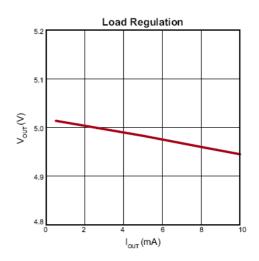






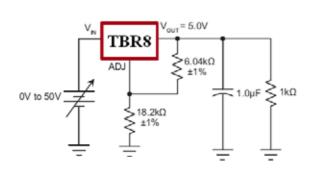


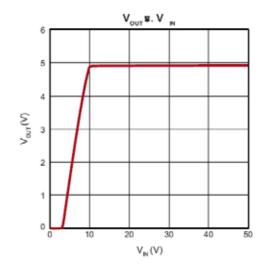


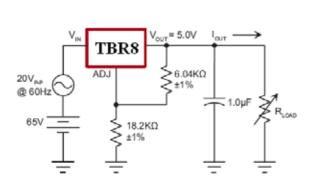


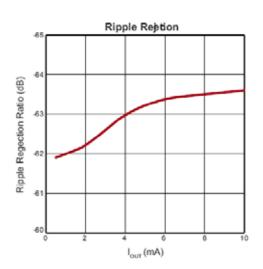


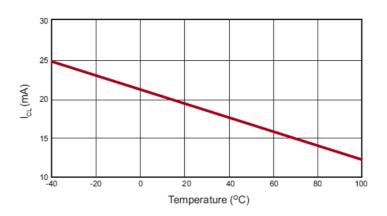
Typical Performance Curves (cont.)







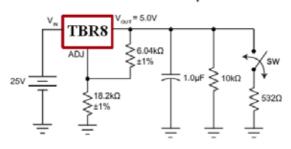






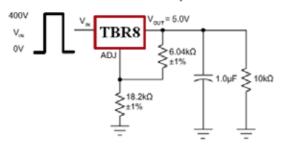
$Typical\ Performance\ Curves\ (cont.)$

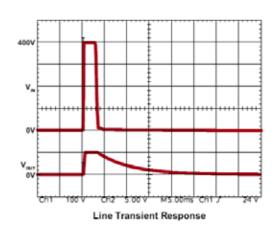
Load Transient Response

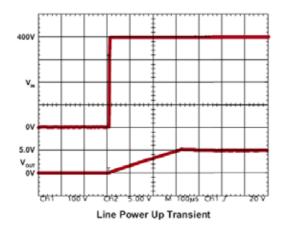


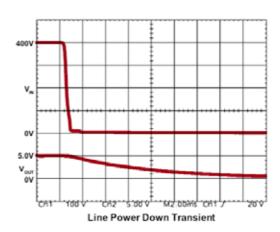


Line Transient Response





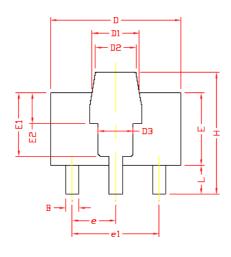


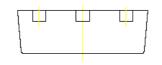


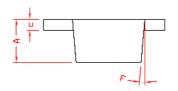


Package Information

SOT-89



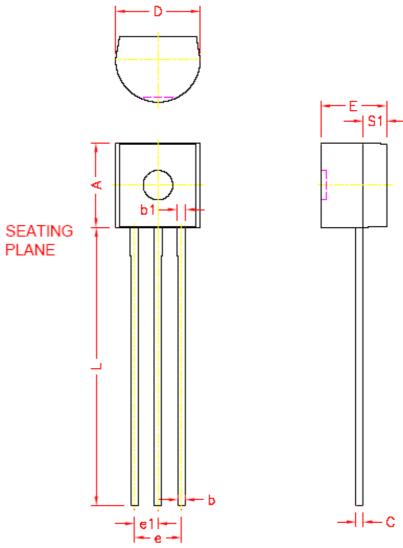




	DIMENSIONS			
REF.	Millimeters			
	Min.	Max.		
Α	1.40	1.60		
В	0.40	0.52		
С	0.35	0.41		
D	4.40	4.60		
D1	1.50	1.70		
D2	1.30	1.50		
D3	1.10	1.30		
Е	2.40	2.60		
E1	2.20	REF.		
E2	1.06	REF.		
е	1.50	REF.		
e1	3.00	REF.		
F	5°	TYP.		
Н	4.05	4.25		
L	0.89	1.20		



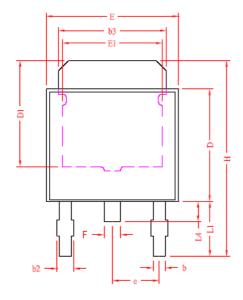
TO-92

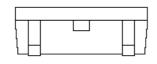


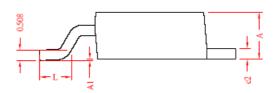
REF.	Millimeter		REF.	Millimeter	
KEF.	Min.	Max.	I KEI	Min.	Max.
Α	4.45	4.7	D	4.44	4.7
Sı	1.02	_	Е	3.30	3.81
b	0.36	0.51	L	12.70	_
bı	0.36	0.76	e1	1.15	1.39
С	0.36	0.51	е	2.42	2.66



TO-252







REF.	Millimeter		REF.	Millimeter		
KEF.	Min.	Max.	KEF.	Min.	Max.	
A	2.20	2.40	Е	6.40	6.80	
A1	0	0.15	E1	3.81		
b	0.50	0.70	е	2.30 REF.		
b2	0.60	0.90	F	0.70	0.90	
b3	5.20	5.50	Н	9.40	10.20	
c2	0.45	0.55	L	1.40	1.77	
D	5.40	5.80	Ll	2.40	3.00	
D1	4.57		L4	0.80	1.20	