



### Super Low Noise GaAs FETs

#### FEATURES

- Low Noise Figure:  
NF = 0.5 dB Typical at 12 GHz
- High Associated Gain:  
Ga = 13 dB Typical at 12 GHz
- $L_g = 0.25 \mu\text{m}$ ,  $W_g = 160 \mu\text{m}$
- All-Gold Metallization for High Reliability
- 100 % DC Tested

#### PHOTO ENLARGEMENT



#### DESCRIPTION

The TC1102 is a GaAs Pseudomorphic High Electron Mobility Transistor (PHEMT) chip, which has very low noise figure and high associated gain. The device can be used in circuits up to 40 GHz and suitable for low noise application including a wide range of commercial and military applications. All devices are 100% DC tested to assure consistent quality. All bond pads are gold plated for either thermo-compression or thermo-sonic wire bonding.

#### ELECTRICAL SPECIFICATIONS ( $T_A=25^\circ\text{C}$ )

Symbol	CONDITIONS	MIN	TYP	MAX	UNIT
NF	Noise Figure at $V_{DS} = 2 \text{ V}$ , $I_{DS} = 10 \text{ mA}$ , $f = 12\text{GHz}$		0.5	0.7	dB
$G_a$	Associated Gain at $V_{DS} = 2 \text{ V}$ , $I_{DS} = 10 \text{ mA}$ , $f = 12\text{GHz}$	11	13		dB
$I_{DSS}$	Saturated Drain-Source Current at $V_{DS} = 2 \text{ V}$ , $V_{GS} = 0 \text{ V}$		40		mA
$g_m$	Transconductance at $V_{DS} = 2 \text{ V}$ , $V_{GS} = 0 \text{ V}$		55		mS
$V_p$	Pinch-off Voltage at $V_{DS} = 2 \text{ V}$ , $I_D = 0.32 \text{ mA}$		-1.0		Volts
$BV_{DGO}$	Drain-Gate Breakdown Voltage at $I_{DGO} = 0.08 \text{ mA}$	5	8		Volts
$R_{th}$	Thermal Resistance		90		$^\circ\text{C/W}$

# TC1102

## ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25 °C)

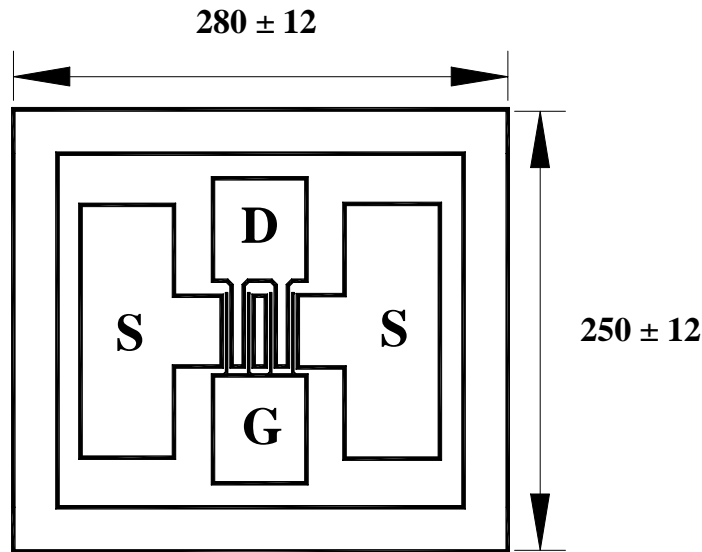
Symbol	Parameter	Rating
V <sub>DS</sub>	Drain-Source Voltage	5 V
V <sub>GS</sub>	Gate-Source Voltage	-3.0 V
I <sub>DS</sub>	Drain Current	I <sub>DSS</sub>
I <sub>GS</sub>	Gate Current	160 μA
P <sub>in</sub>	RF Input Power, CW	14 dBm
P <sub>T</sub>	Continuous Dissipation	150 mW
T <sub>CH</sub>	Channel Temperature	175 °C
T <sub>STG</sub>	Storage Temperature	- 65 °C to +175 °C

## TYPICAL NOISE PARAMETERS (T<sub>A</sub>=25 °C)

V<sub>DS</sub> = 2 V, I<sub>DS</sub> = 10 mA

Frequency (GHz)	NF <sub>opt</sub> (dB)	G <sub>A</sub> (dB)	Γ <sub>opt</sub>		Rn/50
			MAG	ANG	
2	0.30	19.0	0.98	15	0.40
4	0.32	17.4	0.84	30	0.35
6	0.34	15.7	0.68	50	0.26
8	0.37	14.3	0.51	76	0.19
10	0.42	12.9	0.38	107	0.12
12	0.47	11.9	0.28	146	0.08
14	0.56	11.4	0.25	193	0.07
16	0.70	11.2	0.32	250	0.11
18	0.87	10.9	0.49	317	0.23

## CHIP DIMENSIONS



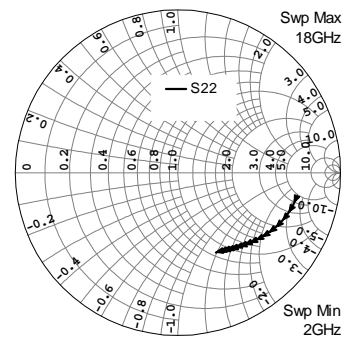
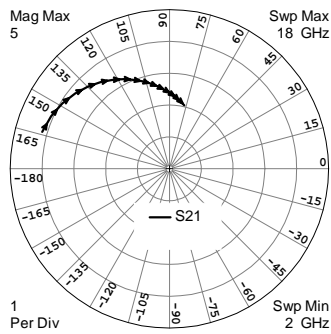
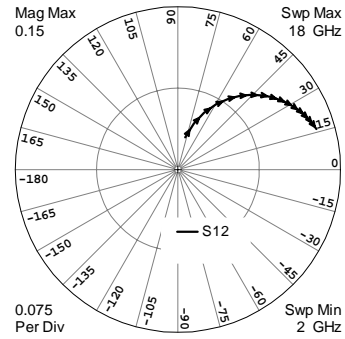
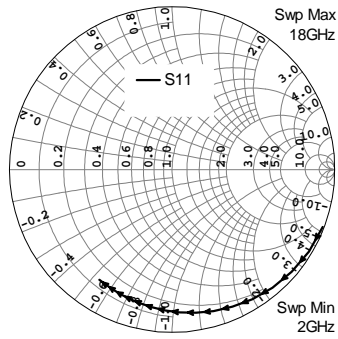
Units: Micrometers  
Chip Thickness: 100

Gate Pad: 55 x 60  
Drain Pad: 55 x 60  
Source Pad: 55 x 14



### TYPICAL SCATTERING PARAMETERS ( $T_A=25^\circ\text{C}$ )

$V_{DS} = 2\text{ V}$ ,  $I_{DS} = 10\text{ mA}$



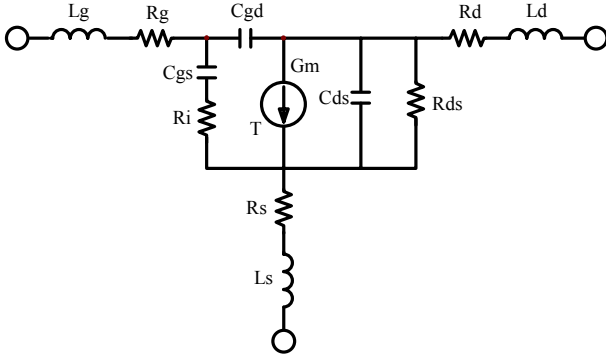
FREQUENCY (GHz)	S11		S21		S12		S22	
	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG
2	0.9909	-20.88	4.1640	163.83	0.0305	77.18	0.7466	-11.01
3	0.9805	-30.94	4.0654	156.02	0.0447	71.04	0.7346	-16.27
4	0.9673	-40.57	3.9385	148.51	0.0577	65.20	0.7193	-21.26
5	0.9524	-49.69	3.7916	141.35	0.0694	59.71	0.7019	-25.95
6	0.9367	-58.26	3.6326	134.56	0.0798	54.59	0.6835	-30.31
7	0.9209	-66.26	3.4682	128.16	0.0888	49.85	0.6650	-34.35
8	0.9057	-73.69	3.3038	122.13	0.0966	45.48	0.6471	-38.08
9	0.8913	-80.57	3.1430	116.45	0.1033	41.45	0.6302	-41.53
10	0.8780	-86.92	2.9885	111.09	0.1090	37.74	0.6146	-44.73
11	0.8659	-92.79	2.8417	106.02	0.1139	34.32	0.6005	-47.70
12	0.8551	-98.22	2.7033	101.22	0.1181	31.16	0.5880	-50.49
13	0.8453	-103.24	2.5737	96.66	0.1216	28.24	0.5770	-53.11
14	0.8366	-107.89	2.4526	92.32	0.1247	25.52	0.5674	-55.59
15	0.8290	-112.20	2.3399	88.16	0.1272	22.98	0.5593	-57.95
16	0.8222	-116.22	2.2349	84.17	0.1295	20.61	0.5525	-60.21
17	0.8163	-119.96	2.1373	80.33	0.1313	18.38	0.5468	-62.39
18	0.8111	-123.45	2.0464	76.64	0.1329	16.28	0.5423	-64.49

• The data does not include gate, drain and source bond wires.

# TC1102

**SMALL SIGNAL MODEL**,  $V_{DS} = 2\text{ V}$ ,  $I_{DS} = 10\text{ mA}$

## SCHEMATIC



## PARAMETERS

Parameters		Parameters	
<b>Lg</b>	0.0384 nH	<b>Rs</b>	1.72 Ohm
<b>Rg</b>	0.97 Ohm	<b>Ls</b>	0.001 nH
<b>Cgs</b>	0.222 pF	<b>Cds</b>	0.061 pF
<b>Ri</b>	1.78 Ohm	<b>Rds</b>	328 Ohm
<b>Cgd</b>	0.0273 pF	<b>Rd</b>	1.698 Ohm
<b>Gm</b>	53.3 mS	<b>Ld</b>	0.0286 nH
<b>T</b>	1.49 psec		

## CHIP HANDLING

**DIE ATTACHMENT:** Conductive epoxy or eutectic die attach is recommended. Eutectic die attach can be accomplished with Au-Sn (80%Au-20%Sn) perform at stage temperature:  $290^{\circ}\text{C} \pm 5^{\circ}\text{C}$ ; Handling Tool: Tweezers; Time: less than 1min.

**WIRE BONDING:** The recommended wire bond method is thermocompression bonding with 0.7 to 1.0 mil (0.018 to 0.025 mm) gold wire. Stage temperature:  $220^{\circ}\text{C}$  to  $250^{\circ}\text{C}$ ; Bond Tip Temperature:  $150^{\circ}\text{C}$ ; Bond Force: 20 to 30 gms depending on size of wire and Bond Tip Temperature.

**HANDLING PRECAUTIONS:** The user must operate in a clean, dry environment. Care should be exercised during handling avoid damage to the devices. Electrostatic Discharge (ESD) precautions should be observed at all stages of storage, handling, assembly, and testing. The static discharge must be less than 300V.