TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

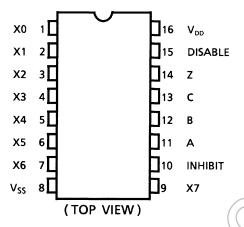
TC4512BP, TC4512BF

TC4512B 8-Channel Data Selector

TC4512B is data selector which selects 8 channel data inputs (X0 through X7) according to binary address inputs A, B and C. Since high impedance can be given to output Z by setting DISABLE input to "H", the wired-OR arrangement can be achieved. DISABLE input takes precedence over other inputs giving the output high impedance.

If DISABLE = "L" and INHIBIT = "H", the data select operation is inhibited and output Z becomes "L" Level.

Pin Assignment

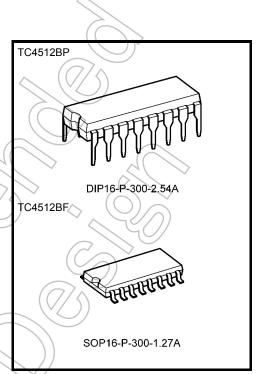


Truth Table

				\ \ / / / / /	
	Inputs				Output
Α	В	C	Inhibit	Disable	Z
L	L	L	L//	L	X0
Н	L	L ^	L	L	X1
L	Н	-	7	L	X2
Н	Н	+		L	X3
L /	_ L ((H)	L	(X4
Н	1	\neq	L 🔿	(¥	X5
(-	H	(±	L		X6
Н	7	Н	Lζ	√/r	X7
*	*	*	Н		L
*	*	*	*	Н	HZ

*: Don't care

HZ: High impedance

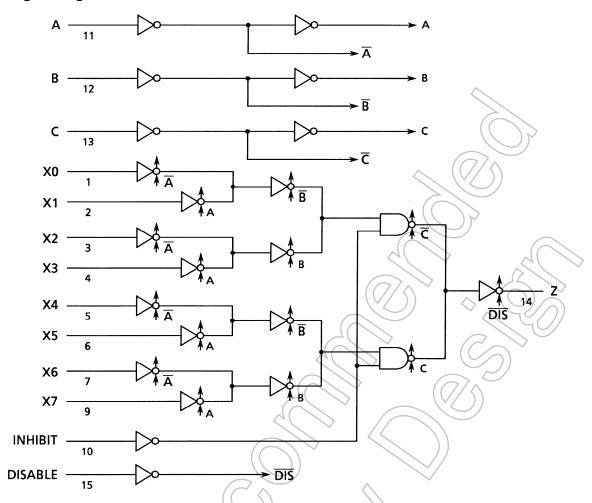


Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

Start of commercial production 1978-04

Logic Diagram



Absolute Maximum Ratings (Note)

_//	11.5	
Symbol	Rating	Unit
V _{DD}	$V_{SS} - 0.5$ to $V_{SS} + 20$	V
V _{IN}	V _{SS} = 0.5 to V _{DD} + 0.5	V
V _{OUT}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
IIN —	±10	mA
PD	300 (DIP)/180 (SOIC)	mW
T _{opr}	-40 to 85	°C
T _{stg}	−65 to 150	°C
	V _{DD} V _{IN} V _{OUT} I _{IN} P _D T _{opr}	V _{DD} (V _{SS} - 0.5 to V _{SS} + 20 V _{IN} (V _{SS} - 0.5 to V _{DD} + 0.5 V _{OUT} (V _{SS} - 0.5 to V _{DD} + 0.5 I _{IN} ±10 P _D 300 (DIP)/180 (SOIC) T _{opr} -40 to 85

Note:

Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V _{IN}	_	0	_	V_{DD}	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics ($V_{SS} = 0 V$)

Observation in		Sym-	Test Condition		-40°C		25°C			85°C		l lmit
Charac	teristics	bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
		V _{OH}	I _{OUT} < 1 μA	5	4.95	_	4.95	5.00	_	4.95	_	
High-level output voltage	10			9.95	_	9.95	10.00 <		9.95	_	V	
ronago			$V_{IN} = V_{SS}, V_{DD}$	15	14.95	_	14.95	15.00		14.95	_	
			lour < 1 uA	5	_	0.05	_	0.00	0.05) }	0.05	
Low-level of voltage	output	V_{OL}	I _{OUT} < 1 μA	10	_	0.05	_	0.00	0.05	/_	0.05	
			$V_{IN} = V_{SS}, V_{DD}$	15	_	0.05	₹\	0.00/	0.05	_	0.05	
			V _{OH} = 4.6 V	5	-0.61	_	-0.51	-1.0		-0.42	_	
			V _{OH} = 2.5 V	5	-2.5	_	-2.1	-4.0	> —	-1.7	_	
Output hig	h current	I _{OH}	V _{OH} = 9.5 V	10	-1.5	_	-1.3	-2.2	_	-1.1	_	mA
			V _{OH} = 13.5 V	15	-4.0	- <	-3.4	9.0	_	2.8	7	
			$V_{IN} = V_{SS}, V_{DD}$,	
		I _{OL}	V _{OL} = 0.4 V	5	0.61	((//	0.51	1.2	-((0.42	<u></u>	mA
Output low	/ current		V _{OL} = 0.5 V	10	1.5		1.3	3.2	(+)	(41)	/ —	
Output low	Current		V _{OL} = 1.5 V	15	4.0		3.4	12.0	> -//	2.8	_	
			$V_{IN} = V_{SS}, V_{DD}$		4()							
		V _{IH}	V _{OUT} = 0.5 V, 4.5 V	5	3.5	>-	3.5	2.75		3.5	_	V
Input high	voltage		V _{OUT} = 1.0 V, 9.0 V	10	7.0	_	7.0	5.5) —	7.0	_	
input nigh	voitage		V _{OUT} = 1.5 V, 13.5 V	15	11,0	-//	11.0	8.25	_	11.0	_	
			I _{OUT} < 1 μA				\					
		V _{IL}	V _{OUT} = 0.5 V, 4.5 V	5	_	1.5		2.25	1.5	_	1.5	V
Input low v	voltago		V _{OUT} = 1.0 V, 9.0 V	_10	_	3.0		4.5	3.0	_	3.0	
input low v	ollage		V _{OUT} = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	
			I _{OUT} < 1 μA		_ <	167,						
Input	"H" level	liH	V _{IH} = 18 V	18		0.1	_	10^{-5}	0.1	_	1.0	μА
current	"L" level	/IL/	V _{IL} = 0 V	18	(7/	<u>\</u> -0.1	_	-10 ⁻⁵	-0.1	_	-1.0	μΑ
3-state output	"H" level	JDH	V _{OH} = 18 V	18		0.4	_	10 ⁻⁴	0.4	_	12	μА
leakage current	"L" level	I _{DL}	V _{OL} ≠ 0 V	18		-0.4	_	-10 ⁻⁴	-0.4	_	-12	μΑ
		2		5	> —	5	_	0.005	5	_	150	
Quiescent current	supply <	IDD	$V_{IN} = V_{SS}, V_{DD}$ (Note)	10	_	10	_	0.010	10	_	300	μΑ
			(Note)	15		20	_	0.015	20	_	600	

Note: All valid input combinations.

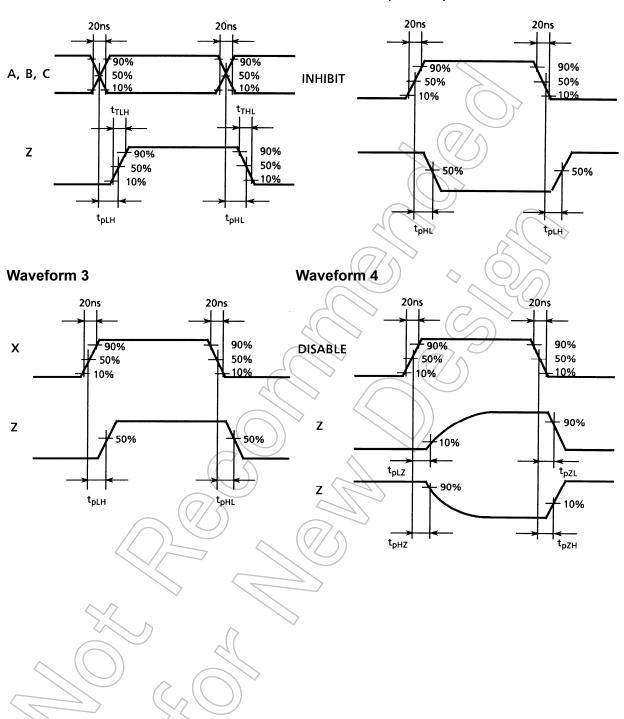
Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Characteristics	Symbol		V _{DD} (V)	IVIIII	ιyp.	IVIAX	Offic
Output transition time			5	_	80	200	
(low to high)	t _{TLH}	_	10	_	50	100	ns
(low to high)			15		40	80	
Output transition time			5		80	200	
(high to low)	t _{THL}	_	10	50	50	100	ns
(ingilite 1611)			15	/ <u>A</u>	40	80	
Propagation delay time	t_pLH	,	5	#	140	280	
(INHIBIT-Z)	t _{pHL}	_	10	\rightarrow -	60	140	ns
(-prii		15	_	40	100	
Propagation delay time	t _{pLH}	$\mathcal{A}($	5	_	240	400	
(A, B, C-Z)	t _{pHL}	-	10	-	95	170	ns
	Pile	(0/1)	15	7	65	2 120	
Propagation delay time	t _{pLH}		5	7	210	360	
(X-Z)	t _{pHL}		10		85	150	ns
	Pile	4	15 ((60	110	
Three state disable time	t _{pZL} , t _{pLZ}		5		60	120	
(DISABLE-Z)	t _{pHZ} , t _{pZH}	$R_L = 1 k\Omega$	(10//) —	25	60	ns
,			15	/ —	20	40	
Input capacitance	C _{IN}	\\ \ \ \		—	5	7.5	pF

Waveforms for Measurement of Dynamic Characteristics

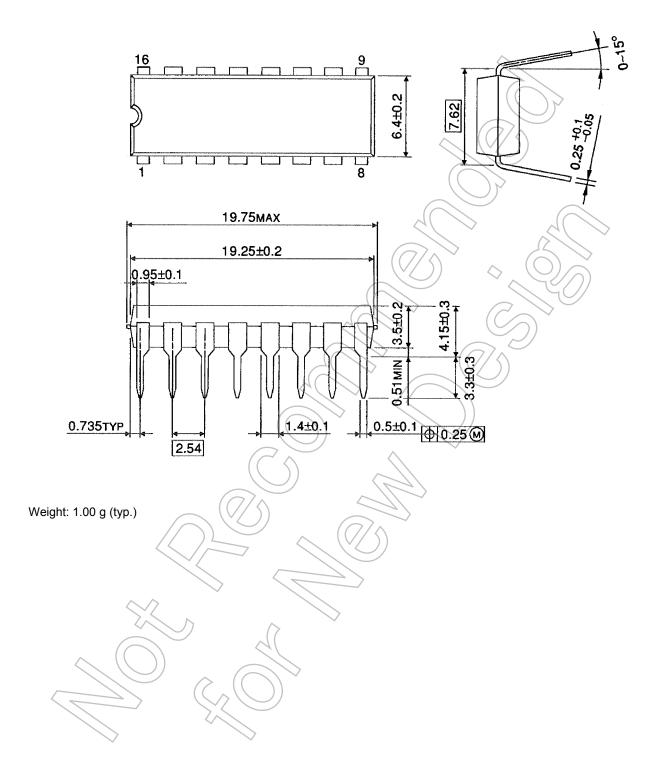
Waveform 1

Waveform 2 (X = "H")



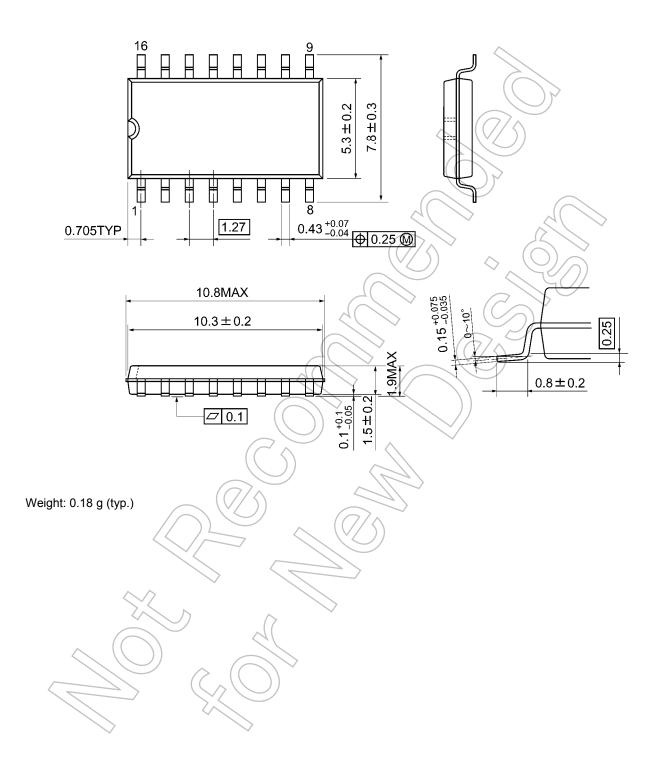
Package Dimensions

DIP16-P-300-2.54A Unit: mm



Package Dimensions

SOP16-P-300-1.27A Unit: mm



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