TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4538BP, TC4538BF

### TC4538BP/TC4538BF Dual Precision

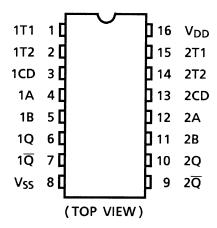
#### Retriggerable/Resettable Monostable Multivibrator

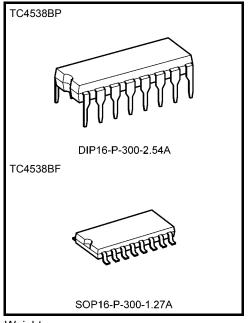
The TC4538BP/BF is the retriggerable/resettable monostable multivibrator and the trigger operation can be made at either the leading or trailing edge by 2 inputs of A and B. Since the output monostable pulse width is decided by time constant of the external resistor (Rx) and the external capacitor (Cx), it becomes possible to set a broad range of output pulse widths.

#### **Features**

•  $t_{WOUT} = 10 \text{ ms} \pm 5\% \text{ (at } RX = 100 \text{ k}\Omega \text{ CX} = 0.1 \text{ }\mu\text{F}, \text{VDD} = 10 \text{ V)}$ 

#### **Pin Assignment**





Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

#### **Truth Table (Note)**

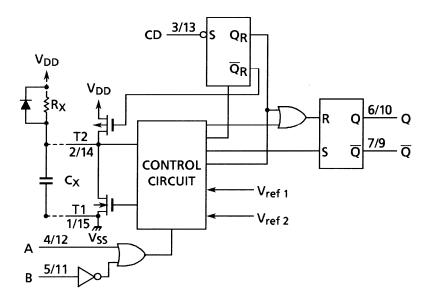
	Inputs		Out	puts	Note			
Α	В	CD	Q	Q				
	Н	Н	Л	П	Output Enable			
	L	Н	L	Н	Inhibit			
Н	$\neg$	Н	L	Н	Inhibit			
L	$\neg$	Н		Ţ	Output Enable			
*	*	L	L	Н	Inhibit			

\*: Don't care

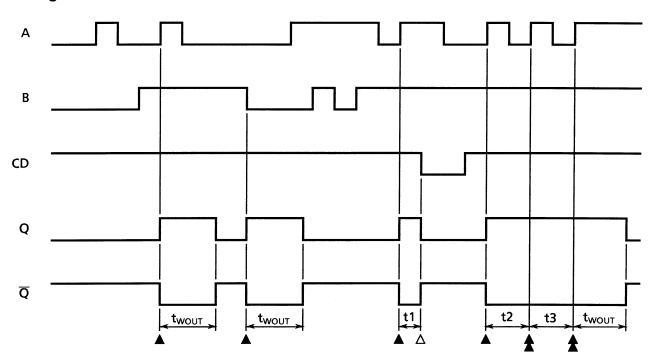
Note: In the case of using only one circuit, CD should be tied to GND,  $T_2$ ,  $T_1$ , Q,  $\overline{Q}$  should be tied to OPEN, and the other inputs should be tied to  $V_{CC}$  or GND.

# **Logic Diagram**

#### 1/2 TC4538BP/BF



### **Timing Chart**



▲: TRIGGER

**★**: RETRIGGER

△: RESET

 $t_{WOUT} = C_X \cdot R_X$ 

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 $t1\cdot t2\cdot t3\;;\quad t1\cdot t2\cdot t3\!<\!t_{WOUT}$ 

#### **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
DC supply voltage	$V_{DD}$	$V_{SS}$ – 0.5 to $V_{SS}$ + 20	V
Input voltage	V <sub>IN</sub>	V <sub>SS</sub> – 0.5 to V <sub>DD</sub> + 0.5	V
Output voltage	V <sub>OUT</sub>	V <sub>SS</sub> – 0.5 to V <sub>DD</sub> + 0.5	٧
DC input current	I <sub>IN</sub>	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T <sub>opr</sub>	−40 to 85	°C
Storage temperature range	T <sub>stg</sub>	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Operating Ranges (V<sub>SS</sub> = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	$V_{DD}$	_	3	_	18	V
Input voltage	$V_{IN}$	_	0	_	$V_{DD}$	V
External resistance	$R_X$	_	5	_	1000	kΩ
External capacitance	C <sub>X</sub>			No limits	i	μF

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

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# Static Electrical Characteristics ( $V_{SS} = 0 V$ )

Characteristics		Sym-	Test Condition		-40°C		25°C			85°C		11. "
		bol		V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level output voltage		V <sub>OH</sub>	$ I_{OUT}  < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5	4.95	_	4.95	5.00	_	4.95	_	
				10	9.95	_	9.95	10.00	_	9.95	_	V
			VIN - VSS, VDD	15	14.95	_	14.95	15.00	_	14.95	_	
			I <sub>OUT</sub>   < 1 μA	5	_	0.05	_	0.00	0.05	_	0.05	
Low-level voltage	output	V <sub>OL</sub>	$V_{IN} = V_{SS}, V_{DD}$	10	_	0.05	_	0.00	0.05		0.05	V
			VIIV - V35, VDD	15	_	0.05	_	0.00	0.05		0.05	
			V <sub>OH</sub> = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_	
			V <sub>OH</sub> = 2.5 V	5	-2.50	_	-2.10	-4.0	_	-1.70	_	mA
Output hig	gh current	IOH	V <sub>OH</sub> = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_	
			V <sub>OH</sub> = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_	
			$V_{IN} = V_{SS}, V_{DD}$									
			V <sub>OL</sub> = 0.4 V	5	0.61	_	0.51	1.5	_	0.42	_	
Output lov	v current	I <sub>OL</sub>	V <sub>OL</sub> = 0.5 V	10	1.50	_	1.30	3.8	_	1.10	_	mA
Output lov	Vourient		V <sub>OL</sub> = 1.5 V	15	4.00	_	3.40	15.0	_	2.80	_	
			$V_{IN} = V_{SS}, V_{DD}$									
		VIH	V <sub>OUT</sub> = 0.5 V, 4.5 V	5	3.5	_	3.5	2.75	_	3.5	_	V
Input high	voltage		V <sub>OUT</sub> = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	_	
input nign	voitage		V <sub>OUT</sub> = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_	
			$ I_{OUT}  < 1 \mu A$									
			V <sub>OUT</sub> = 0.5 V, 4.5 V	5	_	1.5	_	2.25	1.5	_	1.5	
Input low y	lanut law voltage		V <sub>OUT</sub> = 1.0 V, 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0	V
Input low voltage		V <sub>IL</sub>	V <sub>OUT</sub> = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	
			$ I_{OUT}  < 1 \mu A$									
Input	"H" level	l <sub>IH</sub>	V <sub>IH</sub> = 18 V	18	_	0.1		10 <sup>-5</sup>	0.1	_	1.0	μА
current	"L" level	IJL	V <sub>IL</sub> = 0 V	18	_	-0.1	_	$-10^{-5}$	-0.1	_	-1.0	μΛ
			$V_{IN} = V_{SS}, V_{DD}$ (Note)	5		5	_	0.005	5	_	150	
Quiescent supply current		I <sub>DD</sub>		10	_	10	_	0.010	10	_	300	μА
			(.1010)	15		20	_	0.015	20	_	600	

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Note: All valid input combinations.

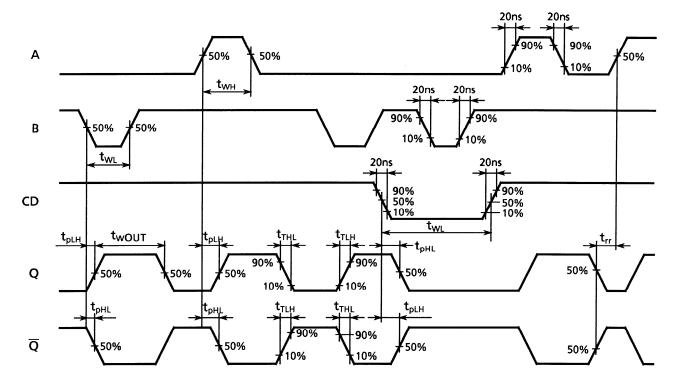
# Dynamic Electrical Characteristics (Ta = 25°C, $V_{SS}$ = 0 V, $C_L$ = 50 pF)

Characteristics	Symbol	Test Condition		Min	Тур.	Max	Unit	
Cital acteristics	Symbol		V <sub>DD</sub> (V)	IVIIII	τyp.	IVIAX	Offic	
Output transition time			5	_	80	200		
(low to high)	t <sub>TLH</sub>	_	10	_	50	100	ns	
(low to flight)			15	_	40	80		
Output transition time			5	_	80	200		
(high to low)	$t_{THL}$	_	10	_	50	100	ns	
(ingit to low)			15	_	40	80		
Propagation delay time	t		5	_	380	760		
(A, B-Q, $\overline{Q}$ )	t <sub>pLH</sub>	_	10	_	150	300	ns	
(A, b-Q, Q)	t <sub>pHL</sub>		15	_	100	220		
Propagation delay time	<b>.</b>		5	_	280	560		
(CD-Q, $\overline{Q}$ )	t <sub>pLH</sub>	_	10	_	110	250	ns	
(CD-Q, Q)	t <sub>pHL</sub>		15	_	75	190		
Min input pulse width	torus		5	_	60	120		
(A, B)	t <sub>WH</sub>	_	10	_	30	60	ns	
(^, b)	t₩L		15	_	25	50		
Min pulse width			5	_	95	190	ns	
(CD)	$t_{WL}$	_	10	_	45	90		
(CD)			15	_	35	70		
			5	_	0	_		
Min retrigger time	t <sub>rr</sub>	_	10	_	0	_	ns	
			15	_	0	_		
		R <sub>X</sub> = 100 kΩ	5	_	206	_		
	t <sub>wOUT</sub>	$R_X = 100 \text{ kg}^2$ $C_X = 0.002 \mu\text{F}$	10	_	204	_	μS	
		Cχ = 0.002 μι	15		205	_		
		R <sub>X</sub> = 100 kΩ	5	9.30	9.95	10.40		
Output pulse width		$C_X = 0.1  \mu F$	10	9.50	10.00	10.50	ms	
		Cχ = 0.1 μι	15	9.55	10.05	10.65		
		R <sub>X</sub> = 100 kΩ	5	_	0.98	_		
		$C_X = 100 \text{ k}\Omega$	10	_	1.00	_	s	
		Οχ = 10 μι	15		1.01	_		
	Δt <sub>wOUT</sub>	t (O2) t (O4)	5	_	±1	_		
Pulse width match between circuits in the same package		$\frac{t_{WOUT}(Q2) - t_{W}(Q1)}{t_{WOUT}(Q1)} \times 100$	10	_	±1	_	%	
. ,		woor ( /	15		±1	_		
Input capacitance	C <sub>IN</sub>	_			5	7.5	pF	

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# **Waveform for Measurement of Dynamic Characteristics**

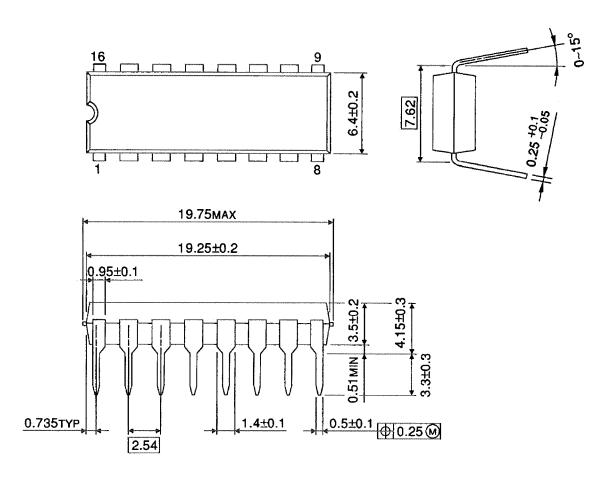
#### Waveform



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# **Package Dimensions**

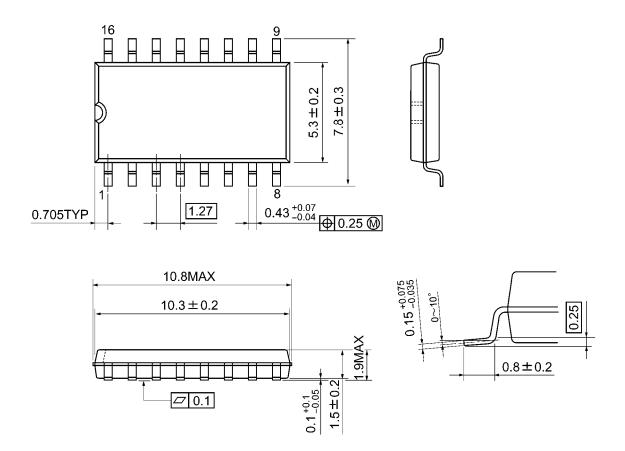
DIP16-P-300-2.54A Unit: mm



Weight: 1.00 g (typ.)

# **Package Dimensions**

SOP16-P-300-1.27A Unit: mm



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Weight: 0.18 g (typ.)

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