TENTATIVE

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

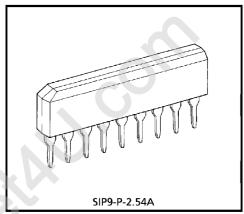
TC5081BP

PHASE COMPARATOR

The TC5081BP is phase comparator for PLL frequency synthesizer type, and consists of a digital phase comparator and an amplifier for active low pass filter.

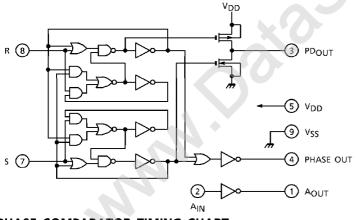
FEATURES

- The phase comparator detects two input pulse phase differences and outputs proportionate positive or negative pulses to PD_{OUT}. When the input pulse phases are the same, PD_{OUT} has high impedance.
- Because the IC is CMOS, the input impedance of the filter for the amp is extremely high and has excellent characteristics.
- TC5081BP comes in a SIP 9 PIN.

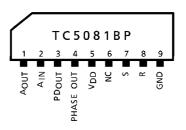


Weight: 0.92g (Typ.)

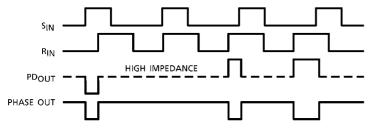
LOGIC DIAGRAM



PIN CONNECTION (SIDE VIEW)



PHASE COMPARATOR TIMING CHART



Sheeth) con

[■] TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

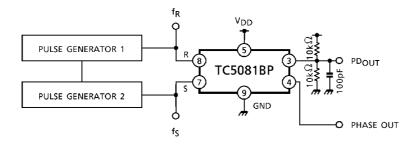
MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V_{DD}	15	٧
Input Voltage	VIN	$-0.3 \sim V_{DD} + 0.3$	>
Operating Temperature	T _{opr}	- 30∼75	°C
Storage Temperature	T _{stg}	- 55~125	°C

ELECTRICAL CHARACTERISTICS ($V_{DD} = 7.5V$, $T_{a} = -30 \sim 75$ °C)

CHARACTE	ERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Operating Supp	ly Voltage	V_{DD}	_	_		4.5	_	12	٧
LOUTDUT VOITAGE —	"H" Level	Voн	_	$V_{IH} = 6.6V$,	$I_{OH} = -50\mu A$	7.3	_	_	٧
	"L" Level	VOL		V _{IL} = 1.6V	I _{OL} = 50μA	_	_	0.2	٧
Quiescent Current		I _{DD}	_	$V_{IH} = 7.5V, V_{IL} = 0V$		_	_	200	μ A
15 State Ecak	"H" Level	ITLH				_	_	500	nA
	"L" Level	I _{TLL}		1			_	- 500	nΑ
Filter Amp. Voltage Gain		Gγ	3	$R_{\text{1}-\text{2}} = 1M\Omega$, $f_{\text{1N}} = 1kHz$ $R_{\text{g}} = 600\Omega$		_	30	_	dB

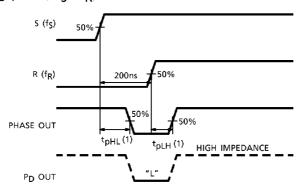
TEST CIRCUIT 1

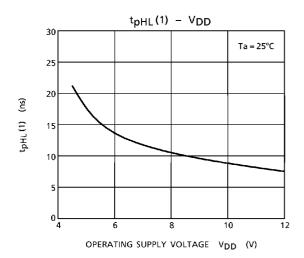


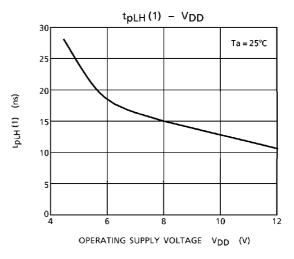
The pulse generator-1 is synchronized with the pulse generator-2. Then, the phase of f_R and f_S is variable.

The products described in this document are subject to foreign exchange and foreign trade control laws.
The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
The information contained herein is subject to change without notice.

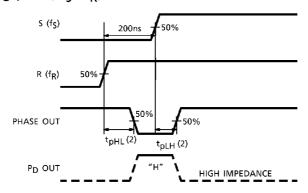
WAVE FORM 1 (The leading phase, $f_S > f_R$)

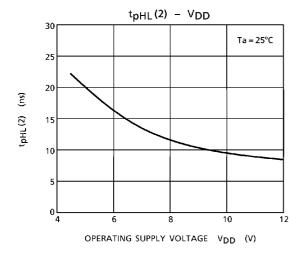


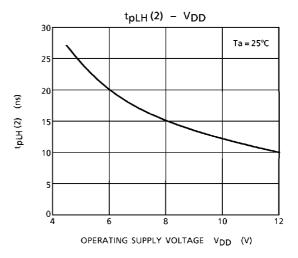




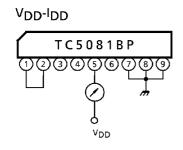
WAVE FORM 2 (The lagging phase, f₅<f_R)



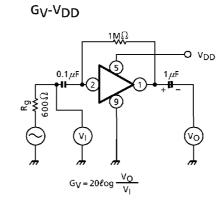


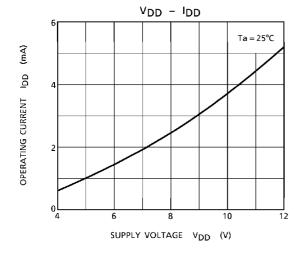


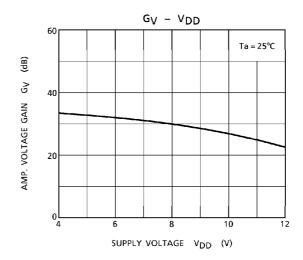
TEST CIRCUIT 2



TEST CIRCUIT 3

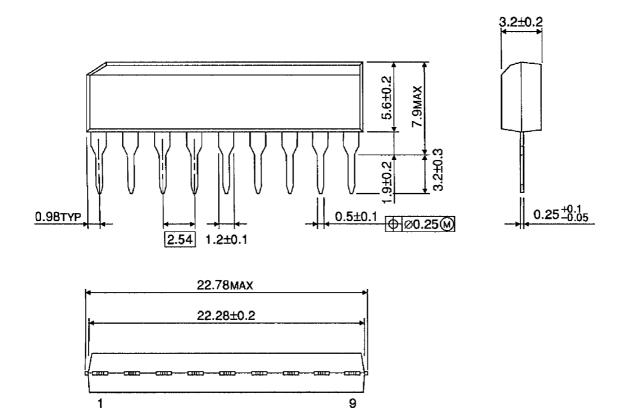






OUTLINE DRAWING

SIP9-P-2.54A Unit: mm



Weight: 0.92g (Typ.)