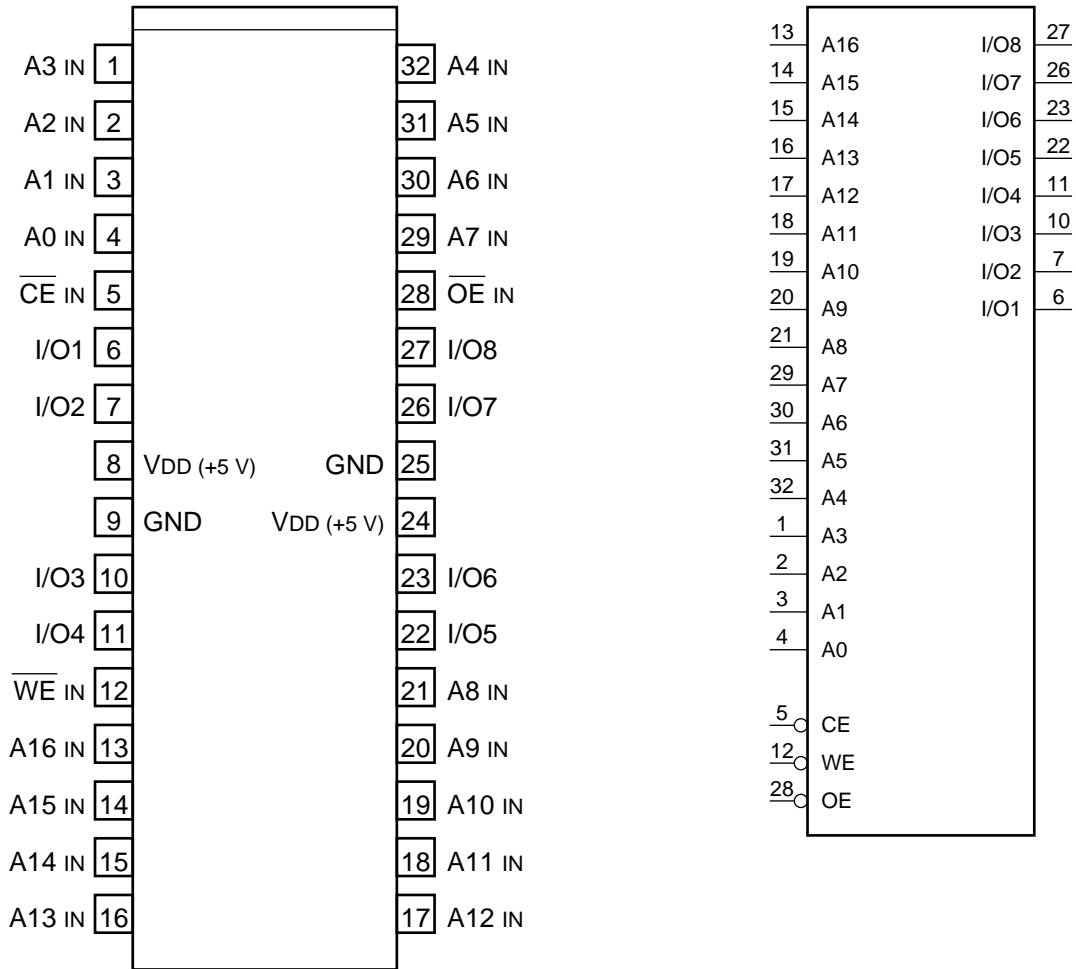

C-MOS 1M (131,072x8)-BIT STATIC RAM
- TOP VIEW -



INPUT

- A0 - A16 ; ADDRESS
- \overline{CE} ; CHIP ENABLE
- \overline{OE} ; OUTPUT BUFFER CONTROL
- \overline{WE} ; WRITE ENABLE

INPUT/OUTPUT

I/O1 - I/O8 ; DATA

ACTIVE MODE	\overline{CE}	\overline{OE}	\overline{WE}	I/O	POWER
READ	0	0	1	D OUT	I _{DDO}
WRITE	0	X	0	D IN	I _{DDO}
OUTPUT DISABLE	0	1	1	HI-Z	I _{DDO}
STANDBY	1	X	X	HI-Z	I _{DDS}

- 0 ; LOW LEVEL
- 1 ; HIGH LEVEL
- HI-Z; HIGH IMPEDANCE
- X ; DON'T CARE

