

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX16240AFT

Low-Voltage 16-Bit Bus Buffer (inverted) with 5-V Tolerant Inputs and Outputs

The TC74LCX16240AFT is a high-performance CMOS 16-bit bus buffer. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

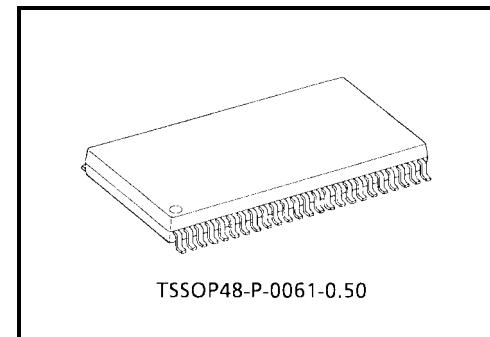
The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for both inputs and outputs.

This device is inverting 3-state buffer having four active-low output enables. It can be used as four 4-bit buffers two 8-bit buffers or one 16-bit buffer. When the \overline{OE} input is high, the outputs are in a high-impedance state. This device is designed to be used with 3-state memory address drivers, etc.

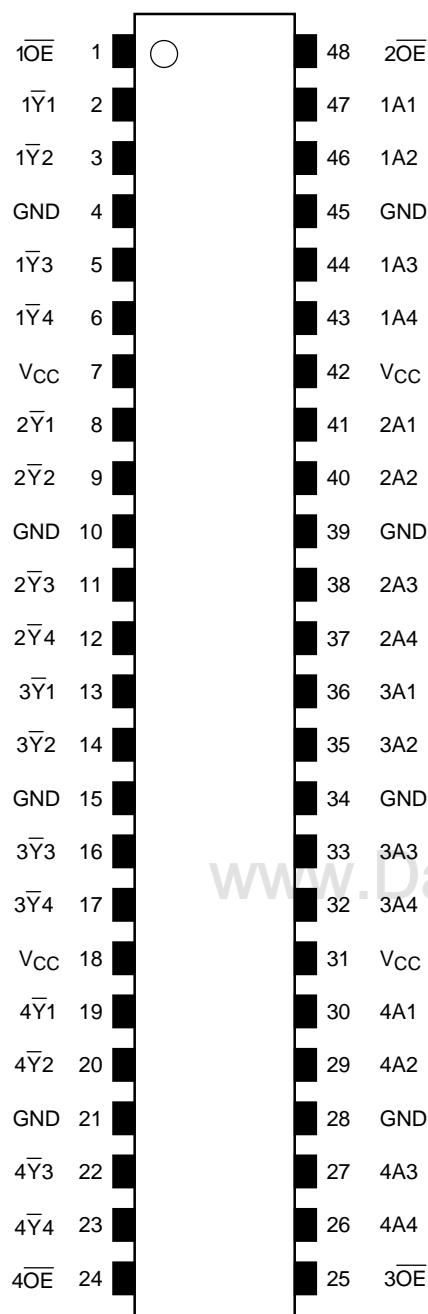
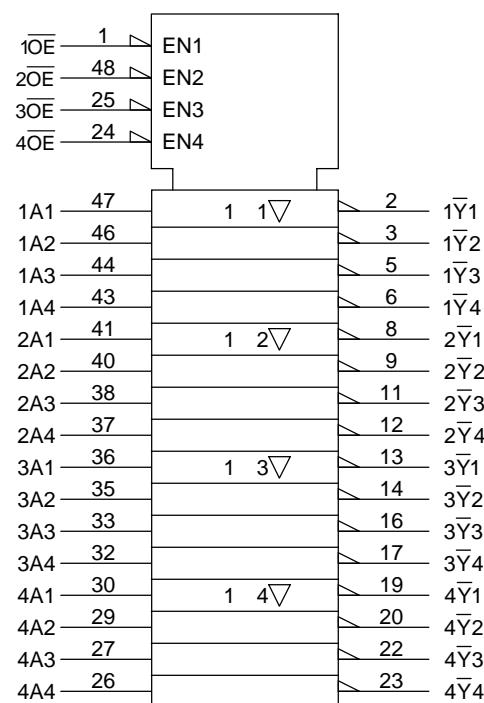
All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: $V_{CC} = 2.0$ to 3.6 V
- High-speed operation: $t_{pd} = 4.9\text{ ns (max)}$ ($V_{CC} = 3.0$ to 3.6 V)
- Output current: $|I_{OH}|/I_{OL} = 24\text{ mA (min)}$ ($V_{CC} = 3.0\text{ V}$)
- Latch-up performance: $\pm 500\text{ mA}$
- Package: TSSOP (thin shrink small outline package)
- Power-down protection provided on all inputs and outputs



Weight: 0.25 g (typ.)

Pin Assignment (top view)**IEC Logic Symbol**

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Truth Table

Inputs		Outputs
$1\overline{OE}$	1A1-1A4	$1\overline{Y}_1 - 1\overline{Y}_4$
L	L	H
L	H	L
H	X	Z

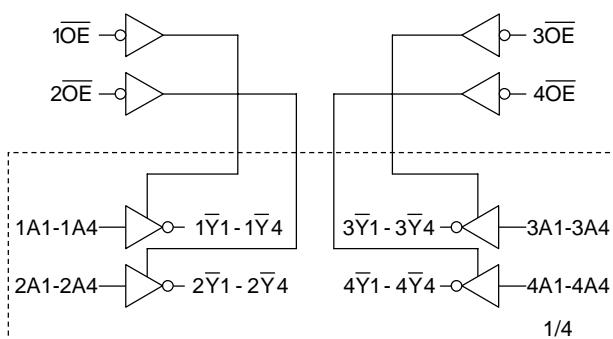
Inputs		Outputs
$2\overline{OE}$	2A1-2A4	$2\overline{Y}_1 - 2\overline{Y}_4$
L	L	H
L	H	L
H	X	Z

Inputs		Outputs
$3\overline{OE}$	3A1-3A4	$3\overline{Y}_1 - 3\overline{Y}_4$
L	L	H
L	H	L
H	X	Z

Inputs		Outputs
$4\overline{OE}$	4A1-4A4	$4\overline{Y}_1 - 4\overline{Y}_4$
L	L	H
L	H	L
H	X	Z

X: Don't care

Z: High impedance

System Diagram

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Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5 to 7.0	V
Input voltage	V _{IN}	-0.5 to 7.0	V
Output voltage	V _{OUT}	-0.5 to 7.0 (Note 1)	V
		-0.5 to V _{CC} + 0.5 (Note 2)	
Input diode current	I _{IK}	-50	mA
Output diode current	I _{OK}	±50 (Note 3)	mA
DC output current	I _{OUT}	±50	mA
Power dissipation	P _D	400	mW
DC V _{CC} /ground current per supply pin	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Output in OFF state

Note 2: High or low state. I_{OUT} absolute maximum rating must be observed.Note 3: V_{OUT} < GND, V_{OUT} > V_{CC}**Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	2.0 to 3.6	V
		1.5 to 3.6 (Note 4)	
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to 5.5 (Note 5)	V
		0 to V _{CC} (Note 6)	
Output current	I _{OH} /I _{OL}	±24 (Note 7)	mA
		±12 (Note 8)	
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 9)	ns/V

Note 4: Data retention only

Note 5: Output in OFF state

Note 6: High or low state

Note 7: V_{CC} = 3.0 to 3.6 VNote 8: V_{CC} = 2.7 to 3.0 VNote 9: V_{IN} = 0.8 to 2.0 V, V_{CC} = 3.0 V

Electrical Characteristics**DC Characteristics (Ta = -40 to 85°C)**

Characteristics		Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit	
Input voltage	H-level		—	2.7 to 3.6					
	L-level	V _{IL}	—	2.7 to 3.6	—	—	0.8		
Output voltage	H-level	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 µA	2.7 to 3.6	V _{CC} -0.2	—	V	
				I _{OH} = -12 mA	2.7	2.2	—		
				I _{OH} = -18 mA	3.0	2.4	—		
				I _{OH} = -24 mA	3.0	2.2	—		
	L-level	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 µA	2.7 to 3.6	—	0.2		
				I _{OL} = 12 mA	2.7	—	0.4		
				I _{OL} = 16 mA	3.0	—	0.4		
				I _{OL} = 24 mA	3.0	—	0.55		
Input leakage current		I _{IN}	V _{IN} = 0 to 5.5 V		2.7 to 3.6	—	±5.0	µA	
3-state output OFF state current		I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = 0 to 5.5 V		2.7 to 3.6	—	±5.0	µA	
Power-off leakage current		I _{OFF}	V _{IN} /V _{OUT} = 5.5 V		0	—	10.0	µA	
Quiescent supply current		I _{CC}	V _{IN} = V _{CC} or GND V _{IN} /V _{OUT} = 3.6 to 5.5 V		2.7 to 3.6	—	20.0	µA	
Increase in I _{CC} per input		ΔI _{CC}	V _{IH} = V _{CC} - 0.6 V		2.7 to 3.6	—	500		

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AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.7	—	5.9	ns
	t _{pHL}		3.3 ± 0.3	1.5	4.9	
3-state output enable time	t _{pZL}	Figure 1, Figure 3	2.7	—	7.5	ns
	t _{pZH}		3.3 ± 0.3	1.5	6.5	
3-state output disable time	t _{pLZ}	Figure 1, Figure 3	2.7	—	6.5	ns
	t _{pHZ}		3.3 ± 0.3	1.5	5.5	
Output to output skew	t _{osLH}	(Note 10)	2.7	—	—	ns
	t _{osHL}		3.3 ± 0.3	—	1.0	

Note 10: Parameter guaranteed by design.

$$(t_{osLH} = |t_{pLHm} - t_{pLHn}|, t_{osHL} = |t_{pHLM} - t_{pHLn}|)$$

Dynamic Switching Characteristics
(Ta = 25°C, input: t_r = t_f = 2.5 ns, C_L = 50 pF, R_L = 500 Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Typ.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	0.8	V

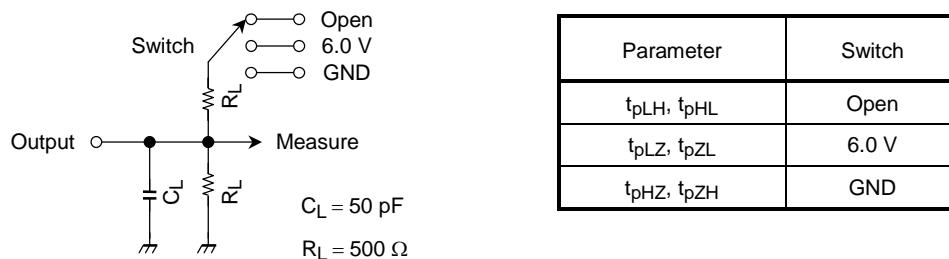
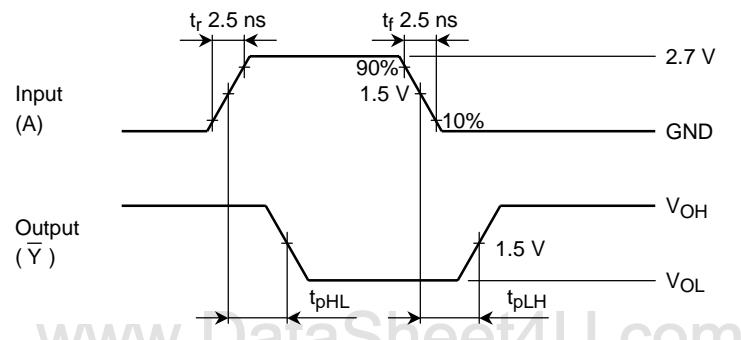
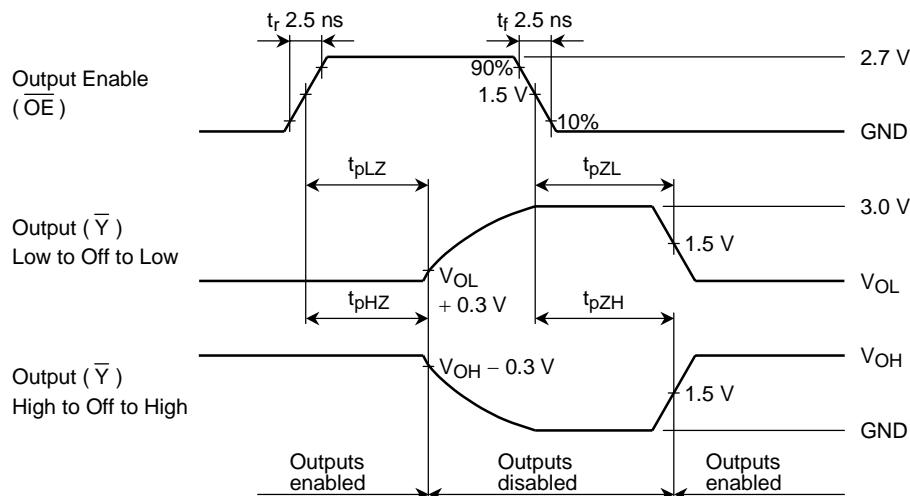
Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Typ.	Unit	
Input capacitance	C _{IN}	—	3.3	7	pF	
Output capacitance	C _{OUT}	—	3.3	8	pF	
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz	(Note 11)	3.3	25	pF

Note 11: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

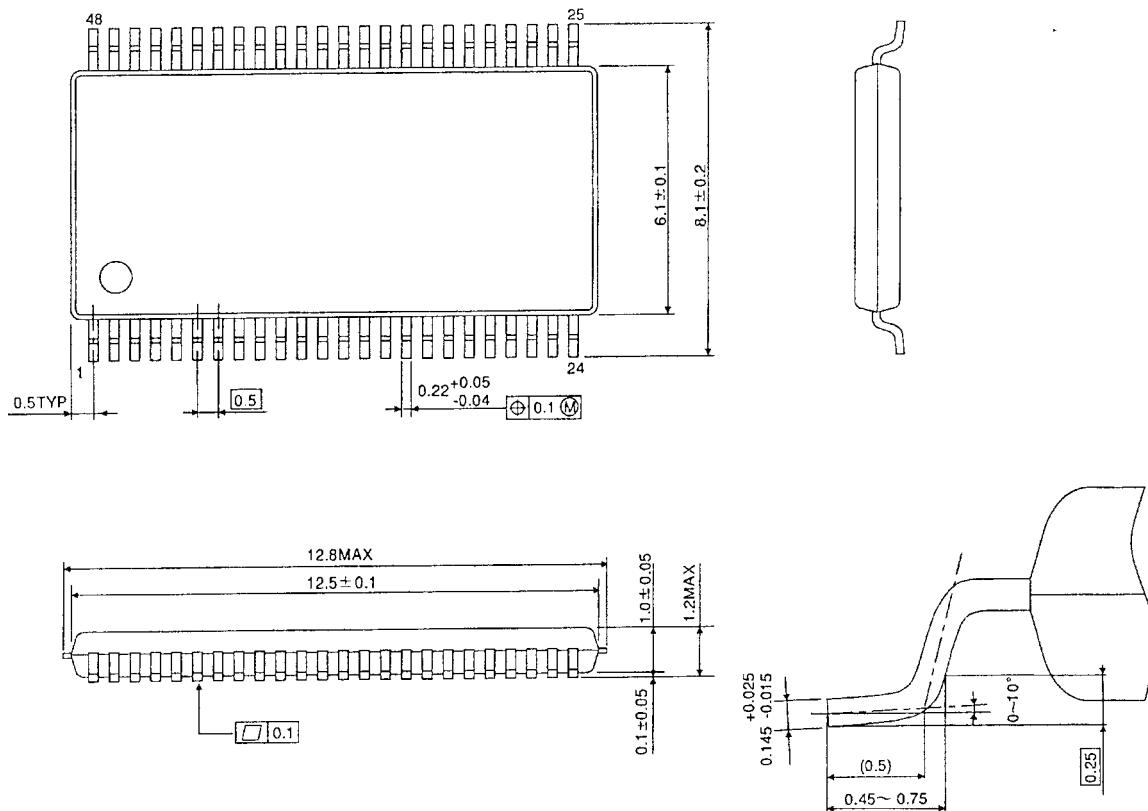
$$I_{CC\ (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/16 \text{ (per bit)}$$

AC Test Circuit**Figure 1****AC Waveform****Figure 2 t_{pLH}, t_{pHL}** **Figure 3 $t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}$**

Package Dimensions

TSSOP48-P-0061-0.50

Unit : mm



Weight: 0.25 g (typ.)

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