

CMOS Digital Integrated Circuits Silicon Monolithic

# TC74VCX157FT

### 1. Functional Description

· Low-Voltage Quad 2-Channel Multiplexer with 3.6-V Tolerant Inputs and Outputs

#### 2. General

The TC74VCX157FT is a high performance CMOS multiplexer which is guaranteed to operate from 1.2 V to 3.6 V. Designed for use in 1.5 V, 1.8 V, 2.5 V or 3.3 V systems, it achieves high, speed operation while maintaining the CMOS low power dissipation.

It is also designed with over voltage tolerant inputs and outputs up to 3.6 V.

It consists of four 2-input digital multiplexers with common select and strobe inputs.

When the  $\overline{ST}$  input is held "H" level, selection of data is inhibited and all the outputs become "L" level. The SELECT decoding determines whether the A or B inputs get routed to their corresponding Y outputs.

All inputs are equipped with protection circuits against static discharge.

#### 3. Features

- (1) Wide operating temperature range:  $T_{opr} = -40$  to 125 °C (Note 1)
- (2) Low-voltage operation:  $V_{CC} = 1.2$  to 3.6 V
- (3) High-speed operation:  $t_{pd} = 3.0 \text{ ns (max)} (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$

$$t_{pd}$$
 = 3.5 ns (max) (V $_{CC}$  = 2.3 to 2.7 V)

$$t_{pd} = 7.0 \text{ ns (max)} (V_{CC} = 1.65 \text{ to } 1.95 \text{ V})$$

$$t_{pd} = 14.0 \text{ ns (max) (V}_{CC} = 1.4 \text{ to } 1.6 \text{ V})$$

$$t_{pd} = 35.0 \text{ ns (max) (V}_{CC} = 1.2 \text{ V)}$$

(4) Output current:  $I_{OH}/I_{OL} = \pm 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$ 

$$I_{OH}/I_{OL} = \pm 18 \text{ mA (min)} (V_{CC} = 2.3 \text{ V})$$

$$I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.65 \text{ V)}$$

$$I_{OH}/I_{OL} = \pm 2$$
 mA (min) ( $V_{CC} = 1.4$  V)

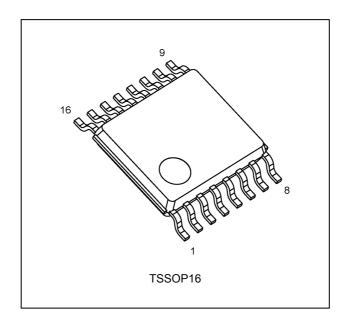
- (5) Latch-up performance: -300 mA
- (6) ESD performance: Human Body Model ≥ ±2000 V
- (7) 3.6 V tolerant function and power-down protection provided on all inputs and outputs.

Note 1: Operating Range spec of  $T_{opr}$  = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

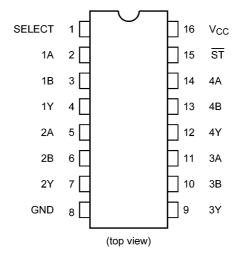
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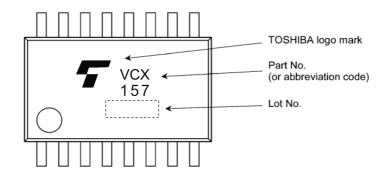
# 4. Packaging



# 5. Pin Assignment



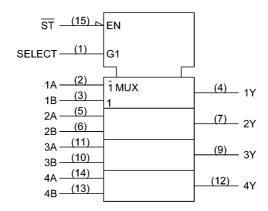
# 6. Marking



Rev.2.0



# 7. IEC Logic Symbol

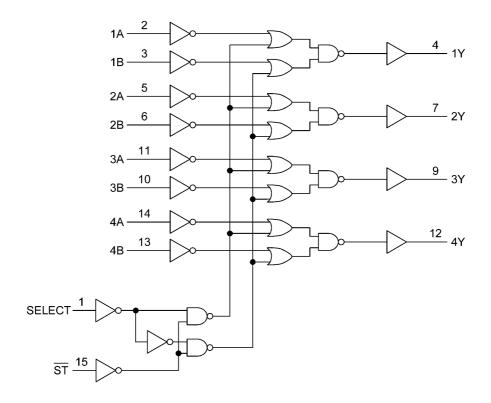


### 8. Truth Table

Inputs ST	Inputs SELECT	Inputs A	Inputs B	Outputs Y
Н	Х	Х	Х	L
L	L	L	Х	L
L	L	Н	Х	Н
L	Н	Χ	L	L
L	Н	X	Н	Н

X: Don't care

# 9. System Diagram



Rev.2.0



### 10. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		-0.5 to 4.6	V
Input voltage	V <sub>IN</sub>		-0.5 to 4.6	V
Output voltage	V <sub>OUT</sub>	(Note 1)	-0.5 to 4.6	V
		(Note 2)	-0.5 to V <sub>CC</sub> + 0.5	
Input diode current	I <sub>IK</sub>		-50	mA
Output diode current	I <sub>OK</sub>	(Note 3)	±50	mA
Output current	I <sub>OUT</sub>		±50	mA
Power dissipation	P <sub>D</sub>	(Note 4)	180	mW
V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>		±100	mA
Storage temperature	T <sub>stg</sub>		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High (H) or Low (L) state. I<sub>OUT</sub> absolute maximum rating must be observed.

Note 3:  $V_{OUT} < GND$ ,  $V_{OUT} > V_{CC}$ 

Note 4: 180 mW in the range of  $T_a$  = -40 to 85 °C. From  $T_a$  = 85 to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.

### 11. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		1.2 to 3.6	V
Input voltage	$V_{IN}$		-0.3 to 3.6	V
Output voltage	V <sub>OUT</sub>	(Note 1)	0 to 3.6	V
		(Note 2)	0 to V <sub>CC</sub>	
Output current	$I_{OH},I_{OL}$	(Note 3)	±24	mA
		(Note 4)	±18	
		(Note 5)	±6	
		(Note 6)	±2	
Operating temperature	$T_{opr}$	(Note 7)	-40 to 125	°C
Input rise and fall times	dt/dv	(Note 8)	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

Note 1: Output in OFF state.

Note 2: High (H) or Low (L) state.

Note 3:  $V_{CC}$  = 3.0 to 3.6 V

Note 4:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ 

Note 5:  $V_{CC}$  = 1.65 to 1.95 V

Note 6:  $V_{CC}$  = 1.4 to 1.6 V

Note 7: Operating Range spec of  $T_{opr}$  = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

Note 8:  $V_{IN}$  = 0.8 to 2.0 V ,  $V_{CC}$  = 3.0 V



### 12. Electrical Characteristics

# 12.1. DC Characteristics (Unless otherwise specified, $T_a$ = -40 to 85 °C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	V <sub>IH</sub>	_		1.2 to 1.4	$V_{CC} \times 0.8$	_	V
				1.4 to 1.65	$V_{CC} \times 0.65$	_	
				1.65 to 2.3	$V_{CC} \times 0.65$	_	
				2.3 to 2.7	1.6	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	V <sub>IL</sub>	_		1.2 to 1.4	_	$V_{CC} \times 0.05$	V
				1.4 to 1.65	_	$V_{CC} \times 0.05$	
				1.65 to 2.3	_	$V_{CC} \times 0.2$	
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -100 μA	1.2	V <sub>CC</sub> - 0.1	_	V
				1.4 to 1.65	V <sub>CC</sub> - 0.2	_	
				1.65 to 3.6	V <sub>CC</sub> - 0.2	_	
			I <sub>OH</sub> = -2 mA	1.4	1.05	_	
			I <sub>OH</sub> = -6 mA	1.65	1.25	_	
				2.3	2.0	_	
			I <sub>OH</sub> = -12 mA	2.3	1.8	_	
				2.7	2.2	_	
			I <sub>OH</sub> = -18 mA	2.3	1.7	_	
				3.0	2.4	_	
			I <sub>OH</sub> = -24 mA	3.0	2.2	_	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 100 μA	1.2	_	0.05	V
				1.4 to 1.65	_	0.05	
				1.65 to 3.6	_	0.2	
			I <sub>OL</sub> = 2 mA	1.4	_	0.35	
			I <sub>OL</sub> = 6 mA	1.65	_	0.3	
			I <sub>OL</sub> = 12 mA	2.3	_	0.4	
				2.7	_	0.4	
			I <sub>OL</sub> = 18 mA	2.3	_	0.6	
				3.0	_	0.4	
			I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V	•	1.2 to 3.6	_	±5.0	μΑ
Power-OFF leakage current	I <sub>OFF</sub>	$V_{IN}/V_{OUT} = 0$ to 3.6 V		0	_	10.0	μΑ
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.2 to 3.6	_	20.0	μΑ
		$V_{CC} \le V_{IN} \le 3.6 \text{ V}$		1.2 to 3.6	_	±20.0	
	Δl <sub>CC</sub>	V <sub>IH</sub> = V <sub>CC</sub> - 0.6 V (per 1 input)		2.7 to 3.6	_	750	μА



# 12.2. DC Characteristics (Note) (Unless otherwise specified, $T_a$ = -40 to 125 °C)

Characteristics	Symbol	Test Condition	on	V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	V <sub>IH</sub>	_		1.2 to 1.4	$V_{CC} \times 0.8$	_	V
				1.4 to 1.65	$V_{CC} \times 0.65$	_	
				1.65 to 2.3	$V_{CC} \times 0.65$	_	
				2.3 to 2.7	1.6	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	V <sub>IL</sub>	_		1.2 to 1.4	_	V <sub>CC</sub> × 0.05	V
				1.4 to 1.65	_	$V_{CC} \times 0.05$	
				1.65 to 2.3	_	V <sub>CC</sub> × 0.2	
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -100 μA	1.2	V <sub>CC</sub> - 0.1	_	V
				1.4 to 1.65	V <sub>CC</sub> - 0.2	_	
				1.65 to 3.6	V <sub>CC</sub> - 0.2	_	
			I <sub>OH</sub> = -2 mA	1.4	1.05	_	
			I <sub>OH</sub> = -6 mA	1.65	1.25	_	
				2.3	2.0	_	
			I <sub>OH</sub> = -12 mA	2.3	1.8	_	
				2.7	2.2	_	
			I <sub>OH</sub> = -18 mA	2.3	1.6	_	
				3.0	2.4	_	
			I <sub>OH</sub> = -24 mA	3.0	2.2	_	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 100 μA	1.2	_	0.05	V
				1.4 to 1.65	_	0.05	
				1.65 to 3.6	_	0.2	
			I <sub>OL</sub> = 2 mA	1.4	_	0.35	
			I <sub>OL</sub> = 6 mA	1.65	_	0.3	
			I <sub>OL</sub> = 12 mA	2.3	_	0.4	
				2.7	_	0.4	
			I <sub>OL</sub> = 18 mA	2.3	_	0.8	
				3.0	_	0.4	
			I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		1.2 to 3.6		±20.0	μΑ
Power-OFF leakage current	I <sub>OFF</sub>	$V_{IN}/V_{OUT} = 0$ to 3.6 V		0	_	40.0	μΑ
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.2 to 3.6	_	80.0	μΑ
		$V_{CC} \le V_{IN} \le 3.6 \text{ V}$		1.2 to 3.6	_	±80.0	
	Δl <sub>CC</sub>	V <sub>IH</sub> = V <sub>CC</sub> - 0.6 V (per 1 input)		2.7 to 3.6	_	1.5	mA

Note: Operating Range spec of  $T_{opr}$  = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.



# 12.3. AC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 85 °C)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		See 12.7 AC Test Circuit,	1.2	3.0	35.0	ns
(A,B - Y)			Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	14.0	
				$1.8 \pm 0.15$	1.5	7.0	
				$2.5\pm0.2$	0.8	3.5	
				$3.3 \pm 0.3$	0.6	3.0	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		See 12.7 AC Test Circuit,	1.2	3.0	45.0	ns
(SELECT - Y)			Fig. 12.8.1, Table 12.8.1	$1.5\pm0.1$	2.0	18.0	
				$1.8\pm0.15$	1.5	9.0	
				$2.5 \pm 0.2$	0.8	4.5	
				$3.3 \pm 0.3$	0.6	3.5	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		See 12.7 AC Test Circuit,	1.2	3.0	45.0	ns
(ST - Y)			Fig. 12.8.1, Table 12.8.1	$1.5\pm0.1$	2.0	18.0	
				$1.8\pm0.15$	1.5	9.0	
				$2.5 \pm 0.2$	0.8	4.5	
				$3.3 \pm 0.3$	0.6	3.5	
Output skew	t <sub>osLH</sub> ,t <sub>osHL</sub>	(Note 1)	_	1.2	_	1.5	ns
				1.5 ± 0.1	-	1.5	
				1.8 ± 0.15	1	0.5	
				2.5 ± 0.2	1	0.5	
				$3.3 \pm 0.3$	-	0.5	

Note 1: Parameter guaranteed by design.  $(t_{osLH} = |t_{PLH}m-t_{PLH}n|, t_{osHL} = |t_{PHL}m-t_{PHL}n|)$ 

# 12.4. AC Characteristics (Note) (Unless otherwise specified, T<sub>a</sub> = -40 to 125 °C)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		See 12.7 AC Test Circuit,	1.2	3.0	46.0	ns
(A,B - Y)			Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	17.8	
				$1.8\pm0.15$	1.5	8.3	
				$2.5 \pm 0.2$	0.8	4.2	
				$3.3 \pm 0.3$	0.6	3.6	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		See 12.7 AC Test Circuit,	1.2	3.0	59.0	ns
(SELECT - Y)			Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	22.9	
				$1.8\pm0.15$	1.5	10.7	
			$2.5\pm0.2$	0.8	5.4		
				$3.3 \pm 0.3$	0.6	4.2	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		See 12.7 AC Test Circuit,	1.2	3.0	59.0	ns
(ST - Y)			Fig. 12.8.1, Table 12.8.1	1.5 ± 0.1	2.0	22.9	
				$1.8\pm0.15$	1.5	10.7	
				$2.5 \pm 0.2$	0.8	5.4	
				$3.3 \pm 0.3$	0.6	4.2	
Output skew	t <sub>osLH</sub> ,t <sub>osHL</sub>	(Note 1)	_	1.2	_	2.0	ns
				1.5 ± 0.1	_	2.0	
				1.8 ± 0.15	-	1.0	
				2.5 ± 0.2		1.0	
				$3.3\pm0.3$		1.0	

Note: Operating Range spec of  $T_{opr}$  = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

Note 1: Parameter guaranteed by design.  $(t_{osLH} = |t_{PLH}m-t_{PLH}n|, t_{osHL} = |t_{PHL}m-t_{PHL}n|)$ 



# 12.5. Dynamic Switching Characteristics (Note) (Unless otherwise specified, $T_a = 25$ °C, Input: $t_f = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Quiet output maximum dynamic V <sub>OL</sub>	V <sub>OLP</sub>	V <sub>IH</sub> = 1.8 V, V <sub>IL</sub> = 0 V	1.8	0.25	V
		V <sub>IH</sub> = 2.5 V, V <sub>IL</sub> = 0 V	2.5	0.6	
		V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V	3.3	0.8	
Quiet output minimum dynamic V <sub>OL</sub>	V <sub>OLV</sub>	V <sub>IH</sub> = 1.8 V, V <sub>IL</sub> = 0 V	1.8	-0.25	V
		V <sub>IH</sub> = 2.5 V, V <sub>IL</sub> = 0 V	2.5	-0.6	
		V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V	3.3	-0.8	
Quiet output minimum dynamic V <sub>OH</sub>	V <sub>OHV</sub>	V <sub>IH</sub> = 1.8 V, V <sub>IL</sub> = 0 V	1.8	1.5	V
		V <sub>IH</sub> = 2.5 V, V <sub>IL</sub> = 0 V	2.5	1.9	
		V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V	3.3	2.2	

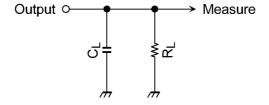
Note: Parameter guaranteed by design.

### 12.6. Capacitive Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>		_	1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 1)	f <sub>IN</sub> = 10 MHz	1.8, 2.5, 3.3	20	pF

Note 1:  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.  $I_{CC}(_{opr}) = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}$ 

### 12.7. AC Test Circuit





### 12.8. AC Waveform

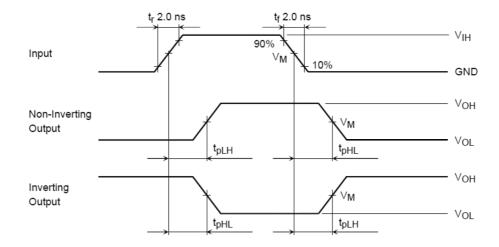


Fig. 12.8.1 t<sub>PLH</sub>, t<sub>PHL</sub>

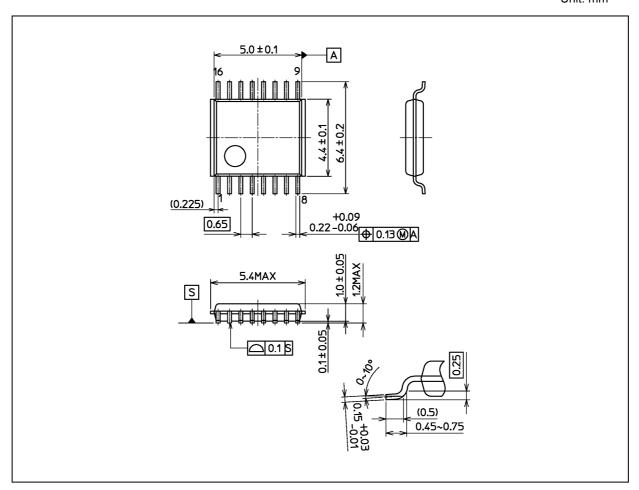
Table 12.8.1 AC Waveform Symbols

	Symbol	$V_{CC}$ = 3.3 $\pm$ 0.3 $V$	$V_{CC}$ = 2.5 ± 0.2 V $V_{CC}$ = 1.8 ± 0.15 V	$V_{CC} = 1.5 \pm 0.1 \text{ V}$ $V_{CC} = 1.2 \text{ V}$
Input	V <sub>IH</sub>	2.7 V	V <sub>CC</sub>	V <sub>CC</sub>
	V <sub>M</sub>	1.5 V	V <sub>CC</sub> /2	V <sub>CC</sub> /2
	t <sub>r</sub> , t <sub>f</sub>	2.0 ns	2.0 ns	2.0 ns
Output	V <sub>M</sub>	1.5 V	V <sub>CC</sub> /2	V <sub>CC</sub> /2
Load	C <sub>L</sub>	30 pF	30 pF	15 pF
	R <sub>L</sub>	500 Ω	500 Ω	2 kΩ



# **Package Dimensions**

Unit: mm



Weight: 0.06 g (typ.)

	Package Name(s)
Nickname: TSSOP16	



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