

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHCT139AF, TC74VHCT139AFN, TC74VHCT139AFT

Dual 2-to-4 Line Decoder

The TC74VHCT139A is an advanced high speed CMOS 2 to 4 LINE DECODER/DEMULTIPLEXER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The active low enable input can be used for gating or it can be used as a data input for demultiplexing applications.

When the enable input is held High, all four outputs are fixed at a high logic level independent of the other inputs.

The input voltage are compatible with TTL output voltage.

This device may be used as a level converter for interfacing 3.3 V to 5 V system.

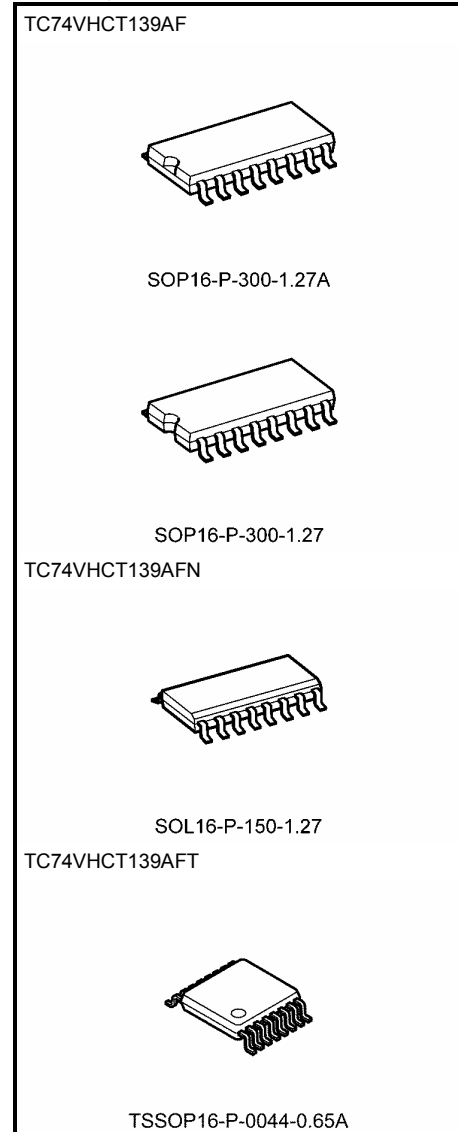
Input protection and output circuit ensure that 0 to 5.5 V can be applied to the input and output (Note) pins without regard to the supply voltage. These structure prevents device destruction due to mismatched supply and input/output voltages such as battery back up, hot board insertion, etc.

Note: $V_{CC} = 0 V$

Features

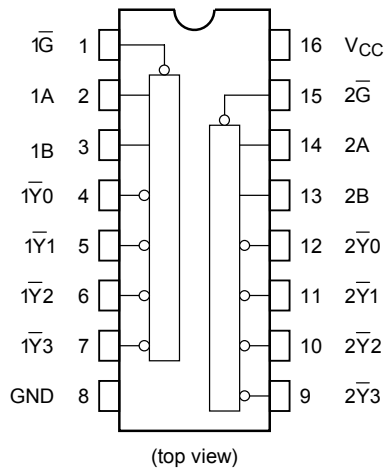
- High speed: $t_{pd} = 5.0 \text{ ns}$ (typ.) at $V_{CC} = 5 V$
- Low power dissipation: $I_{CC} = 4 \mu A$ (max) at $T_a = 25^\circ C$
- Compatible with TTL outputs: $V_{IL} = 0.8 V$ (max)
 $V_{IH} = 2.0 V$ (min)
- Power down protection is provided on all inputs and outputs.
- Balanced propagation delays: $t_{pLH} \approx t_{pHL}$
- Low noise: $V_{OLP} = 0.8 V$ (max)
- Pin and function compatible with the 74 series (74AC/HC/F/ALS/LS etc.) 139 type.

Note: xxxFN (JEDEC SOP) is not available in Japan.

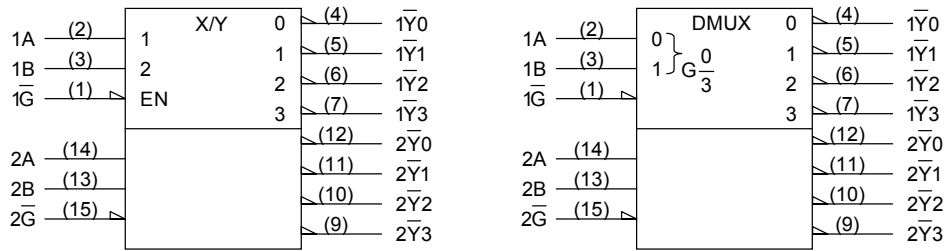


| | |
|----------------------|-----------------|
| Weight | |
| SOP16-P-300-1.27A | : 0.18 g (typ.) |
| SOP16-P-300-1.27 | : 0.18 g (typ.) |
| SOL16-P-150-1.27 | : 0.13 g (typ.) |
| TSSOP16-P-0044-0.65A | : 0.06 g (typ.) |

Pin Assignment



IEC Logic Symbol

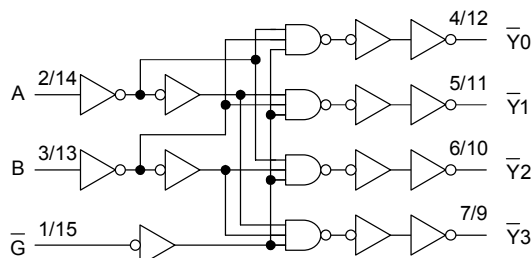


Truth Table

| Inputs | | | Outputs | | | | Selected Output |
|-----------|--------|---|-------------|-------------|-------------|-------------|-----------------|
| Enable | Select | | \bar{Y}_0 | \bar{Y}_1 | \bar{Y}_2 | \bar{Y}_3 | |
| \bar{G} | B | A | | | | | |
| H | X | X | H | H | H | H | None |
| L | L | L | L | H | H | H | \bar{Y}_0 |
| L | L | H | H | L | H | H | \bar{Y}_1 |
| L | H | L | H | H | L | H | \bar{Y}_2 |
| L | H | H | H | H | H | L | \bar{Y}_3 |

X: Don't care

System Diagram



Absolute Maximum Ratings (Note 1)

| Characteristics | Symbol | Rating | Unit |
|-----------------------------|-----------|---------------------------------|-------------|
| Supply voltage range | V_{CC} | -0.5 to 7.0 | V |
| DC input voltage | V_{IN} | -0.5 to 7.0 | V |
| DC output voltage | V_{OUT} | -0.5 to 7.0 (Note 2) | V |
| | | -0.5 to $V_{CC} + 0.5$ (Note 3) | |
| Input diode current | I_{IK} | -20 | mA |
| Output diode current | I_{OK} | ± 20 (Note 4) | mA |
| DC output current | I_{OUT} | ± 25 | mA |
| DC V_{CC} /ground current | I_{CC} | ± 50 | mA |
| Power dissipation | P_D | 180 | mW |
| Storage temperature | T_{stg} | -65 to 150 | $^{\circ}C$ |

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: $V_{CC} = 0$ V

Note 3: High or low state. I_{OUT} absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Recommended Operating Conditions (Note 1)

| Characteristics | Symbol | Rating | Unit |
|--------------------------|-----------|------------------------|-------------|
| Supply voltage | V_{CC} | 4.5 to 5.5 | V |
| Input voltage | V_{IN} | 0 to 5.5 | V |
| Output voltage | V_{OUT} | 0 to 5.5 (Note 2) | V |
| | | 0 to V_{CC} (Note 3) | |
| Operating temperature | T_{opr} | -40 to 85 | $^{\circ}C$ |
| Input rise and fall time | dt/dV | 0 to 20 | ns/V |

Note 1: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND .

Note 2: $V_{CC} = 0$ V

Note 3: High or low state

Electrical Characteristics

DC Characteristics

| Characteristics | Symbol | Test Condition | | Ta = 25°C | | | Ta = -40 to 85°C | | Unit | |
|---------------------------|------------------|---|--------------------------|---------------------|------|------|------------------|------|------|-----|
| | | | | V _{CC} (V) | Min | Typ. | Max | Min | | Max |
| High-level input voltage | V _{IH} | — | | 4.5 to 5.5 | 2.0 | — | — | 2.0 | — | V |
| Low-level input voltage | V _{IL} | — | | 4.5 to 5.5 | — | — | 0.8 | — | 0.8 | V |
| High-level output voltage | V _{OH} | V _{IN} = V _{IH} or V _{IL} | I _{OH} = -50 μA | 4.5 | 4.40 | 4.50 | — | 4.40 | — | V |
| | | | I _{OH} = -8 mA | 4.5 | 3.94 | — | — | 3.80 | — | |
| Low-level output voltage | V _{OL} | V _{IN} = V _{IH} or V _{IL} | I _{OL} = 50 μA | 4.5 | — | 0.0 | 0.1 | — | 0.1 | V |
| | | | I _{OL} = 8 mA | 4.5 | — | — | 0.36 | — | 0.44 | |
| Input leakage current | I _{IN} | V _{IN} = 5.5 V or GND | | 0 to 5.5 | — | — | ±1.0 | — | ±1.0 | μA |
| Quiescent supply current | I _{CC} | V _{IN} = V _{CC} or GND | | 5.5 | — | — | 4.0 | — | 40.0 | μA |
| | I _{CCT} | Per input: V _{IN} = 3.4 V Other input: V _{CC} or GND | | 5.5 | — | — | 1.35 | — | 1.50 | mA |
| Output leakage current | I _{OPD} | V _{OUT} = 5.5 V | | 0 | — | — | 0.5 | — | 5.0 | μA |

AC Characteristics (input: t_r = t_f = 3 ns)

| Characteristics | Symbol | Test Condition | | Ta = 25°C | | | Ta = -40 to 85°C | | Unit | |
|---|------------------|----------------|-----------|---------------------|---------------------|-----|------------------|-----|------|-----|
| | | | | V _{CC} (V) | C _L (pF) | Min | Typ. | Max | | Min |
| Propagation delay time (A, B- \bar{Y}) | t _{pLH} | — | 5.0 ± 0.5 | 15 | — | 5.0 | 7.2 | 1.0 | 8.5 | ns |
| | t _{pHL} | | | 50 | — | 6.5 | 9.2 | 1.0 | 10.5 | |
| Propagation delay time (\bar{G} - \bar{Y}) | t _{pLH} | — | 5.0 ± 0.5 | 15 | — | 5.0 | 7.2 | 1.0 | 8.5 | ns |
| | t _{pHL} | | | 50 | — | 6.5 | 9.2 | 1.0 | 10.5 | |
| Input capacitance | C _{IN} | — | | — | 4 | 10 | — | 10 | pF | |
| Power dissipation capacitance | C _{PD} | (Note) | | — | 32 | — | — | — | pF | |

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

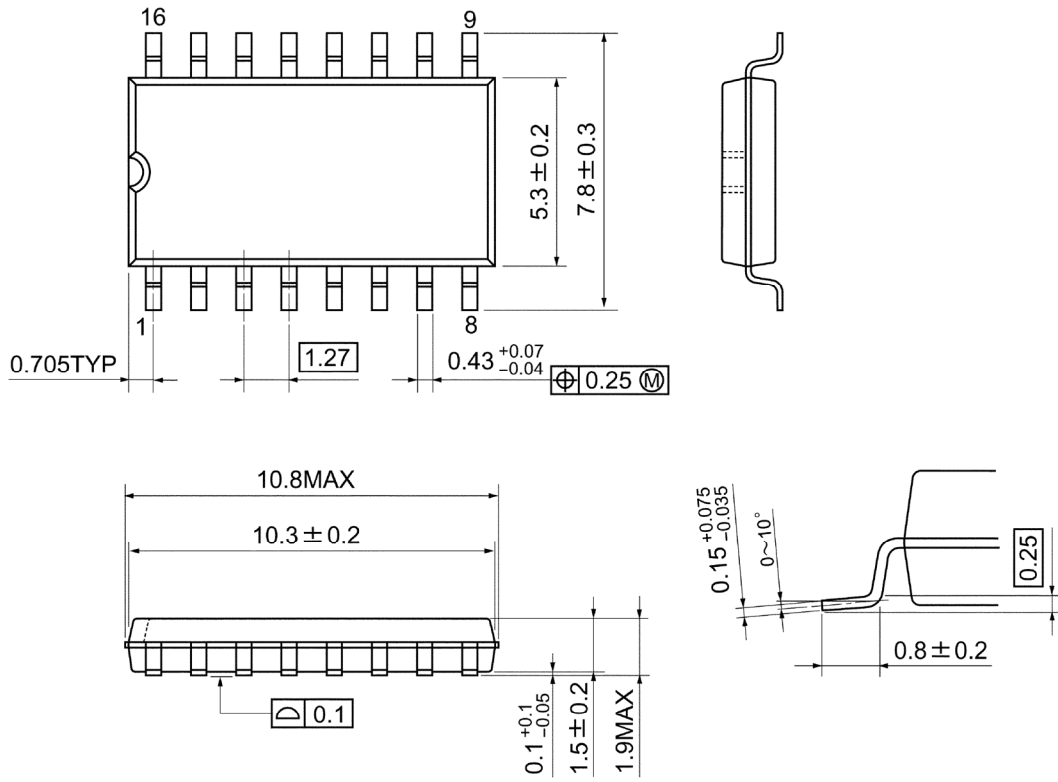
Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2 \text{ (per decoder)}$$

Package Dimensions

SOP16-P-300-1.27A

Unit: mm

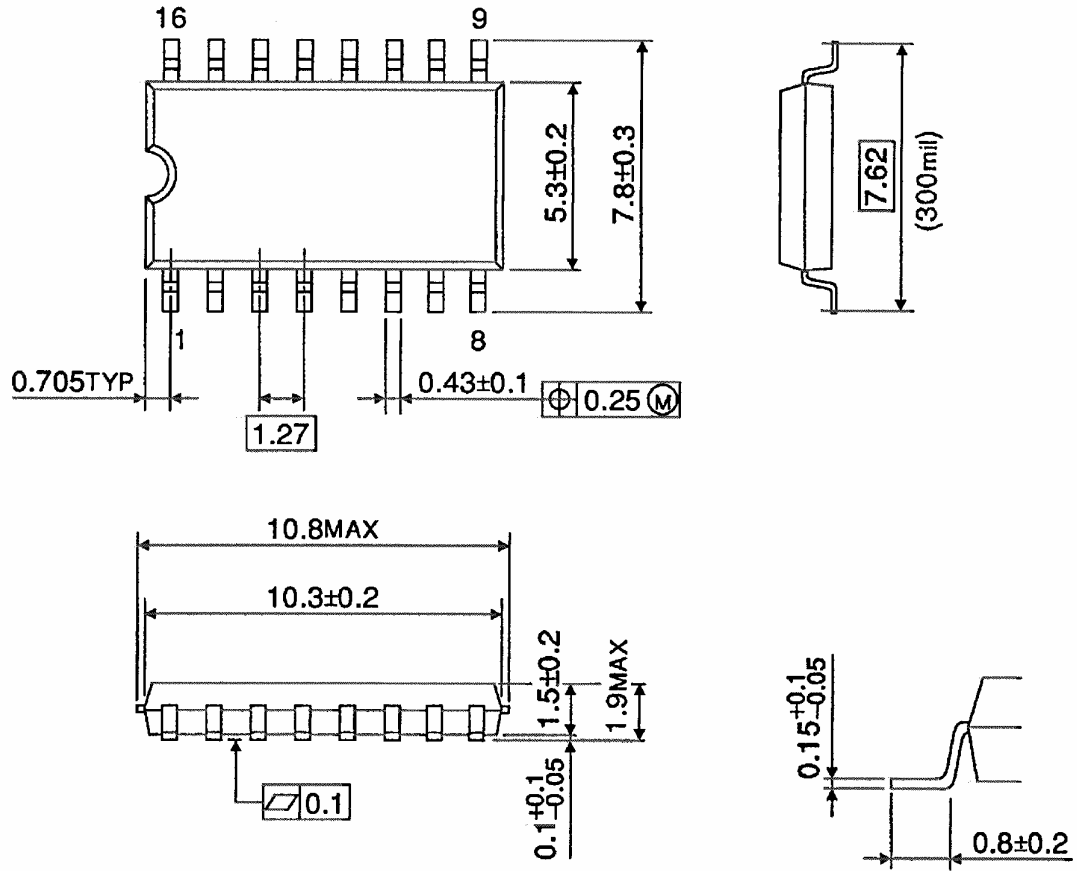


Weight: 0.18 g (typ.)

Package Dimensions

SOP16-P-300-1.27

Unit : mm

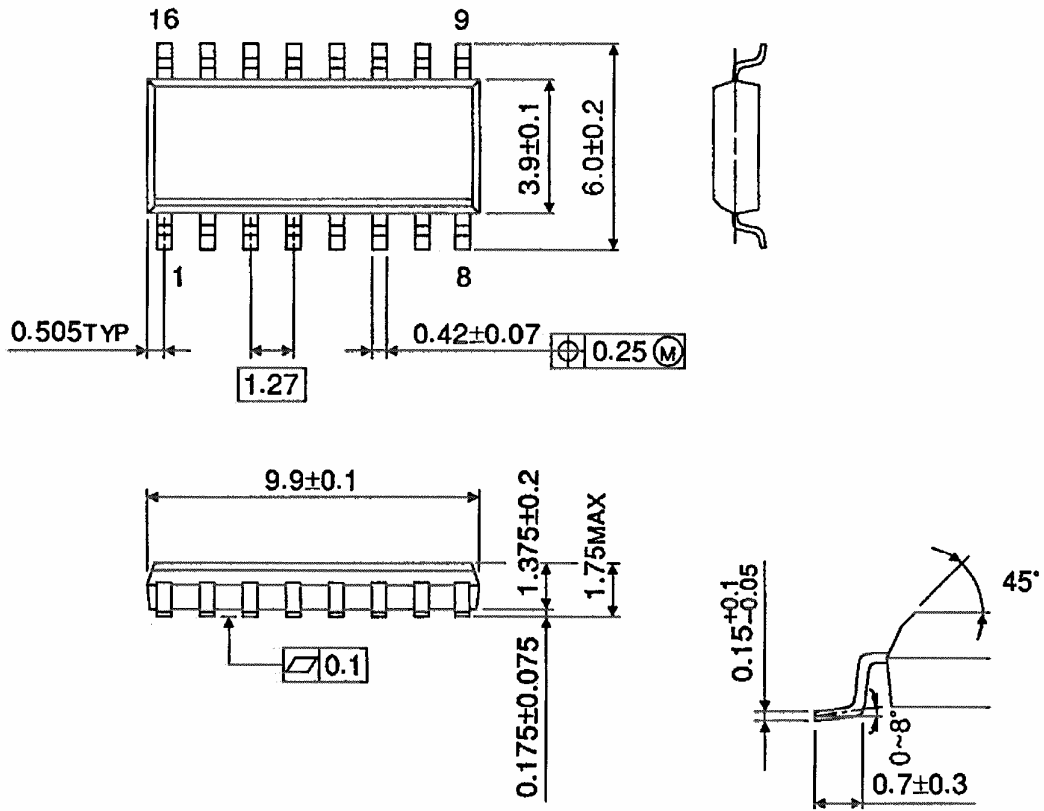


Weight: 0.18 g (typ.)

Package Dimensions (Note)

SOL16-P-150-1.27

Unit : mm



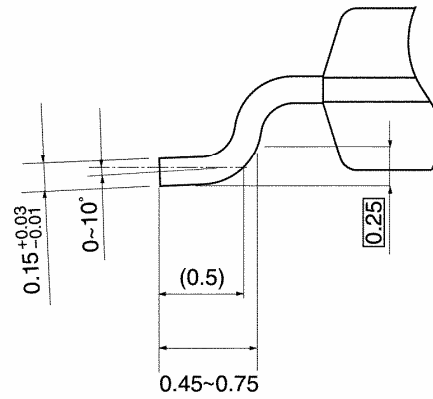
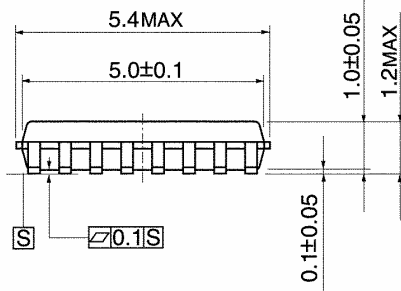
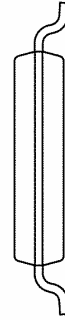
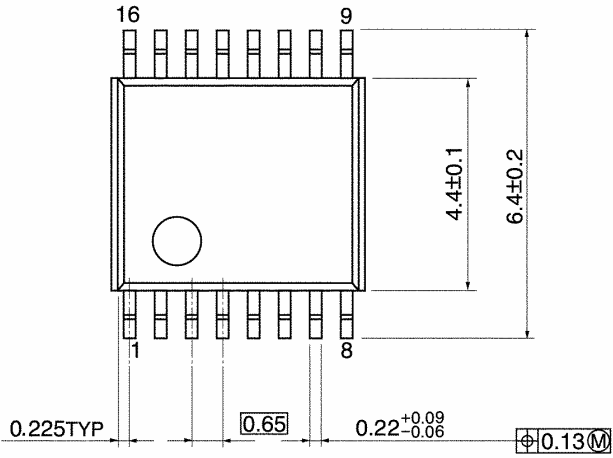
Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

Package Dimensions

TSSOP16-P-0044-0.65A

Unit: mm



Weight: 0.06 g (typ.)

Note: Lead (Pb)-Free Packages**SOP16-P-300-1.27A SOL16-P-150-1.27 TSSOP16-P-0044-0.65A****RESTRICTIONS ON PRODUCT USE**

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