TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7PG17FU

#### **Dual Schmitt Buffer**

#### **Features**

• High output current : ±8 mA (min) at V<sub>CC</sub> = 3 V

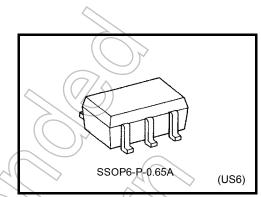
• Super high speed operation : t<sub>pd</sub> = 3.7 ns (typ.)

at  $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$ 

• Operating voltage range :V<sub>CC</sub> = 0.9 to 3.6 V

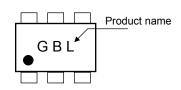
5.5-V tolerant inputs

3.6-V power down protection outputs

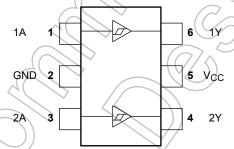


Weight: 0.0068 g (typ.)

#### Marking



# Pin Assignment (top view)



# Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	-0.5 to 4.6	V
DC input voltage	ΑΐΝ	-0.5 to 7.0	V
DC output voltage	Vout	-0.5 to 4.6 (Note 1) -0.5 to V <sub>CC</sub> + 0.5 (Note 2)	٧
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /GND current	lçc	±100	mA
Power dissipation	PD	200	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or Low State. Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

Note 3: V<sub>OUT</sub> < GND

Start of commercial production 2007-03

# **IEC Logic Symbol**

# IN A OUT Y

#### **Truth Table**

А	Y
L	L
Н	Н

# **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	0.9 to 3.6	V
Input voltage	V <sub>IN</sub>	0 to 5.5	٧
Output voltage	Vout	0 to 3.6 (Note 4)	ν .΄
	V <sub>OUT</sub>	0 to V <sub>CC</sub> (Note 5)	1
	I <sub>OH</sub> / I <sub>OL</sub>	±8.0 (Note 6)	$\bigcirc$
		±4.0 (Note 7)	
Output Current		±3:0 (Note 8)	_mA
Output Current		±1.7 (Note 9)	
		±0.3 (Note 10)	
		±0.02 (Note 11)	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C

Note 4:  $V_{CC} = 0V$ 

Note 5: High or Low State. IOUT absolute maximum rating must be observed.

Note 6:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 7:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ 

Note 8:  $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$ 

Note 9:  $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$ 

Note 10:  $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$ 

Note 11:  $V_{CC} = 0.9 \text{ V}$ 



2

## **Electrical Characteristics**

#### **DC Characteristics**

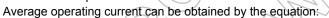
Characteristics		Symbol Test Condition				Ta = 25°C			Ta = -40 to 85°C		Linit
		Syllibol	Test Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
					0.9	_	_ <	0.73	_	0.80	
					1.1	_	_	0.86	_	0.93	1
	Positive	.,,			1.4	_	_	1.07	) /_	1.12	
	threshold voltage	V <sub>P</sub>		_	1.65	_	+0	1.23	_	1.25	
					2.3	_	///	1.66	_	1.68	
Threshold					3.0	-(	7	2.14	_	2.15	V
Voltage					0.9	0.18		_	0.07	_	V
					1.1	0.26		_	0,18	_	
	Negative	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \			1.4	0.36		_ /	0.31		
	threshold voltage	V <sub>N</sub>		_	1.65	0.45		-6	0.41	· _	
					2.3	0.69	_		0.64	_	
					3.0	0.96	_	-	0.91	_	<b>1</b>
					0.9	0.20	-((	0.38	0.15	0.53	
					1.1	0.25		0.41	0.21	0.53	
l livetenesis Mel	14	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \			1.4	0.35	(7/3	0.48	0.34	0.57	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Hysteresis Vol	tage	V <sub>H</sub>	_ </td <td>1.65</td> <td>0.42</td> <td></td> <td>0.56</td> <td>0.40</td> <td>0.60</td> <td>V</td>	1.65	0.42		0.56	0.40	0.60	V	
					2.3	0.60	//-	0.74	0.60	0.76	
			(		3.0	0.79	//—	0.93	0.79	0.94	
				I <sub>OH</sub> =-0.02 mA	0.9	0.75	_		0.75		
				$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V <sub>CC</sub> × 0.75			V <sub>CC</sub> × 0.75		
	High level	V <sub>OH</sub> (	V <sub>IN</sub> = V <sub>JH</sub>	I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	V <sub>CC</sub> × 0.75			V <sub>CC</sub> × 0.75		
				I <sub>OH</sub> = -3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.45	_	_	V <sub>CC</sub> -0.45		
		~<	/	$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0	_	_	2.0	_	
Output				$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48	_	_	2.48	_	V
voltage	\$1			$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	•
		9	VOL VIN = VIL	I <sub>OL</sub> = 0.3 mA	1.1 to 1.3	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
Low	Low level	Low level Vol		1 <sub>OL</sub> = 1.7 mA	1.4 to 1.6	_	_	V <sub>CC</sub> × 0.25		V <sub>CC</sub> × 0.25	
				I <sub>OL</sub> = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	
	,	<b>\\</b>		I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	
	1		~	I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage	current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5V		0 to 3.6	_	_	±0.1	_	±1.0	μА
Power off leakage current IOF		I <sub>OFF</sub>	V <sub>IN</sub> = 5.5V or V <sub>OUT</sub> = 3.6V		0	_	_	1.0	_	10.0	μА
Quiescent supply current		Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		3.6	_		1.0	_	10.0	μА

3 2014-03-01

## AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics	Syllibol		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
		C <sub>L</sub> = 10 pF,	0.9	_	27.3	_	_	_	
			1.1 to 1.3	_	13.0	24.5	1.0	39.2	
			1.4 to 1.6		7.5	11.8	1.0	12.7	
		$R_L = 1 M\Omega$	1.65 to 1.95		6.0	8.5	1.0	9.0	ns
			2.3 to 2.7		4.3	5.4	1.0	5.8	
			3.0 to 3.6	1	3.5	4.4	1.0	4.6	
Propagation delay time	†р∟н †рн∟	$C_L$ = 15 pF, $R_L$ = 1 M $\Omega$	0.9	_	29.5		_	_	
			1.1 to 1.3	_((	14.3	> 26.7	1.0	44.7	
			1.4 to 1.6		)8.	12.7	1.0	14.0	
			1.65 to 1.95	1/	6.3	9.1	1.0	9.5	
			2.3 to 2.7		4.6	5.7	1.0	6.1	
			3.0 to 3.6		3.7	4.6	1.0	5.0	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		40.5	1	(H)	_	
			1.1 to 1.3	_	19.6	35.7	1.0	58.1	
			1.4 to 1.6		10.7	15.8	1.0	17.6	
			1.65 to 1.95	_	7.8	10.7	1.0	11.7	
			2.3 to 2.7		5.4	6.9	1.0	8.1	
			3.0 to 3.6		4.3	5.2	1.0	6.1	
Input capacitance	C <sub>IN</sub>		3.6		) 3		_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 12)	0.9 to 3.6		9		_	_	pF

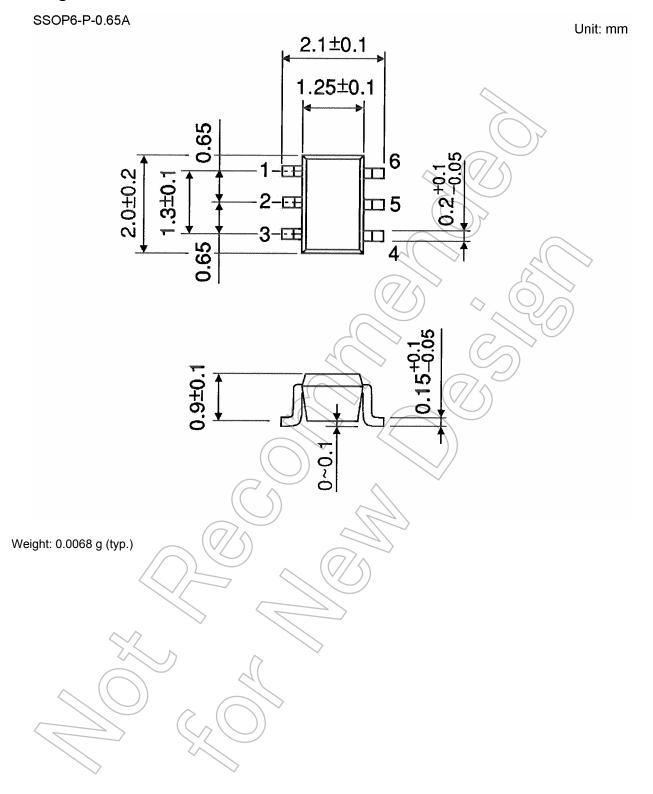
Note 12: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.



 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 



# **Package Dimensions**



5 2014-03-01

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