TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SA00F,TC7SA00FU

2-Input NAND Gate

Features

Low voltage operation: V_{CC} = 1.8 to 3.6 V

• High speed operation : t_{pd} = 2.8 ns (max) (V_{CC} = 3.0 to 3.6 V)

: t_{pd} = 3.7 ns (max) (V_{CC} = 2.3 to 2.7 V)

 $: t_{pd} = 7.4 \text{ ns (max) (V}_{CC} = 1.8 \text{ V)}$

• High output current : I_{OH}/I_{OL} = ±24 mA (min) (V_{CC} = 3.0 V)

: I_{OH}/I_{OL} = ±18 mA (min) (V_{CC} = 2.3 V)

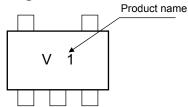
 $: I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.8 \text{ V)}$

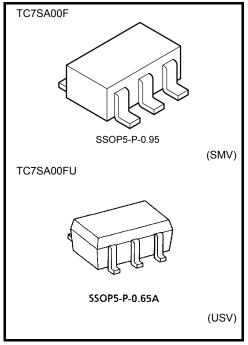
• 3.6-V tolerant inputs.

• 3.6-V power down protection output.

TC74VCX00FT equivalent.

Marking





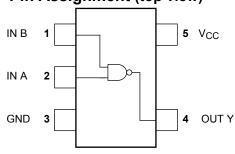
Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

	1			
Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	−0.5 to 4.6	V	
DC input voltage	V _{IN}	−0.5 to 4.6	V	
DC output voltage	\/	-0.5 to 4.6 (Note 1)	V	
DC output voltage	Vout	-0.5 to V _{CC} +0.5 (Note 2)	\ \	
Input diode current	l _{IK}	-50	mA	
Output diode current	lok	-50 (Note 3)	mA	
DC output current	lout	±50	mA	
Power dissipation	PD	200	mW	
DC V _{CC} /ground current	I _{CC}	±100	mA	
Storage temperature range	T _{stg}	-65 to 150	°C	

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High or Low State. IOUT absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

IEC Logic Symbol



Truth Table

Α	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit	
ounnly voltage	Vaa	1.8 to 3.6	V	
supply voltage	V _{CC}	1.2 to 3.6 (Note 4)	V	
Input voltage	V _{IN}	-0.3 to 3.6	V	
Output voltage	Vout	0 to 3.6 (Note 5)	V	
	VOUI	0 to V _{CC} (Note 6)	V	
		± 24 (Note 7)		
Output current	I _{OH} /I _{OL}	± 18 (Note 8)	mA	
		± 6 (Note 9)		
Operating temperature range	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 10)	ns/V	

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or low state

Note 7: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ Note 8: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 9: $V_{CC} = 1.8 V$

Note 10: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

TOSHIBA

DC Characteristics (Ta = -40 to 85°C, 2.7 V < $V_{CC} \le 3.6$ V)

Charac	cteristics	Symbol	Test Condition			Min	May	Unit
Onarac	Acristics	Cymbol	rest e	onation	V _{CC} (V)	IVIIII	Max	Onit
Input voltage	High level	V _{IH}	-	_	2.7 to 3.6	2.0		V
input voitage	Low level	V _{IL}	-	_	2.7 to 3.6		0.8	V
			$I_{OH} = -100 \mu A$	2.7 to 3.6	V _{CC} - 0.2			
Output voltage	VoH	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -12 mA	2.7	2.2	_		
			$I_{OH} = -18 \text{ mA}$	3.0	2.4		V	
			$I_{OH} = -24 \text{ mA}$	3.0	2.2			
			VIN = VIH	$I_{OL} = 100 \mu A$	2.7 to 3.6		0.2	
	Low level	Voi		I _{OL} = 12 mA	2.7		0.4	
	Low level	V _{OL}		I _{OL} = 18 mA	3.0	_	0.4	
				I _{OL} = 24 mA	3.0		0.55	
Input leakage curre	ent	I _{IN}	$V_{IN} = 0$ to 3.6 V	V _{IN} = 0 to 3.6 V		_	±5.0	μА
Power off leakage	current	l _{OFF}	V_{IN} , $V_{OUT} = 0$ to 3.6 V		0	_	10.0	μА
Ouissant supply surrent	loo	V _{IN} = V _{CC} or GNI	V _{IN} = V _{CC} or GND		_	20.0		
Quiescent supply current		Icc	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		2.7 to 3.6	_	±20.0	μΑ
Increase in I _{CC} pe	r input	Δlcc	$V_{IH} = V_{CC} - 0.6 V$	′	2.7 to 3.6	_	750	

DC Characteristics (Ta = -40 to 85°C, 2.3 V \leq V $_{CC} \leq$ 2.7 V)

Charac	eteristics	Symbol	Test Condition			Min	Max	Unit
		-			V _{CC} (V)			
Input voltage	High level	V_{IH}	-	_	2.3 to 2.7	1.6	_	V
input voltage	Low level	V _{IL}	-	_			0.7	V
High level Output voltage			I _{OH} = -100 μA	2.3 to 2.7	V _{CC} - 0.2	_		
	High level	V _{ОН}	$V_{IN} = V_{IH}$ or V_{IL}	$I_{OH} = -6 \text{ mA}$	2.3	2.0	_	
				I _{OH} = -12 mA	2.3	1.8	_	
				I _{OH} = -18 mA	2.3	1.7	_	V
		vel V _{OL}	V _{IN} = V _{IH}	I _{OL} = 100 μA	2.3 to 2.7	_	0.2	-
	Low level			I _{OL} = 12 mA	2.3	_	0.4	
				I _{OL} = 18 mA	2.3	_	0.6	
Input leakage curre	ent	I _{IN}	V _{IN} = 0 to 3.6 V		2.3 to 2.7	_	±5.0	μΑ
Power off leakage	current	l _{OFF}	V_{IN} , $V_{OUT} = 0$ to 3.6 V		0	_	10.0	μΑ
		V _{IN} = V _{CC} or GND		2.3 to 2.7	_	20.0	^	
Quiescent supply of	штеп	Icc	V _{CC} ≤ (V _{IN} , V _{OUT}	-) ≦ 3.6 V	2.3 to 2.7	_	±20.0	μА

DC Characteristics (Ta = -40 to 85°C, 1.8 V \leq V_{CC} < 2.3 V)

Charac	cteristics	Symbol	Symbol Test Condition		-	Min	Max	Unit
Onarac	onone de la constante de la co	Cymbol	1 001 0	on and on	V _{CC} (V)	14	Max	Orme
Input voltage	High level	V _{IH}	/ін —		1.8 to 2.3	V _{CC} × 0.7	ı	V
input voltage	Low level	V _{IL}	V_{OH} $V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OH} = -100 \mu\text{A}$ $I_{OH} = -6 \text{ mA}$	1.8 to 2.3		V _{CC} × 0.2	V	
	High level	V _{OH}	V _{OH} V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.8	V _{CC} - 0.2	_	
Output voltage				$I_{OH} = -6 \text{ mA}$	1.8	1.4	_	V
	Low level	V _{OL}		$I_{OL} = 100 \mu A$	1.8		0.2).2
	Low level	VOL	$V_{IN} = V_{IH}$	I _{OL} = 6 mA	1.8		0.3	
Input leakage curre	ent	I _{IN}	V _{IN} = 0 to 3.6 V		1.8		±5.0	μА
Power off leakage	current	loff	V _{IN} , V _{OUT} = 0 to 3.6 V		0		10.0	μА
Quiescent supply current		Icc	V _{IN} = V _{CC} or GNI)	1.8	_	20.0	μА
Quiescent supply o	Juitein	icc	V _{CC} ≤ (V _{IN} , V _{OU})	-) ≦ 3.6 V	1.8	_	±20.0	μΑ

AC Characteristics (Ta = -40 to 85°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500~\Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH} t _{pHL}	Figure 1, Figure 2	1.8	1.5	7.4	
			2.5 ± 0.2	1.0	3.7	ns
			3.3 ± 0.3	8.0	2.8	

For $C_L = 50\ pF$, add approximately 300 ps to the AC maximum specification.

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Cumbal	Test Condition		Turn	Unit
Characteristics	Symbol	rest Condition	V _{CC} (V	— Тур.)	Offic
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note	1.8	0.25	
Quiet output maximum dynamic V_{OL}	V_{OLP}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Note	11) 2.5	0.6	V
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note	11) 3.3	0.8	
	V _{OLV}	$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note	1.8	-0.25	
Quiet output minimum dynamic V _{OL}		$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Note	11) 2.5	-0.6	V
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note	11) 3.3	-0.8	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note	1.8	1.5	
Quiet output minimum dynamic V _{OH}	V_{OHV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Note	11) 2.5	1.9	V
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note	11) 3.3	2.2	

Note 11: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol		Test Condition		V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}		_		1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz		(Note 12)	1.8, 2.5, 3.3	20	pF

Note 12: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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Average operating current can be obtained by the equation.

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

AC Test Circuit

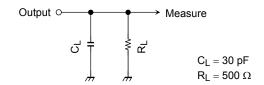


Figure 1

AC Waveforms

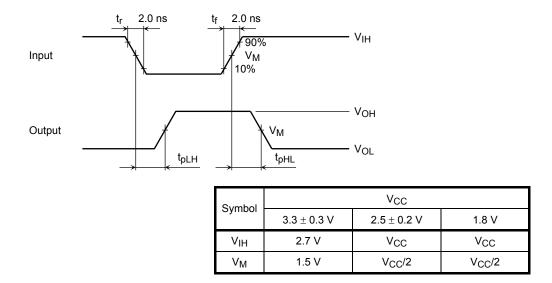


Figure 2 t_{pLH}, t_{pHL}

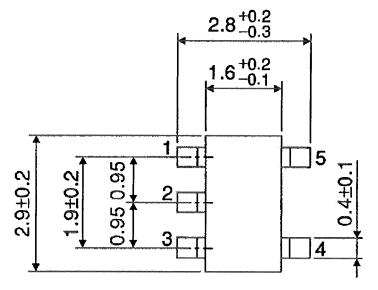
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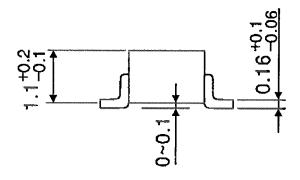


Package Dimensions

SSOP5-P-0.95 Unit: mm

TC7SA00F/FU



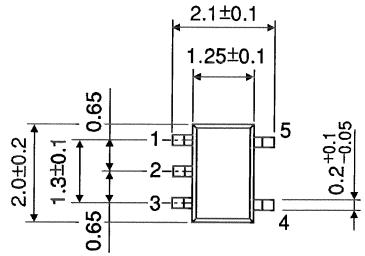


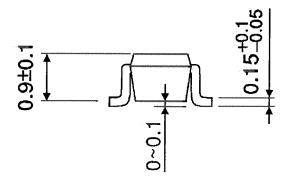
Weight: 0.016 g (typ.)

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Package Dimensions

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

8 2009-07-29

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