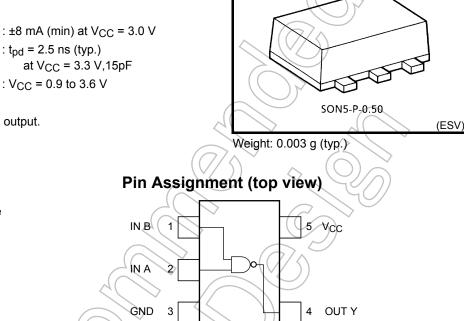
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG00FE

2-Input NAND Gate

Features

- High output current : ±8 mA (min) at V_{CC} = 3.0 V
- Super high speed operation :t_{pd} = 2.5 ns (typ.)
- Operating voltage range
- 5.5-V tolerant inputs.
- 3.6-V power down protection output.



Marking

Absolute Maximum Ratings (Ta = 25°C)

Product Name

Symbol	Rating	Unit
Vcc	-0.5 to 4.6	V
VIN	-0.5 to 7.0	V
N	-0.5 to 4.6 (Note 1)	V
V001	-0.5 to V _{CC} + 0.5 (Note 2)	v
I _{IK}	-20	mA
Іок	-20 (Note 3)	mA
IOUT	±25	mA
lec	±50	mA
PD	150	mW
T _{stg}	−65 to 150	°C
	Vcc VIN Vout IIK IoK IoUT FD	V _{CC} -0.5 to 4.6 V _{IN} -0.5 to 7.0 V _{OUT} -0.5 to 4.6 (Note 1) -0.5 to V _{CC} + 0.5 (Note 2) I _{IK} -20 I _{OK} -20 (Note 3) I _{OUT} ±25 I _{CC} ±50 P _D 150

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:
$$V_{CC} = 0V$$

Note 2: High or Low state. Do not exceed $I_{\mbox{OUT}}$ of absolute maximum ratings.

Note 3: V_{OUT} < GND

Start of commercial production 2005-02

<u>TOSHIBA</u>

IEC Logic Symbol



Truth	Table

А	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

berating Ranges		
Characteristics	Symbol	Rating
Supply voltage	V _{CC}	0.9 to 3.6 V
Input voltage	V _{IN}	0 to 5:5 V
Output voltage	Vour	0 to 3,6 (Note 4)
Output voltage	Vout	0 to V _{CC} (Note 5)
Output Current		± 8.0 (Note 6)
		±4,0 (Note 7)
	I _{OH} /I _{OL}	± 3.0 (Note 8)
	'OH/'OL	± 1.7 (Note 9)
	<	± 0.3 (Note 10)
		± 0.02 (Note 11)
Operating temperature	T _{opr}	-40 to 85 °C
Input rise and fall time	dt/dv	0 to 10 (Note 12) ns/V

Note 4: $V_{CC} = 0V$ Note 5: High or Low state. Note 6: $V_{CC} = 3.0$ to 3.6 V Note 7: $V_{CC} = 2.3$ to 2.7 V Note 8: $V_{CC} = 1.65$ to 1.95 V Note 9: $V_{CC} = 1.4$ to 1.6 V Note 10: $V_{CC} = 1.1$ to 1.3 V Note 11: $V_{CC} = 0.9$ V

Note 12: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	ol Test Condition V _{CC} (V)		Ta = 25°C			Ta = -40 to 85°C		Unit	
Characteristics	Symbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
				0.9	V _{CC}		K	V _{CC}	_	
				1.1 to 1.3	V _{CC} × 0.7	_		V _{CC} ×0.7		
High-level input voltage	VIH		_	1.4 to 1.6	V _{CC} × 0.65	-6		V _{CC} × 0.65		v
voltage				1.65 to 1.95	V _{CC} × 0.65		\bigcirc	V _{CC} × 0.65		
				2.3 to 2.7	1.7	(-)	\geq –	1.7	—	
				3.0 to 3.6	2.0	\geq	_	2.0	_	
				0.9	4	\searrow	GND	St)	GND	
				1.1 to 1.3	25	>	V _{CC} × 0.3	5-	V _{CC} × 0.3	
Low-level input voltage	V _{IL}		_	1.4 to 1.6	\mathcal{D}	_	V _{CC} × 0.35	L)	V _{CC} × 0.35	v
voltage				1.65 to 1.95	~ _	-((V _{CC} × 0.35	>_	V _{CC} × 0.35	
			G	2.3 to 2.7	_	$\overline{\Box}$	0.7	_	0.7	
			20	3.0 to 3.6	I))0.8	_	0.8	
			I _{OH} =-0.02 mA	0.9	0.75	\sum	_	0.75	_	
			I _{OH} = -0.3 mA	1.1 to 1.3	Vcc × 0.75)}	—	V _{CC} × 0.75		
High-level output	V _{ОН}	V _{IN} = V _{IH} or V _{IL}	10H = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	/_	_	V _{CC} × 0.75	_	v
voltage			I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45	_	
			I _{OH} = -4.0 mA	2.3 to 2.7	2.0			2.0		
			1 _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	_	2.48	_	
	$\langle \rangle$		I _{OL} = 0.02 mA	0.9	_	—	0.1	_	0.1	
		\triangleright	I _{OL} = 0.3 mA	1.1 to 1.3	_	_	V _{CC} × 0.25		V _{CC} × 0.25	
Low-level output	VoL	V _{IN} = V _{IH}	I _{OL} = 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	V
			I _{OL} = 3.0 mA	1.65 to 1.95			0.45		0.45	
$\sim (())$)		lo⊾ = 4.0 mA	2.3 to 2.7			0.4		0.4	
		> (C	I _{OL} = 8.0 mA	3.0 to 3.6			0.4		0.4	
Input leakage current	IIN	$V_{\rm IN} \neq 0$ to 5	.5V	0 to 3.6			±0.1		±1.0	μA
Power off leakage current	IOFF	V _{IN} = 0 to 5.5V V _{OUT} = 0 to 3.6V		0			1.0	_	10.0	μA
Quiescent supply current	Icc	$V_{IN} = V_{CC} c$	or GND	3.6			1.0	_	10.0	μΑ

AC Electrical Characteristics (unless otherwise specified, Input $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40	Unit	
Characteristics Symbol		Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
		C _L = 10 pF,	0.9	_	26.9		—	—	ns
			1.1 to 1.3	_	10.9	20.7	1.0	38.6	
			1.4 to 1.6		5.9	9.6	1.0	11.3	
		$R_L = 1 M\Omega$	1.65 to 1.95	_	4.5	7.0	1.0	7.5	
			2.3 to 2.7	_	2.9	4.4	1.0	4.9	
			3.0 to 3.6	- <	2.2	3.5	1.0	4.1	
		$C_{L} = 15 \text{ pF},$ $R_{L} = 1 \text{ M}\Omega$ $1.4 \text{ to } 1.6 - 6.5 - 10.$ $1.65 \text{ to } 1.95 - 5.0 - 7.7$ $2.3 \text{ to } 2.7 - 3.2 - 4.9$	0.9	_	30.0)	—	—	
	tplh tphl		1.1 to 1.3	_	12.0	24.2	1.0	42.0	
Propagation delay time			1.4 to 1.6	76	6.5	10.5	1.0	12.6	
Fropagation delay time			1.65 to 1.95	4	5.0	7.7	1.0	8.0	
			2.3 to 2.7		3.2	4.9	1.0	5.6	
			3.8 ((1.0	4.4				
		С _L = 30 рF,	0.9		45.0	\sim	~4/	7 –	
			1.1 to 1.3	\rightarrow	18.0	33.4	1.0	63.2	-
			1.4 to 1.6	~ _	8.9	14.8	1.0	17.9	
		$R_L = 1 M\Omega$	1.65 to 1.95		6.9	10.3	1.0	10.8	
		20	2.3 to 2.7		4.4	6.4	1.0	6.8	
		40	3.0 to 3.6		3.5	4.9	1.0	5.4	
Input capacitance	C _{IN}		3.6	X	3	_	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 to 3.6	_	6	_	—	—	pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

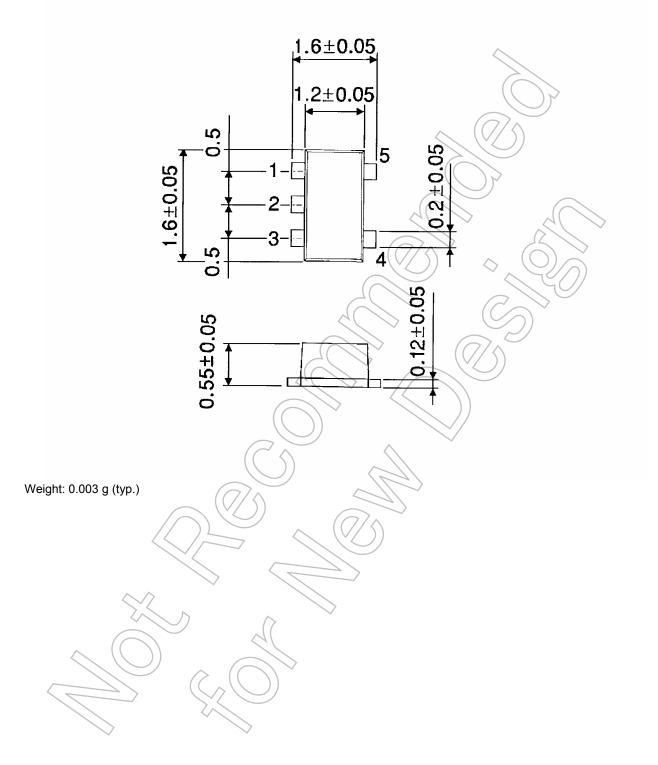
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.50

Unit : mm



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