TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG00FU

2 Input NAND Gate

Features

• High output current: ±8 mA (min) at V_{CC} = 3 V

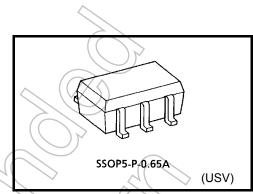
• High-speed operation: t_{pd} = 2.5 ns (typ.)

at $V_{CC} = 3.3 \text{ V},15\text{pF}$

Operating voltage range: V_{CC} = 0.9 to 3.6 V

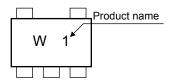
5.5-V tolerant inputs.

• 3.6-V power down protection output.

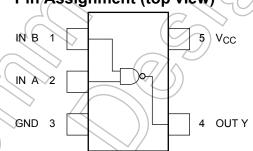


Weight: 0.006 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Voc	-0.5 to 4.6	V
DC input voltage	ViN	-0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to 4.6 (Note 1)	V
		-0.5 to V _{CC} + 0.5 (Note 2)	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

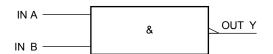
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

Note 2: High or Low State. Do not exceed I_{OUT} of absolute maximum ratings

Note 3: V_{OUT} < GND Start of commercial production 2005-02

IEC Logic Symbol



Truth Table

Α	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

Characteristics	Symbol	Rating
Supply voltage	V _{CC}	0.9 to 3.6
Input voltage	V _{IN}	0 to 5.5
Output voltage	Vout	0 to 3.6 (Note 4)
	V001	0 to VCC (Note 5)
Output Current		± 8.0 (Note 6)
		±4.0 (Note 7)
	I _{OH} /I _{OL}	±3.0 (Note 8) mA
	'OH/'OL	± 1.7 (Note 9)
		± 0.3 (Note 10)
		± 0.02 (Note 11)
Operating temperature	T _{opr}	- 40 to 85 °C
Input rise and fall time	dt/dv	0 to 10 (Note 12) ns/V

Note 4: $V_{CC} = 0V$

Note 5: High or Low state.

Note 6: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 7: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 8: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 9: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 10: V_{CC} = 1.1 to 1.3 V

Note 11: $V_{CC} = 0.9 \text{ V}$

Note 12: $V_{IN} = 0.8 \text{ to } 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

Electrical Characteristics

DC Characteristics

Characteristics	Cumbal	Test Condition			Ta = 25°C			Ta = -40 to 85°C		- Unit
Characteristics Symbol Te		rest	V _{CC} (V)		Min	Тур.	Max <	Min	Max	Unit
				0.9	V _{CC}	_	_	VCC	_	
		_		1.1 to 1.3	V _{CC} × 0.7		(V _{CC} ×0.7	<i>></i> –	_
High-level input voltage	1.4 to 1.6			V _{CC} × 0.65	\Diamond	$\sqrt{}$	V _{CC} × 0.65	_	٧	
	1.65 to 1.95			V _{CC} × 0.65	-((7	V _{CC} × 0.65	_		
				2.3 to 2.7	1.7)	1.7		
				3.0 to 3.6	2.0 <	1(-/	>-	2.0	7(-//	
				0.9			GND	-12	GND	~
))	V _C C × 0.3	7	Vec × 0.3	
Low-level input	V _{IL}		_	1.4 to 1.6		_	V _{CC} × 0.35	7	VCC × 0.35	V
voltage				1.65 to 1.95		_	V _{CC} × 0.35	9	V _{CC} × 0.35	
				2.3 to 2.7	_	- ((.0.7 \)	0.7	
		4	3.0 to 3.6	-//	1	0.8	,	0.8		
			I _{OH} =-0.02 mA	0.9	0.75	_ \		0.75	_	
		$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V _{CC} × 0.75	$\langle \rangle$	/_	V _{CC} × 0.75	_		
High-level	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_		V _{CC} × 0.75	_	٧
output voltage VOH or V _{IL}	OI VIL	$l_{OH} = -3.0 \text{ mA}$	1.65 to 1.95	V _{CC} -0.45	<u> </u>		V _{CC} -0.45			
			1 _{OH} = −4.0 mA	2.3 to 2.7	2.0	_		2.0	_	
		I _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	_	2.48			
		~	$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
<	\/\		$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
Low-level output voltage	VOL	V _{IN} = V _{IH}	I _{OL} = 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	٧
\ (($I_{OL} = 3.0 \text{ mA}$	1.65 to 1.95	_	_	0.45	_	0.45	
		\rightarrow ($I_{OL} = 4.0 \text{ mA}$	2.3 to 2.7	_	_	0.4		0.4	
			I _{OL} = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current	I _{IN}	V _{IN} = 0 to 5.5V		0 to 3.6		_	±0.1	_	±1.0	μА
Power off leakage current	l _{OFF}	V _{IN} = 0 to 5.5V V _{OUT} = 0 to 3.6V		0	_	_	1.0	_	10.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		3.6	_	_	1.0	_	10.0	μА

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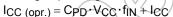
AC Electrical Characteristics (unless otherwise specified, Input $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			V _{CC} (V)	Min	Тур.	Max	Min	Max	
		$\begin{aligned} C_L &= 10 \text{ pF}, \\ R_L &= 1 \text{ M}\Omega \end{aligned}$	0.9	_	26.9	_	_	_	
			1.1 to 1.3	_	10.9	20.7	1.0	38.6	ns
			1.4 to 1.6	_	5.9	9.6	1.0	11.3	
			1.65 to 1.95	_	4.5	7.0	1.0	7.5	
			2.3 to 2.7	_	2.9	4:4	1.0	4.9	
Propagation delay time			3.0 to 3.6	-//	2.2	3.5	1.0	4.1	
	tpLH tpHL	$C_L = 15 pF$, $R_L = 1 M\Omega$	0.9	-((30.0		_	_	
			1.1 to 1.3		12.0	24.2	1.0	42.0	
			1.4 to 1.6		6.5	10.5	1,0	12.6	
			1.65 to 1.95		5.0	7.7	1.0	8.0	
			2.3 to 2.7	7A-V	3.2	4.9	1.0	5.6	
			3.0 to 3.6	<i>J</i>	2.5	3.8	(1.0)	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	45.0	7		_	
			1.1 to 1.3	_	18.0	33.4	1.0	63.2	
			1.4 to 1.6	_	8.9	14.8	1.0	17.9	
			1.65 to 1.95	_	6.9	10.3	1.0	10.8	
			2.3 to 2.7		4.4	6.4	1.0	6.8	
			3.0 to 3.6	`	3.5	4.9	1.0	5.4	
Input capacitance	C _{IN}	((-))	3.6	1	// 3		_	_	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 to 3.6		6	_	_	_	pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

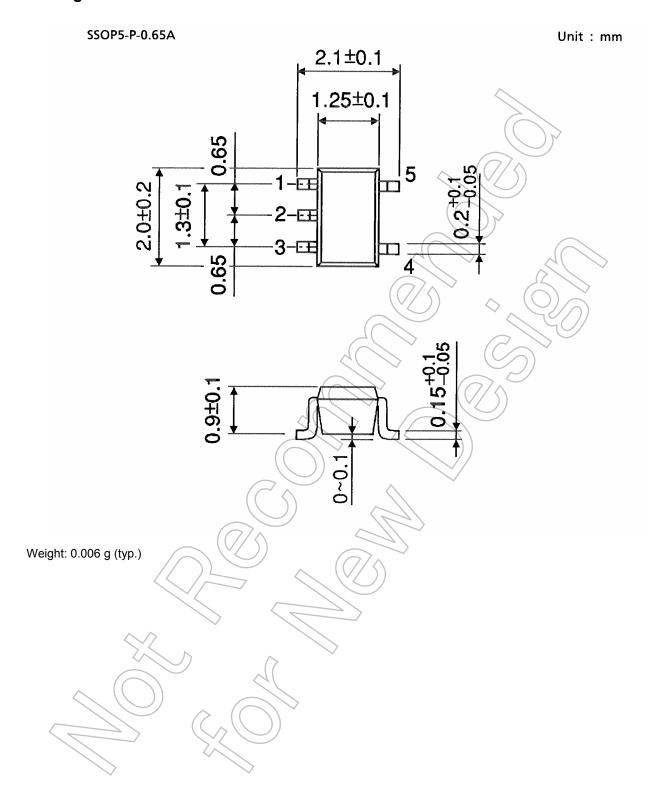
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Average operating current can be obtained by the equation:





Package Dimensions



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