TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG02FE

2-Input NOR Gate

Features

- High output current
 - : ± 8 mA (min) at V_{CC} = 3.0 V

Symbol

Vcc

VIN

- Super high speed operation : t_{pd} = 2.4 ns (typ.)
 - at V_{CC} = 3.3 V,15pF
- Operating voltage range : V_{CC} = 0.9 to 3.6 V

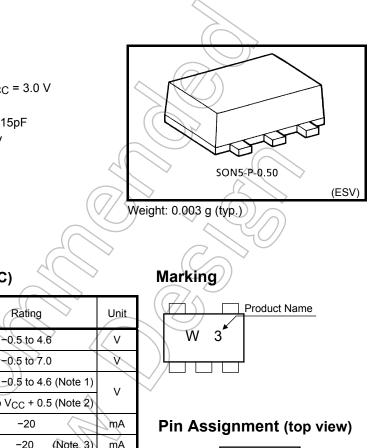
Absolute Maximum Ratings (Ta = 25°C)

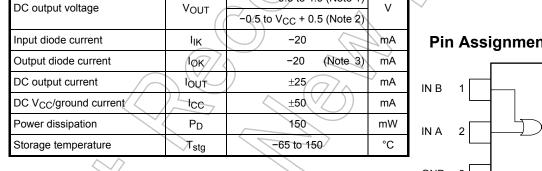
- 5.5-V tolerant inputs
- 3.6-V power down protection output

Characteristics

Supply voltage

DC input voltage

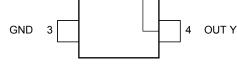




Rating

-0.5 to 4.6

-0.5 to 7.0



- Note:
 - Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings. Note 3: V_{OUT} < GND

5 V_{CC}

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IEC Logic Symbol



Truth Table								
	А	В	Y					
	L	L	Н					
	L	Н	L					
	Н	L	L					
	Н	Н	L					

Operating Ranges

berating Ranges		\sim (7/s)	_
Characteristics	Symbol	Rating	
Supply voltage	V _{CC}	0.9 to 3.6	
Input voltage	V _{IN}	0 to 5.5 V	
Output voltage	Vour	0 to 3.6 (Note 4)	
Oulput voltage	V _{OUT}	0 to V _{CC} (Note 5)	$\leq >$
		± 8.0 (Note 6)	YM.
		±4.0 (Note 7)	
Output Current	IOH/IOL	±3.0 (Note 8) (MA)	
Output Current	UH/UL	± 1.7 (Note 9))
		± 0.3 (Note 10)	
		± 0.02 (Note 11)	
Operating temperature	T _{opr}	-40 to 85 °C	
Input rise and fall time	dt/dv	0 to 10 (Note 12) ns/V	
			_

Note 4: $V_{CC} = 0V$

Note 5: High or Low state.

Note 6: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ Note 7: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 8: $V_{CC} = 1.65$ to 1.95 V

Note 9: $V_{CC} = 1.4$ to 1.6 V

Note 10: $V_{CC} = 1.1$ to 1.3 V

Note 11: $V_{CC} = 0.9 V$

Note 12: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	I Test Condition			Ta = 25°C			$Ta = -40 \text{ to } 85^{\circ}C$		Unit
Sym Sym		Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
				0.9	V _{CC}	_	K	V _{CC}	_	
				1.1 to 1.3	V _{CC} × 0.7			V _{CC}		
High-level input voltage	VIH		_	1.4 to 1.6	V _{CC} × 0.65	-(C		V _{CC} × 0.65		V
Vollago				1.65 to 1.95	V _{CC} × 0.65		\mathcal{D}	V _{CC} × 0.65		
				2.3 to 2.7	1.7	(-)	> _	1.7	—	
				3.0 to 3.6	2.0		_	2.0	_	
				0.9	đ	\geq	GND	Xt)	GND	
Low-level input voltage	VIL			1.1 to 1.3	75	> _ <	V _{CC} × 0.3		V _{CC} × 0.3	V
				1.4 to 1.6	\sum	_	V _{CC} × 0.35	L)	V _{CC} × 0.35	
				1.65 to 1.95	<u> </u>	-((V _{CC} × 0.35	>_	$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$	
			G	2.3 to 2.7	_	$\overline{}$			0.7	
			a	3.0 to 3.6	1))0.8		0.8	
			I _{OH} =-0.02 mA	0.9	0.75) -	_	0.75		
			I _{OH} = -0.3 mA	1.1 to 1.3	Vcc × 0.75)}		V _{CC} × 0.75		
High-level output	V _{ОН}	VIN = VIL	10H = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75			V _{CC} × 0.75		V
voltage			1 _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45	_		V _{CC} -0.45		
	\frown		I _{OH} = -4.0 mA	2.3 to 2.7	2.0	_	_	2.0	_	
		$) \subseteq$	1 _{OH} = -8.0 mA	3.0 to 3.6	2.48	_		2.48	_	
	$\langle \rangle$		I _{OL} = 0.02 mA	0.9	_	_	0.1		0.1	
		\triangleright	I _{OL} = 0.3 mA	1,1 to 1.3	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
Low-level output	Vol	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 1.7 mA	1.4 to 1.6		_	$V_{CC} \times 0.25$		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	V
	$\sum_{i=1}^{n}$		loL = 3.0 mA	1.65 to 1.95			0.45		0.45	
			l _{OL} = 4.0 mA	2.3 to 2.7	_	_	0.4	—	0.4	
	/	> (C	I _{OL} = 8.0 mA	3.0 to 3.6	_	—	0.4	—	0.4	
Input leakage current	IIN	$V_{IN} = 0$ to 5.5 V		0 to 3.6	_	_	±0.1	_	±1.0	μA
Power off leakage current	I _{OFF}	V _{IN} = 0 to 5.5 V V _{OUT} = 0 to 3.6 V		0	_	_	1.0		10.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$ or GND		3.6	_	_	1.0	_	10.0	μΑ

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Oberestaristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		L Incid
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	17.0		—	_	ns
			1.1 to 1.3	_	8.8	18.4	1.0	34.2	
			1.4 to 1.6		5.0	8.5	1.0	10.0	
			1.65 to 1.95		3.8	6.2	1.0	6.7	
	tр∟н tрн∟		2.3 to 2.7		2.7	3.9	1.0	4.4	
			3.0 to 3.6		2.1	3.1	1.0	3.7	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	20.7	\mathbb{Z}	—	_	
			1.1 to 1.3	_ (10.6	21.5	1.0	37.2	
Propagation delay time			1.4 to 1.6		5.9	9.3	1.0	11.2	
r ropagation delay time			1.65 to 1.95	\mathcal{A}	4.5	6.9	1.0	7.1	
			2.3 to 2.7	\geq	3.0	4.4	1.0	5.0	
			3.0 to 3.6	//	2.4	3.4)1.0	3.9	
		C _L = 30 pF, R _L = 1 MΩ	0.9))	29.6	$\langle \boldsymbol{\mathcal{H}} \rangle$	(4)/	_	
			1.1 to 1.3	$\geq -$	14.8	29.6	1.0	56.0	
			1.4 to 1.6	_	8.0	13.1	1.0	15.9	
			1.65 to 1.95	_	6.0	9.2	1.0	9.6	
			2.3 to 2.7		3.9)) 5.7	1.0	6.1	
			3.0 to 3.6		3.0	4.4	1.0	4.8	
Input capacitance	C _{IN}		3.6	/	3	—	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 to 3.6	$\langle \rangle$	6	—	—	_	pF

Note 13: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

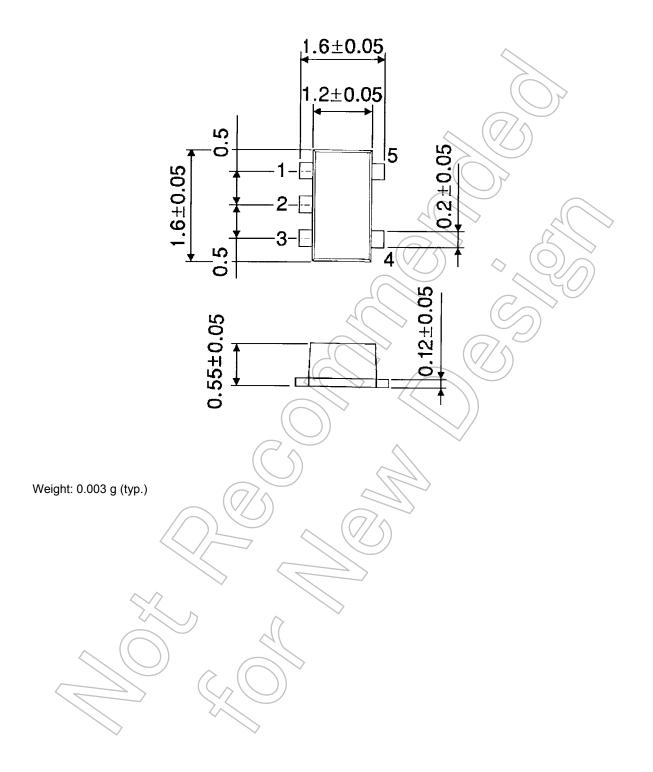
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.50

Unit : mm



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