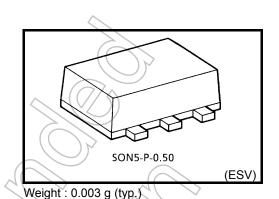
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SG07FE

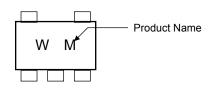
Non-Inverter (Open Drain)

#### **Features**

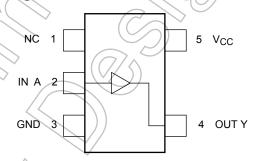
- High output current: 8mA (min.) at V<sub>CC</sub> = 3.0V
- High speed operation:  $t_{pZL}$  = 2.5 ns (typ.) at  $V_{CC}$  = 3.3 V, 15 pF
- Operating voltage range: V<sub>CC</sub> = 0.9 to 3.6 V
- 5.5-V tolerant input
- 3.6-V power down protection output



#### Marking



#### Pin Assignment (top view)



# Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	-0.5 to 4.6	V
DC input voltage	VIN	-0.5 to 7.0	V
DC output voltage	Vout	-0.5 to 4.6 (Note 1)	٧
Input diode current	l <sub>IK</sub>	-20	mA
Output diode current	I <sub>OK</sub> />	-20 (Note 2)	mA
DC output current	IOUT (	25	mA
DC V <sub>CC</sub> /ground current	tce	±50	mA
Power dissipation	(PD)	150	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

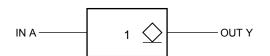
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

Note 2: VOUT < GND

Start of commercial production 2007-08

# **IEC Logic Symbol**



#### **Truth Table**

Α	Υ
L	L
Н	Z

Z: High impedance

## **Operating Ranges**

Characteristics	Symbol	Rating
Supply voltage	V <sub>CC</sub>	0.9 to 3.6
Input voltage	V <sub>IN</sub>	0 to 5.5
Output voltage	V <sub>OUT</sub>	0 to 3.6
Output current	l <sub>OL</sub>	8.0 (Note 3)
		4.0 (Note 4)
		3.0 (Note 5) mA
		1:7 (Note 6)
		0.3 (Note 7)
		0.02 (Note 8)
Operating temperature	Topr	-40 to 85 °C
Input rise and fall time	dt/dv	0 to 10 (Note 9) ns/V

Note 3:  $V_{CC}$  = 3.0 to 3.6 V

Note 4:  $V_{CC}$  = 2.3 to 2.7 V

Note 5: V<sub>CC</sub> = 1.65 to 1.95 V

Note 6: V<sub>CC</sub> = 1.4 to 1.6 V

Note 7:  $V_{CC}$  = 1.1 to 1.3 V

Note 8: V<sub>CC</sub> = 0.9 V

Note 9:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V



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## **Electrical Characteristics**

#### **DC Characteristics**

Characteristics Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
Characteristics	Symbol	rest Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
High-level				0.9	V <sub>CC</sub>		- <	V <sub>CC</sub>		
	V <sub>IH</sub>	_		1.1 to 1.3	V <sub>CC</sub> × 0.7	l		V <sub>CC</sub> × 0.7		V
				1.4 to 1.6	V <sub>CC</sub> × 0.65		76	V <sub>CC</sub> × 0.65	<i>&gt;</i> _	
input voltage				1.65 to 1.95	V <sub>CC</sub> × 0.65	4	Y.	V <sub>CC</sub> × 0.65	_	
				2.3 to 2.7	1.7	-((	7)	1.7	_	
				3.0 to 3.6	2.0			2.0		
		_		0.9	- <	(-/	GND		GND	
				1.1 to 1.3	6		V <sub>CC</sub> × 0.3	-52	VCC × 0.3	V
Low-level	V <sub>IL</sub>			1.4 to 1.6		<i>)</i>	V <sub>CC</sub> × 0.35		V <sub>CC</sub> × 0.35	
input voltage				1.65 to 1.95	//		V <sub>CC</sub> × 0.35	7	V <sub>CC</sub> × 0.35	
				2.3 to 2.7	>	_	0.7	4	0.7	
				3.0 to 3.6	_	- (	0.8 \$	\ -	0.8	
	V <sub>OL</sub> \	V <sub>IN</sub> = V <sub>IL</sub>	$I_{OL} = 0.02 \text{ mA}$	0.9	-//		0.1	_	0.1	
			$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	4	-	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
Low-level output voltage			I <sub>OL</sub> = 1.7 mA	1.4 to 1.6		<i>\\</i>	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	V
output rollago			$I_{OL} = 3.0 \text{ mA}$	1.65 to 1.95	4	_	0.45	_	0.45	
			$I_{OL} = 4.0 \text{ mA}$	2.3 to 2.7	15	> —	0.4	_	0.4	
		I <sub>OL</sub> = 8.0 mA		3.0 to 3.6	<i>&gt;</i>	_	0.4	_	0.4	
Input leakage current	lın	$V_{IN} = 0 \text{ to } $	5.5V	0 to 3.6	) —	_	±0.1	_	±1.0	μА
Output OFF state current	loz	$V_{IN} = V_{IH}$ $V_{OUT} = 0 t$	to 3.6 V	0.9 to 3.6	_	_	±1.0	_	±10.0	μА
Power-off leakage current	loff	V <sub>IN</sub> = 5.5 \ or V <sub>OUT</sub> =		0.0	_	_	1.0	_	10.0	μА
Quiescent supply current	lec	V <sub>IN</sub> = V <sub>CC</sub>	or GND	3.6	_	_	1.0	_	10.0	μА

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## AC Characteristics (Unless otherwise specified, Input: $t_r = t_f = 3$ ns)

	ties Osmalist Test Osmalities			Ta = 25°C		Ta = -40 to 85°C		Unit	
Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
		$\begin{aligned} C_L &= 10 \text{ pF}, \\ R_L &= 100 \text{ k}\Omega \end{aligned}$	0.9	_	11.9	_	_	_	
			1.1 to 1.3	_	6.3	11.5	1.0	15.0	
			1.4 to 1.6	_	4.2	6.5	1.0	9.5	
		$C_L = 10 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95	_	3.4	5.5	(1.0	7.1	
			2.3 to 2.7	_	2.7	3.9	1.0	4.5	
			3.0 to 3.6	_	2.3	3.4	1.0	3.9	
		$\begin{aligned} C_L &= 15 \text{ pF}, \\ R_L &= 100 \text{ k}\Omega \end{aligned}$	0.9	_	12.8		_		
			1.1 to 1.3	_	7.2	12.8	1.0	17.5	
Propagation delay time	$t_{pZL}$		1.4 to 1.6		4.6	7.7	1.0	10.5	ns
		$C_L = 15 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95	_	3.9	6.6	1.0	7.9	$\checkmark$
			2.3 to 2.7	+	3.2	4.5	1.0	5.5	
			3.0 to 3.6	7,5	2.5	3.7	1.0	4.6)	
		$\begin{aligned} C_L &= 30 \text{ pF}, \\ R_L &= 100 \text{ k}\Omega \end{aligned}$	0.9		16.4	-6			
			1.1 to 1.3	$\rightarrow$	9.4	17.8	1.0)	21.5	
			1,4 to 1.6	> —	5.7	9.8	1.0	12.1	
		$C_L = 30 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to1.95	-/	4.4	7.5	1.0	10.3	
			2.3 to 2.7	+(	3.6	5.3	1.0	6.5	
			3.0 to 3.6		2.8	4.1	1.0	5.1	
		$\begin{aligned} C_L &= 10 \text{ pF,} \\ R_L &= 100 \text{ k}\Omega \end{aligned}$	0.9	_	112.5	_	_		
/			1.1 to 1.3	1	8.6	15.7	1.0	22.7	
			1.4 to 1.6	12	7.5	9.5	1.0	10.6	
		$C_L = 10 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95	3	7.1	8.7	1.0	9.6	
			2.3 to 2.7	$\setminus -$	6.8	7.9	1.0	8.8	
			3.0 to 3.6	/ _	6.5	7.5	1.0	8.4	
		$\begin{aligned} C_L &= 15 \text{ pF}, \\ R_L &= 100 \text{ k}\Omega \end{aligned}$	0.9	_	134.9	_	_		
<u> </u>			1.1 to 1.3	_	10.5	16.8	1.0	24.7	
Propagation delay time	$\int_{t_{pLZ}}$		1.4 to 1.6	_	9.0	10.4	1.0	11.3	ns
		$C_L = 15 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95	_	8.5	9.7	1.0	10.5	
	^		2.3 to 2.7	_	7.9	8.8	1.0	10.1	
	((\)		3.0 to 3.6	_	7.6	8.3	1.0	9.5	
	7,	$C_L = 30 \text{ pF},$ $R_L = 100 \text{ k}\Omega$	0.9	_	214.5		_		
			1.1 to 1.3	_	14.1	18.6	1.0	26.7	
		$C_L = 30 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.4 to1.6	_	13.5	14.5	1.0	16.0	
			1.65 to 1.95	_	12.7	13.8	1.0	15.0	
			2.3 to 2.7	_	12.2	13.5	1.0	14.7	
			3.0 to 3.6	_	11.9	12.8	1.0	14.4	
Input capacitance	C <sub>IN</sub>	_	3.6	_	3	_	_	_	pF
Power dissipation capacitance	$C_{PD}$	(Note10)	0.9 to 3.6	_	6	_	_	_	pF

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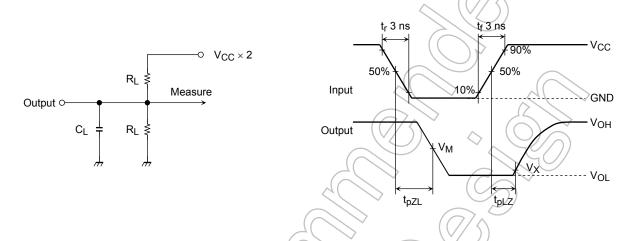
Note 10 : C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

#### **Measurement Circuit for AC Characteristic**

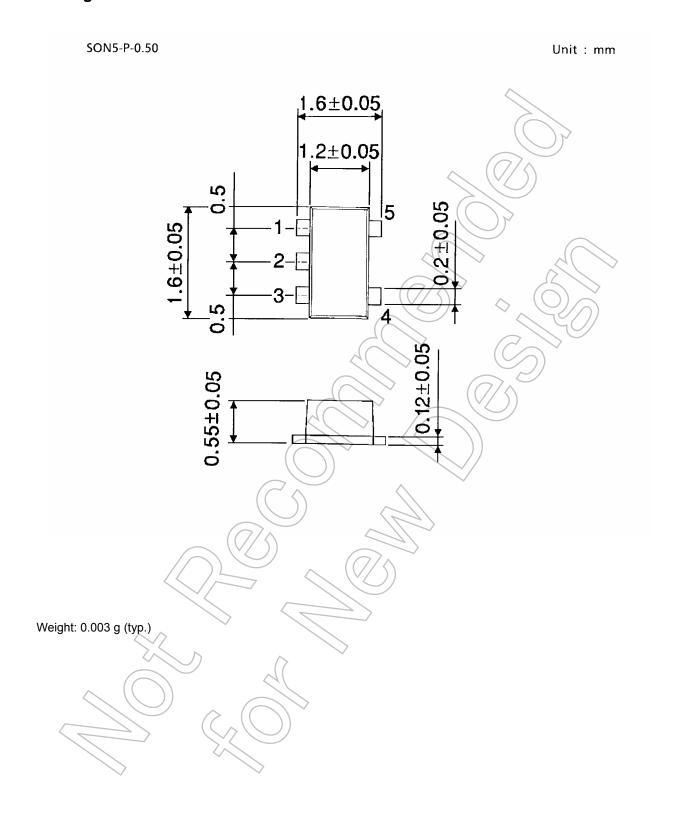
## **Measurement Waveform**



Symbol	Vcc								
Oymbor	3.3±0.3 V	2.5±0.2 V	1.8±0.15 V	1.5±0.1 V	1,2±0.1 V	0.9 V			
$V_{M}$	V <sub>CC</sub> / 2	V <sub>CC</sub> /2	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2			
VX	V <sub>OL</sub> + 0.3 V	V <sub>OL</sub> + 0.15 V	V <sub>OL</sub> + 0.15 V	V <sub>OL</sub> + 0.1 V	V <sub>OL</sub> + 0.1 V	V <sub>OL</sub> + 0.1 V			

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# **Package Dimensions**



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