TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

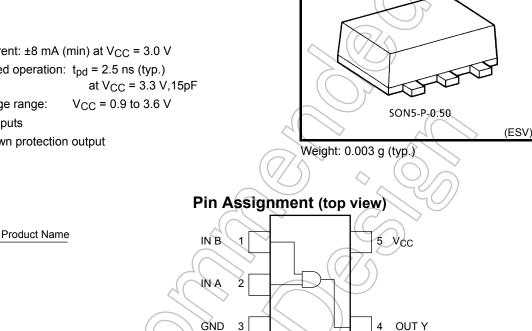
TC7SG08FE

2-Input AND Gate

Features

Marking

- High output current: ±8 mA (min) at V_{CC} = 3.0 V
- Super high speed operation: t_{pd} = 2.5 ns (typ.)
- Operating voltage range: V_{CC} = 0.9 to 3.6 V
- 5.5-V tolerant inputs
- 3.6-V power down protection output



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	-0.5 to 4.6	V
DC input voltage	VIN	-0.5 to 7.0	V
	Vout	-0.5 to 4.6 (Note 1)	V
DC output voltage		-0.5 to V _{CC} + 0.5 (Note 2)	v
Input diode current	I _{IK}	-20	mA
Output diode current	Іок	-20 (Note 3)	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	50	mW
Storage temperature	Tstg	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:
$$V_{CC} = 0V$$

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: V_{OUT} < GND

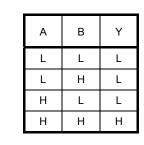
Start of commercial production 2005-02

TOSHIBA

IEC Logic Symbol

Truth Table





Operating Ranges

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Characteristics	Symbol	Rating	Unit	6
Supply voltage	V _{CC}	0.9 to 3.6	v	
Input voltage	V _{IN}	0 to 5.5	v (f)	
Output voltage	V _{OUT}	0 to 3.6 (Note 4) 0 to V _{CC} (Note 5)	- V	2
Output Current	IOH/IOL	$\begin{array}{c c} \pm 8.0 & (Note 6) \\ \hline \pm 4.0 & (Note 7) \\ \hline \pm 3.0 & (Note 8) \\ \hline \pm 1.7 & (Note 9) \\ \hline \pm 0.3 & (Note 10) \\ \hline \pm 0.02 & (Note 11) \end{array}$	mA	
Operating temperature	Topr	-40 to 85	°C	
Input rise and fall time	(dt/dv	0 to 10 (Note 12)	ns/V	

Note 4: $V_{CC} = 0V$ Note 5: High or Low state: Note 6: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ Note 7: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ Note 8: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$ Note 9: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$ Note 10: $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$ Note 11: $V_{CC} = 0.9 \text{ V}$ Note 12: $V_{IN} = 0.8 \text{ to } 2.0 \text{ V}$, $V_{CC} = 3.0 \text{ V}$

Electrical Characteristics

DC Characteristics

Characteristics	Characteristics Symbol Test Condition			Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit	
Sindraciensiics Symbol		Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
				0.9	V _{CC}	—	\mathcal{I}	V _{CC}	_	V
				1.1 to 1.3	V _{CC} × 0.7	_	Ĥ	Vcc × 0.7	_	
High-level input voltage	VIH			1.4 to 1.6	V _{CC} × 0.65	-((V _{CC} × 0.65		
voluge				1.65 to 1.95	V _{CC} × 0.65		\sum	$\begin{array}{c} V_{CC} \\ \times \ 0.65 \end{array}$		
				2.3 to 2.7	1.7	(-)	2-	1.7	—	
				3.0 to 3.6	2.0		_	2.0	-	
				0.9	J.	\rightarrow	GND	A	GND	v
				1.1 to 1.3	775	>	V _{CC} × 0.3	37	V _{CC} × 0.3	
Low-level input voltage	VIL			1.4 to 1.6	\mathcal{D}	_	V _{CC} × 0.35	ZD) V _{CC} × 0.35	
vonage				1.65 to 1.95	<u> </u>	- (V _{CC} × 0.35	>_	$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$	
			G	2.3 to 2.7	_	E	0.7	—	0.7	
			20	3.0 to 3.6	-($\langle \Psi \rangle$) 0.8	_	0.8	
			I _{OH} =-0.02 mA	0.9	0.75	$\langle - \rangle$	_	0.75	—	
			I _{OH} = -0.3 mA	1.1 to 1.3	Vcc × 0.75	$) \rightarrow$		$\begin{array}{c} V_{CC} \\ \times \ 0.75 \end{array}$		
igh-level output	V _{ОН}	VIN = VIH	10H = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	×	_	V _{CC} × 0.75	_	V
voltage			IOH = -3.0 mA	1.65 to 1.95	Vcc -0.45	_	_	V _{CC} -0.45		
			ЮН = -4.0 mA	2.3 to 2.7	2.0	_	_	2.0	_	
		$) \leq$	I _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	_	2.48	_	
	\leq		I _{OL} = 0.02 mA	0.9		_	0.1	_	0.1	
		\supset	I _{OL} = 0.3 mA	1.1 to 1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
Low-level output	VOL	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 1.7 mA	1.4 to 1.6		_	V _{CC} × 0.25	_	V _{CC} × 0.25	V
			l _{OL} = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	
			I _{OL} = 4.0 mA	2.3 to 2.7			0.4	_	0.4	
		> (C	I _{OL} = 8.0 mA	3.0 to 3.6			0.4		0.4	
Input leakage current	IIN	$V_{\rm IN} = 0$ to 5.5 V		0 to 3.6			±0.1		±1.0	μA
Power off leakage current	I _{OFF}	V _{IN} = 0 to 5.5 V V _{OUT} = 0 to 3.6 V		0			1.0	_	10.0	μA
Quiescent supply current	ICC	VIN = VCC or GND		3.6		_	1.0	_	10.0	μΑ

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
Characteristics			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	tpLH tpHL	$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	26.9	_	_	—	
			1.1 to 1.3	_	10.9	20.7	1.0	38.6	
			1.4 to 1.6		5.9	9.6	1.0	11.3	ns
			1.65 to 1.95	_	4.5	7.0	1.0	7.5	
			2.3 to 2.7	_	2.9	4.4	1.0	4.9	
			3.0 to 3.6	- ~	2.2	3.5	1.0	4.1	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	30.0	9		—	
			1.1 to 1.3	_	12.0	24.2	1.0	42.0	
			1.4 to 1.6	7	6.5	10.5	1.0	12.6	
			1.65 to 1.95	Æ	5,0	7.7	1.0	8.0	
			2.3 to 2.7		3.2	4.9	2 1.0	5.6	
			3.0 to 3.6		2.5	3.8)1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		45.0		~~~/)	/ _	
			1.1 to 1.3	\rightarrow	18.0	33.4	1.0	63.2	
			1.4 to 1.6	_	8.9	14.8)	1.0	17.9	
			1.65 to 1.95	_	6.9	10.3	1.0	10.8	
			2.3 to 2.7		4.4) 6.4	1.0	6.8	
			3.0 to 3.6		3.5	4.9	1.0	5.4	
Input capacitance	C _{IN}		3.6	$\overline{\langle}$	3	—	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 to 3.6		6	_	—	—	pF

Note 13: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

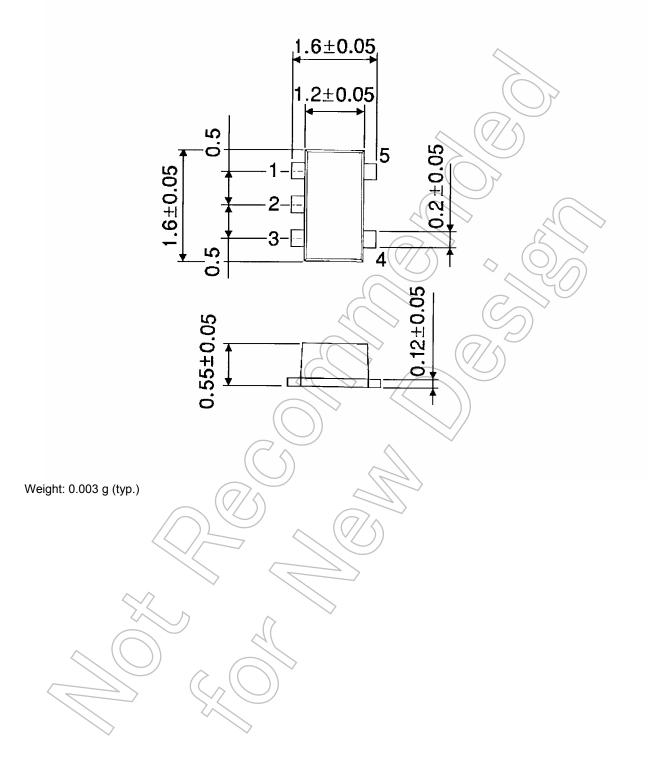
Average operating current can be obtained by the equation: $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.50

Unit : mm



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