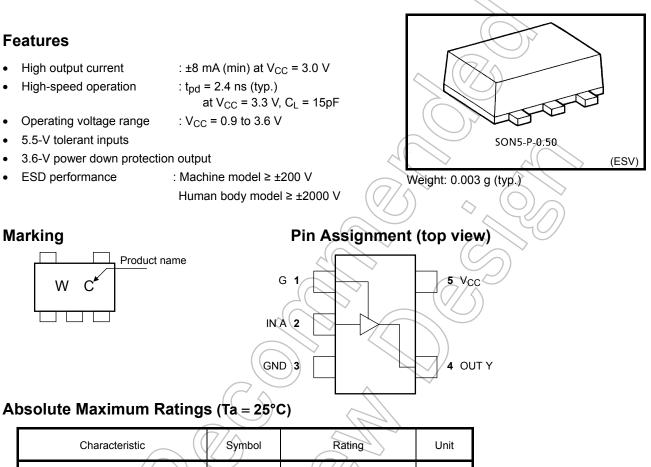
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG126FE

Bus Buffer with 3-STATE Output



		onit
V _{CC}	−0.5 to 4.6	V
V _{IN}	-0.5 to 7.0	V
Vour	-0.5 to 4.6 (Note 1)	V
VOUI	-0.5 to V _{CC} + 0.5 (Note 2)	v
lik	-20	mA
<-lok	-20 (Note 3)	mA
IOUT	±25	mA
Icc	±50	mA
Pp	150	mW
T _{stg}	−65 to 150	°C
		V _{CC} -0.5 to 4.6 V _{IN} -0.5 to 7.0 -0.5 to 4.6 (Note 1) VOUT -0.5 to V _{CC} + 0.5 (Note 2) I _{IK} -20 I _{OK} -20 (Note 3) I _{OUT} ±25 I _{CC} ±50 P _B 150

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:
$$V_{CC} = 0V$$

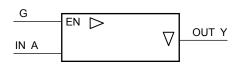
Note 2: High or Low State. Do not exceed $I_{\mbox{OUT}}$ of absolute maximum ratings.

Note 3: V_{OUT} < GND

Start of commercial production 2005-04

<u>TOSHIBA</u>

IEC Logic Symbol



1	Truth Table								
	G	А	Y						
	L	Х	Z						
	Н	L	L						
	Н	Н	Н						

Operating Ranges

Operating Ranges			$\overline{\overline{\Omega}}$					
Characteristic	Symbol	Rating	Unit					
Supply voltage	V _{CC}	0.9 to 3.6) > v					
Input voltage	V _{IN}	0 to 5.5	V					
Output voltage	V _{OUT}	0 to 3.6 (Note 4)						
	V 001	0 to V _{CE} (Note 5)						
		±8.0 (Note 6)						
		±4.0 (Note 7)	<u> </u>					
Output current	I _{OH} /I _{OL}	±3.0 (Note 8)	mA					
	OTFOL	±1.7 (Note 9)	(\mathcal{A})					
		±0.3 (Note 10)	20					
		±0.02 (Note 11)						
Operating temperature	T _{opr}	-40 to 85	°C					
Input rise and fall time	dt/dv	0 to 10 (Note 12)	ns/V					
Note 4: $V_{CC} = 0V$								
Note 5: High or Low state.	$(C \land$							
Note 6: $V_{CC} = 3.0$ to 3.6 V								
Note 7: $V_{CC} = 2.3$ to 2.7 V	7/5							
Note 8: $V_{CC} = 1.65$ to 1.95 V	\bigcirc	\overline{O}						
Note 9: $V_{CC} = 1.4$ to 1.6 V		$\langle \langle \vee \rangle \rangle$						
Note 10: $V_{CC} = 1.1$ to 1.3 V								
Note 11: V _{CC} = 0.9 V								

Note 12: V_{IN} = 0.8 to 2.0 V, V_{CC} = 3.0 V

Electrical Characteristics

DC Characteristics

Characte	rictic	Symbol	Test Condition $Ta = 25^{\circ}$ V_{CC} (V) Min Typ.		Ta = 25°C				2	Ta = -40	Unit
Characte	IISUC	Symbol			Тур.	Max	Min	Max	Unit		
					0.9	V _{CC}	_ <	/	V _{CC}	_	
					1.1 to 1.3	V _{CC} × 0.7	_		V _{CC} × 0.7		
	High level	VIH		_	1.4 to 1.6	V _{CC} × 0.65	6		V _{CC} × 0.65	_	
					1.65 to 1.95	V _{CC} × 0.65		Y	V _{CC} × 0.65	_	
					2.3 to 2.7	1.7	46	_	1.7	_	
Input voltage					3.0 to 3.6	2.0	\geq	—	2.0	—	v
input voltage					0.9	L (\geq	GND	4E	GND	v
					1.1 to 1.3		_	V _{CC} × 0.3		$V_{CC} \times 0.3$	
	Low level	V _{IL}		_	1.4 to 1.6	9_		Vcc × 0.35	L)	V _{CC} × 0.35	
				<	1.65 to 1.95	_	-(0	V _{CC} × 0.35	~ _	V _{CC} × 0.35	
				\bigcirc	2.3 to 2.7		(F)	0.7	_	0.7	
					3.0 to 3.6	(0.8	_	0.8	
				I _{OH} =-0.02 mA	0.9	0.75			0.75		
				I _{OH} = -0.3 mA	1.1 to 1.3	V _{CC} × 0.75))_	_	V _{CC} × 0.75		
	High level	V _{OH}		1 _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_		$V_{CC} \times 0.75$	_	
				+ _{ОН} = −3.0 mA	1.65 to 1.95	V _{CC} -0.45			V _{CC} -0.45		
		\frown	(// 5)	I _{OH} = -4.0 mA	2.3 to 2.7	2.0	_		2.0		
Output voltage		\bigcap		I _{OH} = -8.0 mA	3.0 to 3.6	2.48	_		2.48		V
		\sum		I _{OL} = 0.02 mA	0.9			0.1	_	0.1	
			4	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_		V _{CC} × 0.25	_	V _{CC} × 0.25	
	Low level	VoL	VIN = VIL or VIH	I _{OL} = 1.7 mA	1.4 to 1.6			V _{CC} × 0.25		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
		2	2	l _{OL} = 3.0 mA	1.65 to 1.95			0.45	_	0.45	
$\langle \langle \rangle$	())			I _{OL} = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	
				I _{OL} = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage ci	urrent	IN	$V_{IN} = 0$ to	5.5V	0 to 3.6			±0.1		±1.0	μA
3-state output o current	ff-state	I _{OZ}	VIN = VIH V _{OUT} = 0		0.9 to 3.6	—	—	1.0	_	10.0	μA
Power off leaka	ge current	I _{OFF}	V _{IN} = 0 to V _{OUT} = 0		0.0	—	_	1.0	_	10.0	μA
Quiescent supp	ly current	ICC	$V_{IN} = V_{CC}$	or GND	3.6		_	1.0		10.0	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristic	Symbol	Test Condition			Ta = 25°C	;	Ta = -40	to 85°C	Unit				
Characteristic	Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit				
			0.9	_	15.3	—	—	_					
			1.1 to 1.3		8.3	18.4	1.0	34.2					
		C _L = 10 pF,	1.4 to 1.6		5.0	8.5	1.0	10.0					
		$R_L = 1 M\Omega$	1.65 to 1.95		4.0	6.2	1.0	6.7					
			2.3 to 2.7	_	2.6	3.9	1.0	4.4					
			3.0 to 3.6	- ~	2.1	3,1	1.0	3.7					
			0.9	_	17.7	\sum	—	_					
			1.1 to 1.3	(9.6	21.5	1.0	37.2					
Propagation delay time	t _{pLH}	C _L = 15 pF,	1.4 to 1.6	$\left(\left(\right) \right)$	5.6	9.3	1.0	11.2	20				
Fropagation delay time	t _{pHL}	$R_L = 1 M\Omega$	1.65 to 1.95	Æ	4,5	6.9	1.0	7.1	ns				
			2.3 to 2.7		2.9	4.4	1.0	5.0					
			3.0 to 3.6	//	2.4	3.4)1.0	3.9					
			0,9		29.0	K	CZ)/	—					
			1.1 to 1.3	\rightarrow	14.5	29.6	1.0	56.0	-				
		C _L = 30 pF,	1.4 to 1.6		8.2	13.1	1.0	15.9					
		$R_{L} = 1 M\Omega$	1.65 to 1.95		6.0	9.2	1.0	9.6					
			2.3 to 2.7		4.0)) 5.7	1.0	6.1					
			3.0 to 3.6		3.3	4.4	1.0	4.8					
		$\begin{array}{l} C_{L} = 10 \ pF, \\ R_{L} = 100 \ k\Omega \end{array}$	0.9	Ń	22.7	_	_	—					
							1.1 to 1.3	_	10.9	18.7	1.0	29.8	
		\Box	1.4 to 1.6		5.9	8.7	1.0	9.8					
		$C_L = 10 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95	\rightarrow	4.5	6.3	1.0	6.8					
		$(\overline{\mathbf{A}})$	2.3 to 2.7	> -	3.1	4.2	1.0	4.5					
	0 > 0		3.0 to 3.6	_	2.4	3.2	1.0	3.5					
		$\begin{array}{l} C_{L} = 15 \; pF, \\ R_{L} = 100 \; k\Omega \end{array}$	0.9	_	25.3	_	_	_					
	\searrow		1.1 to 1.3	_	11.9	20.7	1.0	34.7					
Output enable time	t _{pZL}		1.4 to 1.6	_	6.5	9.5	1.0	11.1	ns				
	t _{pZH}	$C_L = 15 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95	_	4.9	6.8	1.0	7.2					
		15	2.3 to 2.7	_	3.3	4.4	1.0	4.8					
	- (-		3.0 to 3.6	—	2.5	3.4	1.0	3.7					
	$\sum_{i=1}^{n} (i)$	C _L = 30 pF, R _L = 100 kΩ	0.9		37.7	_	_	_					
	2		1.1 to 1.3		17.1	30.7	1.0	50.5					
			1.4 to 1.6	—	8.8	13.1	1.0	15.1					
		$C_L = 30 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95	—	6.6	9.2	1.0	9.9					
			2.3 to 2.7	_	4.1	5.4	1.0	5.8					
			3.0 to 3.6		3.1	4.1	1.0	4.5					

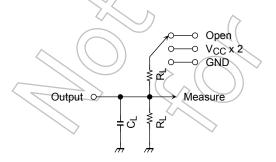
Characteristic	Symbol	Test Condition				Ta = -40 to 85°C		Unit							
Characteristic	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit						
		$\begin{array}{l} \textbf{C}_{L} = \textbf{10 pF}, \\ \textbf{R}_{L} = \textbf{100 k}\Omega \end{array}$	0.9	_	117.6	_	—	_							
			1.1 to 1.3	_	9.2	16.0	1.0	22.4							
			1.4 to 1.6		7.1	9.1	1.0	10.4							
		$C_L = 10 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95		6.7	8.3	1.0	9.0							
		-	2.3 to 2.7		6.2	7.3) 1.0	8.8							
			3.0 to 3.6		5.8	6.9	1.0	7.6							
		$C_L = 15 \text{ pF},$ $R_L = 100 \text{ k}\Omega$	0.9	_	139.2	\square	—	_							
									1.1 to 1.3	_	(10.0)	^{>} 16.9	1.0	25.1	
Output disable time		$C_L = 15 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.4 to 1.6	f	7.8	9.8	1.0	11.3	ns						
			C _L = 15 p⊦, R _L = 5 kΩ			1.65 to 1.95	Z	7.4	9.2	<1.0	10.6				
							_	2.3 to 2.7		7.0	8.2	1.0	10.3		
				3.0 to 3.6	$\langle \rangle$	6.8 <	7.7	1.9	9.5						
			$\begin{array}{l} C_L = 30 \ pF, \\ R_L = 100 \ k\Omega \end{array}$	0.9		230.8									
							1.1 to 1.3		14.0	20.8	1.0	31.9			
			1.4 to 1.6		12.2	13.5	1.0	14.9							
			$C_L = 30 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95	I	11,5))13.0	1.0	13.9						
						2,3 to 2.7		11.3	12.2	1.0	13.5				
			3.0 to 3.6	X	10.9	11.8	1.0	12.9							
Input capacitance	C _{IN}	$(\in))$	3.6	1	//3		—	—	pF						
Power dissipation capacitance	C _{PD}	(Note13)	0.9 to 3.6	_	8	_	—	_	pF						

Note 13:C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

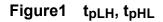
Average operating current can be obtained by the equation

 I_{CC} (opr.) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

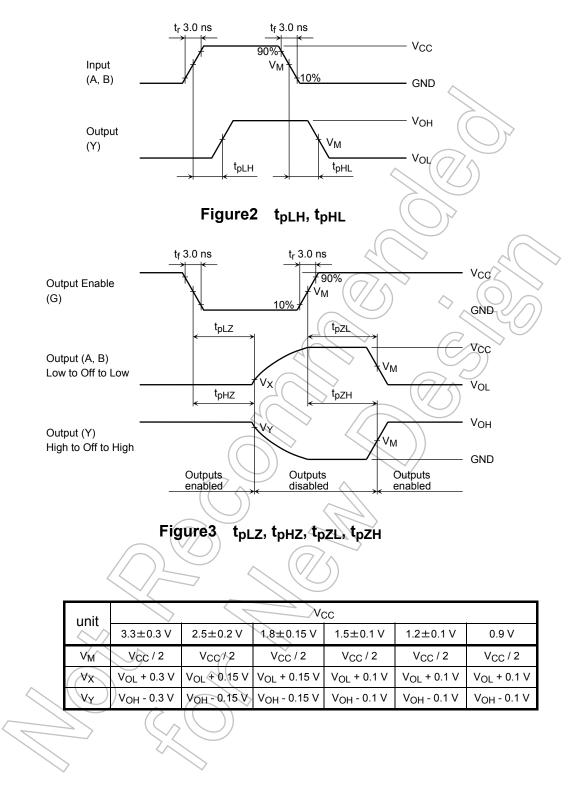
AC Characteristics Measurement Circuit



Characteristics	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	V _{CC} x 2
t _{pHZ} , t _{pZH}	GND



AC Characteristics Measurement Circuit

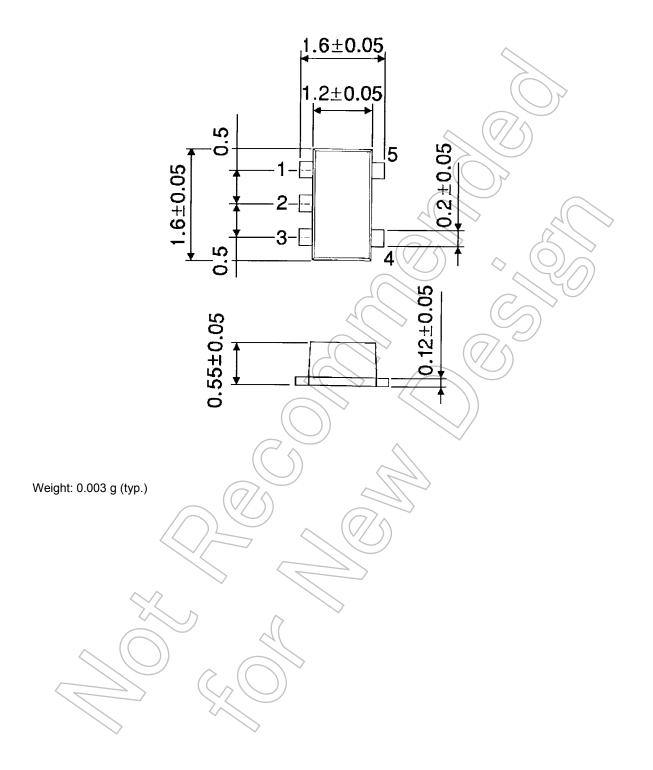


TOSHIBA

Package Dimensions

SON5-P-0.50

Unit : mm



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