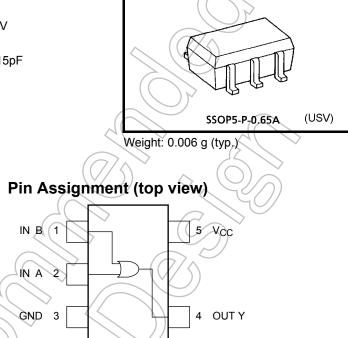
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SG32FU

#### 2 Input OR Gate

#### Features

- High output current: ±8 mA (min) at V<sub>CC</sub> = 3.0 V
- High-speed operation: t<sub>pd</sub> = 2.4 ns (typ.)
  - at V<sub>CC</sub> = 3.3 V, 15pF
- Operating voltage range:  $V_{CC}$  = 0.9 to 3.6 V
- 5.0-V tolerant inputs
- 3.6-V power down protection output.



#### Marking

# Absolute Maximum Ratings (Ta = 25°C)

Product name

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vec	-0,5 to 4,6	V	
DC input voltage	VIN	-0.5 to 7.0	V	
	Vout	-0.5 to 4.6 (Note 1)	V	
DC output voltage		-0.5 to V <sub>CC</sub> + 0.5 (Note 2)	v	
Input diode current	IIK	-20	mA	
Output diode current	Іок	-20 (Note 3)	mA	
DC output current	TUOL	±25	mA	
DC V <sub>CC</sub> /ground current	ICC	±50	mA	
Power dissipation	PD	200	mW	
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: 
$$V_{CC} = 0V$$

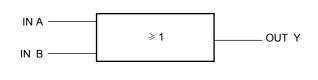
Note 3: V<sub>OUT</sub> < GND

Start of commercial production 2005-02

Note 2: High or Low State.  $I_{\mbox{OUT}}$  abusolute maximum rating must be observed.

# <u>TOSHIBA</u>

## **IEC Logic Symbol**



T	ruth	Table	e	_
	А	В	Y	
	L	L	L	
	L	Н	Н	
	Н	L	Н	$\langle$
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-				
			$\langle$	
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## **Operating Rating**

perating Rating			$\langle \bigcirc \rangle$	_
Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	0.9 to 3.6	v	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	$\mathcal{A}( \ \ )$
Output voltage	V <sub>OUT</sub>	0 to 3.6 (Note 4)	- v (	$\leq >$
	V001	0 to V <sub>CC</sub> (Note 5)		
		±8.0 (Note 6)		40
		±4.0 (Note 7)	$\mathcal{C}$	$>$ $\bigcirc$
Output Current	I <sub>OH</sub> /I <sub>OL</sub>	±3.0 (Note 8)	mA	
	UH/UL	±1.7 (Note 9)		
		±0.3 (Note 10)	$\mathcal{O}$	
	<	±0.02 (Note 11)		
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 12)	ns/V	
		A		-

Note 4:  $V_{CC} = 0V$ Note 5: High or Low state. Note 6:  $V_{CC} = 3.0$  to 3.6 V Note 7:  $V_{CC} = 2.3$  to 2.7 V Note 8:  $V_{CC} = 1.65$  to 1.95 V Note 9:  $V_{CC} = 1.4$  to 1.6 V Note 10:  $V_{CC} = 1.1$  to 1.3 V Note 11:  $V_{CC} = 0.9$  V Note 12:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V

### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test	Toet	t Condition		Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
onaracteristics	Oymbol	Circuit			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
					0.9	V <sub>CC</sub>	_ <	X	V <sub>CC</sub>		
					1.1 to 1.3	$\begin{array}{c} V_{CC} \\ \times \ 0.7 \end{array}$	—		V <sub>CC</sub> ×0.7		
High-level input voltage	VIH	_		_	1.4 to 1.6	$V_{CC} \times 0.65$	-(0		V <sub>CC</sub> × 0.65	_	V
voltage					1.65 to 1.95	V <sub>CC</sub> × 0.65		$\mathcal{D}$	V <sub>CC</sub> × 0.65		
					2.3 to 2.7	1.7	(-)	>	1.7	_	
					3.0 to 3.6	2.0		_	2.0		
					0.9 <	1F	$\geq$	GND	St )	GND	
					1.1 to 1.3			V <sub>CC</sub> × 0.3	5-0	V <sub>CC</sub> × 0.3	
Low-level input voltage	VIL	_		_	1.4 to 1.6	2		Vcc × 0.35	T)	V <sub>CC</sub> × 0.35	v
vollage				<	1.65 to 1.95			V <sub>CC</sub> × 0.35		V <sub>CC</sub> × 0.35	
				$\bigcirc$	2.3 to 2.7	_		0.7		0.7	
					3.0 to 3.6	1(	$(\forall E)$	0.8		0.8	
				I <sub>OH</sub> =-0.02 mA	0.9	0.75			0.75		
				I <sub>OH</sub> = -0.3 mA	1.1 to 1.3	V <sub>CC</sub> × 0.75	))-		V <sub>CC</sub> × 0.75		
High-level output voltage	V <sub>OH</sub>	_	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	$I_{OH} = -1.7 \text{ mA}$	1.4 to 1.6	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_	v
output voltage				I <sub>OH</sub> = −3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.45	—	_	V <sub>CC</sub> -0.45	_	
		$\frown$	(// 5)	I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0	—	—	2.0	—	
		$\bigcirc$		I <sub>OH</sub> = -8.0 mA	3.0 to 3.6	2.48	—	_	2.48	_	
		$\sqrt{r}$		I <sub>OL</sub> = 0.02 mA	0.9	_	—	0.1		0.1	
			>	1 <sub>OL</sub> = 0.3 mA	1.1 to 1.3	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		V <sub>CC</sub> × 0.25	
Low-level output <sup>&lt;</sup> voltage	Vol	5	$V_{IN} = V_{IL}$	I <sub>OL</sub> = 1.7 mA	1.4 to 1.6	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		V <sub>CC</sub> × 0.25	V
(		$\mathcal{I}$	2	I <sub>OL</sub> = 3.0 mA	1.65 to 1.95	_	—	0.45	—	0.45	
				I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	_	—	0.4	—	0.4	
		$\bigcirc$		I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_	—	0.4	—	0.4	
Input leakage current		$\mathbb{R}$	V <sub>IN</sub> = 0 to	5.5 V	0 to 3.6		_	±0.1		±1.0	μΑ
Power off leakage current	I <sub>OFF</sub>		V <sub>IN</sub> = 0 to V <sub>OUT</sub> = 0		0			1.0		10.0	μA
Quiescent supply current	ICC		$V_{IN} = V_{CC}$	or GND	3.6		_	1.0		10.0	μΑ

#### AC Electrical Characteristics (unless otherwise specified, input $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
			C <sub>L (</sub> pF)	Min	Тур.	Max	Min	Max	
			0.9	_	17.0				ns
			1.1 to 1.3	_	8.8	18.4	1.0	34.2	
		C <sub>L</sub> = 10 pF,	1.4 to 1.6	_	5.0	8.5	1.0	10.0	
		$R_L = 1 M\Omega$	1.65 to 1.95		3.8	6.2	1.0	6.7	
			2.3 to 2.7	$\geq$	2.7	3.9	1.0	4.4	
Propagation delay time			3.0 to 3.6		2.1	3.1	1.0	3.7	
	tplh tphl	C <sub>L</sub> = 15 pF, R <sub>L</sub> = 1 MΩ	0.9	_((	20.7	> -	_	_	
			1.1 to 1.3		10.6	21.5	1.0	37.2	
			1.4 to 1.6	1(	5.9	9.3	1.0	11.2	
			1.65 to 1.95	$\langle \cdot \rangle$	4.5	6.9	1.0	7.1	
			2.3 to 2.7		3.0	4.4	1.0	5.0	
			3.0 to 3.6	2	2.4	3.4	(1,0)	3.9	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		29.6				
			1.1 to 1.3		14.8	29.6	1.0	56.0	
			1.4 to 1.6	_	8.0	43.1	1.0	15.9	
			1.65 to 1.95		6.0	9.2	1.0	9.6	
			2.3 to 2.7		3.9	5.7	1.0	6.1	
			3.0 to 3.6	-	3.0	4.4	1.0	4.8	
Input capacitance	C <sub>IN</sub>	((-))	3.6	$\geq$	3	_	—	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 13)	0.9 to 3.6		6		_	—	pF

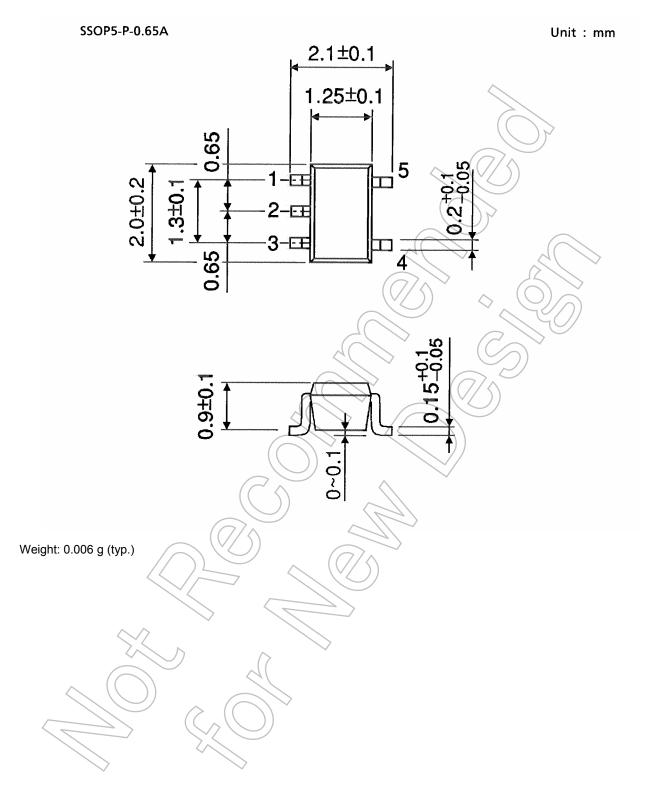
Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

# **TOSHIBA**

# Package Dimensions



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