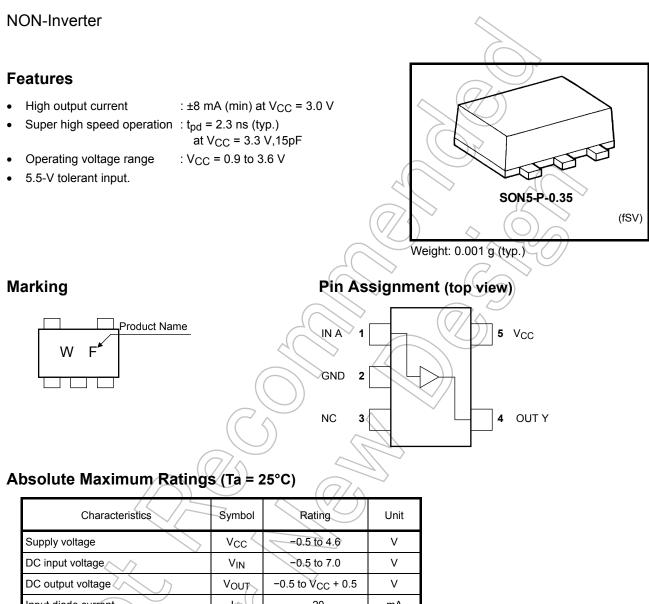
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic





Characteristics	Symbol	reating	Onit
Supply voltage	v _{cc} <	-0.5 to 4.6	V
DC input voltage	VIN	-0.5 to 7.0	V
DC output voltage	VOUT	-0.5 to V _{CC} + 0.5	V
Input diode current	μ κ (-20	mA
Output diode current	Ток	±20 (Note 1)	mA
DC output current		±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

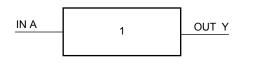
Note 1:
$$V_{OUT} < GND, V_{OUT} > V_{CC}$$

Start of commercial production 2004-11



IEC Logic Symbol

Truth Table



А	Y
L	L
Н	Н

0

erating Ranges			\sim	$\overline{0}$	
Characteristics	Symbol	Rating		Unit	
Supply voltage	V _{CC}	0.9 to 3.6)Ň	
Input voltage	V _{IN}	0 to 5.5	\frown	v	
Output voltage	V _{OUT}	0 to V _{CC}	\sum	V	
		± 8.0	(Note 2)		\leq
		± 4.0	(Note 3)	\Diamond	2m
Output Current	1	±3.0	±3.0 (Note 4)		
Oulpul Current	I _{OH} /I _{OL}	±1.7	(Note 5)	mA	\sim
		± 0.3	(Note 6)		
		± 0.02	(Note 7)	7/5	
Operating temperature	T _{opr}	-40 to 85		⊖°¢	
Input rise and fall time	dt/dv	0 to 10	(Note 8)	ns/V	
Note 2: V _{CC} = 3.0 to 3.6 V			$\geq //$		
Note 3: $V_{CC} = 2.3$ to 2.7 V		\mathcal{I}	\sim		
Note 4: V _{CC} = 1.65 to 1.95 V	$(\bigcirc \bigcirc$	\sim			
Note 5: V _{CC} = 1.4 to 1.6 V			,		
Note 6: $V_{CC} = 1.1$ to 1.3 V	7/\$				
Note 7: V _{CC} = 0.9 V	\bigcirc	$(\overline{\Omega})$			
Note 8: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} =$	3.0 V				
\wedge					
	\bigcirc				
$\langle \rangle \langle \rangle$	(\bigcirc)				

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit		
				0.9	V _{CC}	_	K	V _{CC}	_	
	VIH			1.1 to 1.3	V _{CC} × 0.7	_		V _{CC} × 0.7	_	
High-level input voltage			_	1.4 to 1.6	V _{CC} × 0.65	-((7/5	V _{CC} × 0.65	—	v
voltage				1.65 to 1.95	V _{CC} × 0.65		\bigcirc	V _{CC} × 0.65	_	
				2.3 to 2.7	1.7	(-)		1.7	—	
				3.0 to 3.6	2.0	\sum		2.0	_	
				0.9	4	\searrow	GND	A	GND	V
				1.1 to 1.3	775	>	V _{CC} × 0.3	57	V _{CC} × 0.3	
Low-level input voltage	V _{IL}			1.4 to 1.6	\mathcal{O}	_	V _{CC} × 0.35	FD) V _{CC} × 0.35	
voitage				1.65 to 1.95	<u> </u>	- (V _{CC} × 0.35	>_	$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$	
				2.3 to 2.7	_	\square	0.7	—	0.7	
				3.0 to 3.6	_		0.8		0.8	
			I _{OH} =-0.02 mA	0.9	0.75	$\langle - \rangle$		0.75	—	
High-level output voltage	V _{OH}	VIN = VIH	I _{OH} = -0.3 mA	1.1 to 1.3	V _{CC} × 0.75	$) \rightarrow$	_	V _{CC} × 0.75	_	
			10H = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	×	_	V _{CC} × 0.75	_	V
			1 _{OH} = -3.0 mA	1.65 to 1.95	Vcc -0,45	_	_	V _{CC} -0.45	_	
			I _{OH} = -4.0 mA	2.3 to 2.7	2.0			2.0		
/			1 _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	_	2.48	_	
	\leq		I _{OL} = 0.02 mA	0.9	_	_	0.1	—	0.1	
		\triangleright	I _{OL} = 0.3 mA	1.1 to 1.3		_	V _{CC} × 0.25	—	V _{CC} × 0.25	
Low-level output voltage	V _{OL} V _{IN} = V _{IL}	$V_{IN} = V_{IL}$	I _{OL} = 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	V
	\sim		J _{OL} = 3.0 mA	1.65 to 1.95	_		0.45		0.45	
			I _{OL} = 4.0 mA	2.3 to 2.7	_		0.4		0.4	
		> (C	I _{OL} = 8.0 mA	3.0 to 3.6	_		0.4		0.4	
Input leakage current	IIN	$V_{\rm IN} \neq 0$ to 5.5V		0 to 3.6	_		±0.1		±1.0	μA
Quiescent supply current	ICC	VIN = VCC	$V_{IN} = V_{CC}$ or GND		_	—	1.0	_	10.0	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		$Ta = 25^{\circ}C \qquad Ta = -40 \text{ to } 85^{\circ}C$			Unit		
	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		18.6		—	—	
			1.1 to 1.3	_	8.7	18.4	1.0	34.2	
			1.4 to 1.6	_	4.9	8.5	1.0	10.0	ns
			1.65 to 1.95		3.8	6.2	1.0	6.7	
			2.3 to 2.7		2.6	3.9	1.0	4.4	
			3.0 to 3.6	- <	2.1	3.1	1.0	3.7	
Dropogation delay time	t _{pLH} t _{pHL}		0.9	_	21.0)	—	—	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.1 to 1.3		9.8	21.5	1.0	37.1	
			1.4 to 1.6	7	5.4	9.3	1.0	11.2	
Propagation delay time			1.65 to 1.95	Æ	4,2	6.9	1.0	7,1	
			2.3 to 2.7		2.8	4.4	2 1.0	5.0	
			3.0 to 3.6	//	2.3	3.4)1.0	3.9	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		31.2		~~~/)	/ _	
			1.1 to 1.3	\geq –	13.8	29.6	1.0	56.0	
			1.4 to 1.6	_	7.4	13.1)	1.0	15.9	
			1.65 to 1.95	_	5.6	9.2	1.0	9.6	
			2.3 to 2.7		3.7))5.7	1.0	6.1	
		$\mathcal{A}(\mathcal{A})$	3.0 to 3.6	(-	2.9	4.4	1.0	4.8	
Input capacitance	C _{IN}		3.6	$\overline{\langle}$	3	—	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 9)	0.9 to 3.6		6	_	_	—	pF

Note 9: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

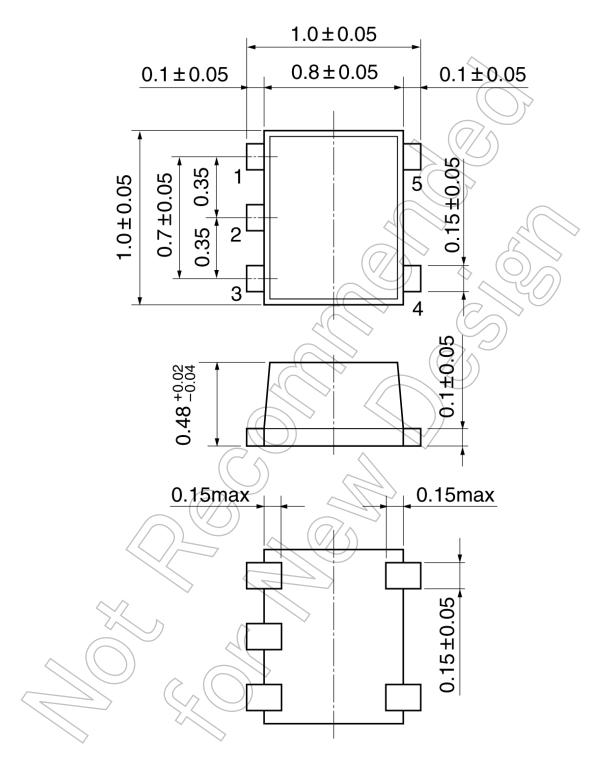
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.35

Unit: mm



Weight: 0.001 g (typ.)

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