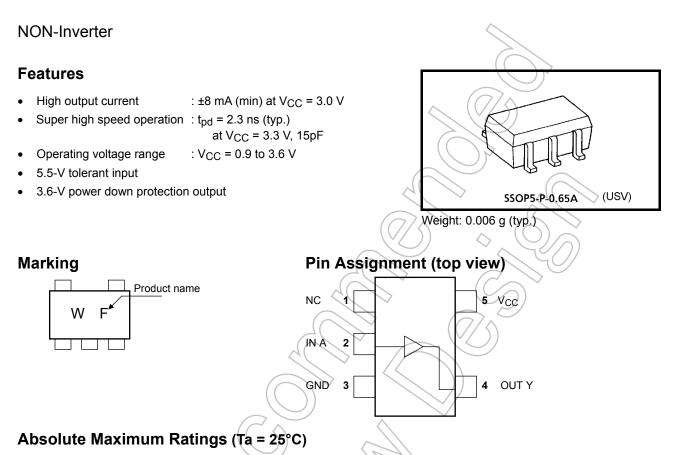
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG34FU



Characteristics	Symbol	Rating	Unit		
Supply voltage	Vcc	-0.5 to 4.6	V		
DC input voltage	\supset VIN	-0.5 to 7.0	V		
DC output voltage	Vau	-0.5 to 4.6 (Note 1)	V		
	Vout	-0.5 to V _{CC} +0.5 (Note 2)			
Input diode current	IIK ~	-20	mA		
Output diode current	loк	-20 (Note 3)	mA		
DC output current	lout	±25	mA		
DC V _{CC} /ground current	Icc	±50	mA		
Power dissipation	PD	200	mW		
Storage temperature	T _{stg}	-65 to 150	°C		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

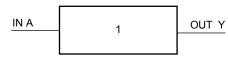
Note 1: $V_{CC}=0V$

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings. Note 3: V_{OUT} <GND

Start of commercial production 2005-02

TOSHIBA

IEC Logic Symbol



Truth Table

A	Y
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating
Supply voltage	V _{CC}	0.9 to 3.6
Input voltage	V _{IN}	0 to 5.5
Output voltage	Vout	0 to 3.6 (Note 4) V
	V001	0 to V _{CC} (Note 5)
		± 8.0 (Note 6)
		± 4.0 (Note 7)
Output Current	I _{OH} /I _{OL}	± 3.0 (Note 8) mA
	OTFOL	± 1.7 (Note 9)
		± 0.3 (Note 10)
		± 0.02 (Note 11)
Operating temperature	T _{opr}	-40 to 85 °C
Input rise and fall time	dt/dv <	0 to 10 (Note 12) ns/V
Note 4: V _{CC} = 0V	((
Note 5: High or Low state.		$)$ \sim
Note 6: V _{CC} = 3.0 to 3.6 V	$(C \land$	
Note 7: $V_{CC} = 2.3$ to 2.7 V		
Note 8: $V_{CC} = 1.65$ to 1.95 V	7/5	
Note 9: $V_{CC} = 1.4$ to 1.6 V	\bigcirc	
Note 10: V _{CC} = 1.1 to 1.3 V	7 4	
Note 11: V _{CC} = 0.9 V		
Note 12: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3$	3.0 V 🧹	
$\langle \rangle / \gamma$		

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Toot Condition			Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
Gharactenstics Syntbol		Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
			0.9	V _{CC}	—	\mathcal{I}	V _{CC}	_		
High-level input VIH				1.1 to 1.3	V _{CC} × 0.7	_	Ĥ	Vcc × 0.7	_	V
				1.4 to 1.6	V _{CC} × 0.65	-((V _{CC} × 0.65		
voluge	onage			1.65 to 1.95	V _{CC} × 0.65		\sum	V _{CC} × 0.65		
			2.3 to 2.7	1.7	(-)	2-	1.7	—	l	
				3.0 to 3.6	2.0		_	2.0	-	
					4	\rightarrow	GND	A)	GND	
				1.1 to 1.3	775	>	V _{CC} × 0.3	37	V _{CC} × 0.3	
Low-level input VIL VIL				\mathbb{D}	_	V _{CC} × 0.35	T D) V _{CC} × 0.35	V	
				×	- (V _{CC} × 0.35	>_	V _{CC} × 0.35		
			G	2.3 to 2.7	_	\square	0.7		0.7	
				3.0 to 3.6	-(0.8		0.8	
			I _{OH} =-0.02 mA	0.9	0.75	$\langle - \rangle$	_	0.75	—	
			I _{OH} = -0.3 mA	1.1 to 1.3	V _{CC} ×0.75	$) \rightarrow$		V _{CC} × 0.75	_	
High-level output	V _{ОН}	V _{IN} = V _{IH}	1 _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	~		$\begin{array}{c} V_{CC} \\ \times \ 0.75 \end{array}$		V
voltage	-		I _{OH} = -3.0 mA	1.65 to 1.95	Vcc -0.45	_	_	V _{CC} -0.45		
			I _{OH} = -4.0 mA	2.3 to 2.7	2.0	_	_	2.0	_	
			1 _{OH} = -8.0 mA	3.0 to 3.6	2.48	—		2.48		
	\leq		I _{OL} = 0.02 mA	0.9		_	0.1	_	0.1	
		\triangleright	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3		_	V _{CC} × 0.25		V _{CC} × 0.25	
Low-level output	Vol	$V_{IN} = V_{IL}$	I _{OL} = 1.7 mA	1.4 to 1.6			$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	V
		J _{OL} = 3.0 mA			—	0.45	_	0.45		
			I _{OL} = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	
		> (C	I _{OL} = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current	IIN	V _{IN} = 0 to	5.5 V	0 to 3.6			±0.1		±1.0	μA
Power off leakage current	I _{OFF}	V _{IN} = 0 to 5.5 V V _{OUT} = 0 to 3.6 V		0	_		1.0		10.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$ or GND		3.6	_	_	1.0	_	10.0	μΑ

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		$Ta = -40$ to $85^{\circ}C$		l la it	
Characteristics Syn	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	tрLH tpHL	$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	18.6	_	_	—	
			1.1 to 1.3		8.7	18.4	1.0	34.2	
			1.4 to 1.6		4.9	8.5	1.0	10.0	
			1.65 to 1.95	_	3.8	6.2	1.0	6.7	
			2.3 to 2.7	_	2.6	3.9	1.0	4.4	
			3.0 to 3.6	- <	2.1	3.1	1.0	3.7	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	21.0	\mathcal{T}		—	- ns
			1.1 to 1.3	_	9.8	21.5	1.0	37.1	
			1.4 to 1.6	76	5.4	9.3	1.0	11.2	
			1.65 to 1.95	A	4,2	6.9	1.0	7,1	
			2.3 to 2.7		2.8	4.4	2 1.0	5.0	
			3.0 to 3.6	//-5)	2.3	3.4	1.0	3.9	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		31.2		~~~)	/ _	
			1.1 to 1.3	\geq –	13.8	29.6	1.0	56.0	
			1.4 to 1.6	_	7.4	13.1)	1.0	15.9	-
			1.65 to 1.95	_	5.6	9.2	1.0	9.6	
			2.3 to 2.7		3.7))5.7	1.0	6.1	
			3.0 to 3.6		2.9	4.4	1.0	4.8	
Input capacitance	C _{IN}		3.6	\times	3	—	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 to 3.6		6	_		—	pF

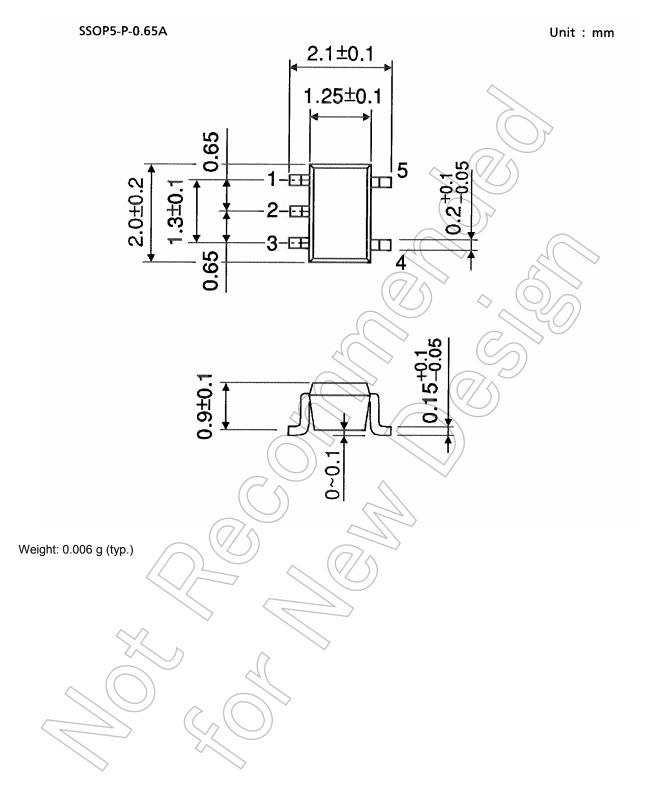
Note 13: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions



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