TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SG86FU

## 2-Input EXCLUSIVE OR Gate

#### **Features**

• High output current : ±8 mA (min) at V<sub>CC</sub> = 3 V

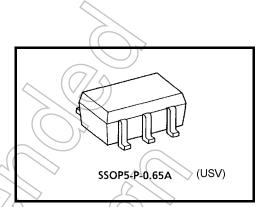
Super high speed operation : t<sub>pd</sub> = 2.6 ns (typ.)

at V<sub>CC</sub> = 3.3 V, 15pF

Operating voltage range : V<sub>CC</sub> = 0.9 to 3.6 V

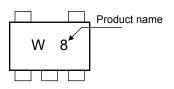
• 5.5-V tolerant inputs.

• 3.6-V power down protection output.

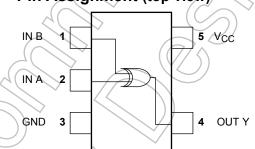


Weight: 0.006 g (typ.)

### Marking



## Pin Assignment (top view)



# Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	-0.5 to 4.6	V
DC input voltage	VIN	-0.5 to 7.0	V
DC output voltage	V <sub>OUT</sub>	-0.5 to 4.6 (Note 1)	V
OC output voitage		-0.5 to V <sub>CC</sub> + 0.5 (Note 2)	V
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	IOK	-20 (Note 3)	mA
DC output current	IOUT	±25	mA
DC V <sub>CC</sub> /ground current	(Icc)	±50	mA
Power dissipation	PD	200	mW
Storage temperature	T <sub>stg</sub>	−65 to 150	°C

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V<sub>CC</sub> = 0V

Note 2: High or Low state. Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

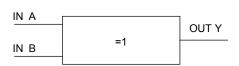
Start of commercial production

Note 3: V<sub>OUT</sub> < GND

2005-02

## **IEC Logic Symbol**

### **Truth Table**



Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

## **Operating Ranges**

Characteristics	Symbol	Rating
Supply voltage	V <sub>CC</sub>	0.9 to 3.6 V
Input voltage	V <sub>IN</sub>	0 to 5.5
Output voltage	V <sub>OUT</sub>	0 to 3,6 (Note 4)
	VOU1	0 to V <sub>CC</sub> (Note 5)
	I <sub>OH</sub> /I <sub>OL</sub>	± 8.0 (Note 6)
		±4.0 (Note 7)
Output Current		± 3.0 (Note 8) mA
Output Current		± 1.7 (Note 9)
		± 0.3 (Note 10)
		± 0.02 (Note 11)
Operating temperature	T <sub>opr</sub>	-40 to 85 °C
Input rise and fall time	dt/dv	0 to 10 (Note 12) ns/V

Note 4:  $V_{CC} = 0V$ 

Note 5: High or Low state.

Note 6:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 7:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ 

Note 8:  $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$ 

Note 9:  $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$ 

Note 10:  $V_{CC} = 1.1$  to 1.3 V

Note 11:  $V_{CC} = 0.9 V$ 

Note 12:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V

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## **Electrical Characteristics**

### **DC Characteristics**

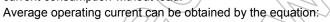
Characteristics Symbol Test Condition				Ta = 25°C			Ta = -40 to 85°C		Unit	
Sharacteristics Symbol		Test Condition V <sub>C</sub>		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
			0.9	V <sub>CC</sub>	_	4	V <sub>CC</sub>			
				1.1 to 1.3	V <sub>CC</sub> × 0.7			V <sub>CC</sub> × 0.7		
High-level input VIH	V <sub>IH</sub>	_		1.4 to 1.6	V <sub>CC</sub> × 0.65	-(	7/4	V <sub>CC</sub> × 0.65		٧
Voltage					V <sub>CC</sub> × 0.65			V <sub>CC</sub> × 0.65		
				2.3 to 2.7	1.7	(-)	> -	1.7	_	
				3.0 to 3.6	2.0		_	2.0	_	
				0.9	4	$\rightarrow$	GND	H.	GND	
				1.1 to 1.3	775	>	V <sub>CC</sub> × 0.3	3	V <sub>CC</sub> × 0.3	V
Low-level input voltage	V <sub>IL</sub>		_	1.4 to 1.6		_	V <sub>CC</sub> × 0.35	H	V <sub>CC</sub> × 0.35	
Voltage					_	- (	V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35	
				2.3 to 2.7	_		0.7		0.7	
				3.0 to 3.6	_	\ <del>\</del> \	0.8		8.0	
			$I_{OH} = -0.02 \text{ mA}$	0.9	0.75	1	_	0.75	_	
			$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V <sub>CC</sub>	)	_	V <sub>CC</sub> × 0.75	_	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75		V
Voltage			I <sub>OH</sub> = -3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0,45	_	_	V <sub>CC</sub> -0.45	_	
			$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0	_	_	2.0	_	
			$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48	_	_	2.48	_	
			$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
		$\triangleright$	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
Low-level output voltage	VOL	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 1.7 mA	1.4 to 1.6	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	V
			$I_{OL} = 3.0 \text{ mA}$	1.65 to 1.95			0.45		0.45	
	))		$I_{OL} = 4.0 \text{ mA}$	2.3 to2.7	_	_	0.4	_	0.4	
		> ((	I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V		0 to 3.6	_	_	±0.1	_	±1.0	μА
Power off leakage current	l <sub>OFF</sub>	V <sub>IN</sub> = 0 to 5.5 V V <sub>OUT</sub> = 0 to 3.6 V		0	_	_	1.0		10.0	μА
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub>	or GND	3.6	_	_	1.0	_	10.0	μΑ

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## AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
Ondractoristics Oynibo		rest Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	23.0	_	_	_	ns
			1.1 to 1.3	_	11.7	20.9	1.0	39.1	
			1.4 to 1.6	_	6.7	10.0	1.0	11.8	
			1.65 to 1.95	_	5.1	6.6	1.0	7.6	
			2.3 to 2.7	_	3.4	4.1	1.0	4.7	
			3.0 to 3.6	- ^	2.7	3.3	1.0	3.9	
		$C_L$ = 15 pF, $R_L$ = 1 M $\Omega$	0.9	_ `	23.7	$\mathcal{L}$	—	_	
	tpLH tpHL		1.1 to 1.3	_	11.9	22.8	1.0	39.4	
Propagation delay time			1.4 to 1.6	7	6.7	9.9	1.0	11.9	
Tropagation delay time			1.65 to 1.95	4	5,1	7.3	1.0	7,5	
			2.3 to 2.7	2	3.4	4.7	2 1.0	5.3	
			3.0 to 3.6	//-5)	2.7	3.6	).0	4.1	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		32.1	$\triangle$	K()	/ _	
			1.1 to 1.3	<i>&gt;</i> −	15.7	31.4	1.0	59.4	
			1.4 to 1.6	_	8.7	13.9	1.0	16.9	
			1.65 to 1.95	_	6.5	9.8	1.0	10.2	
			2.3 to 2.7		4.2	))6.0	1.0	6.5	
			3.0 to 3.6		3.4	4.7	1.0	5.1	
Input capacitance	C <sub>IN</sub>		3.6	/	) 3	_	_	_	pF
Power dissipation capacitance	$C_{PD}$	(Note 13)	0.9 to 3.6	_//	//9	_	_	_	pF

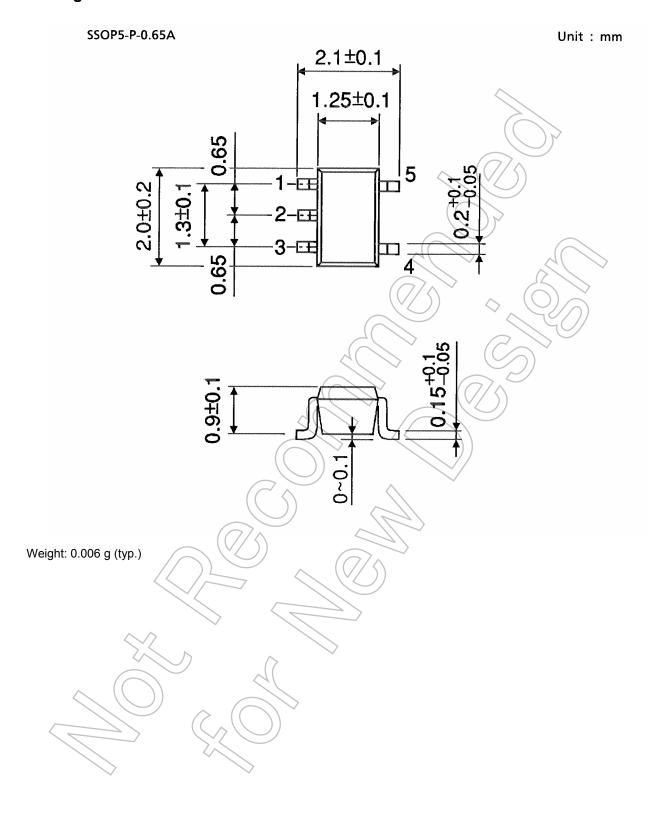
Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.



ICC (opr.) = CPD·VCC·fIN + ICC



## **Package Dimensions**



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