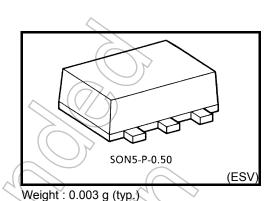
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH05FE

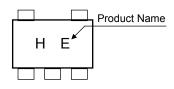
INVERTER (Open Drain)

Features

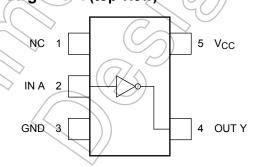
- High speed: t_{pd} = 3.7 ns (typ.) at V_{CC} = 5 V, 15 pF
- Low power dissipation: I_{CC} = 2 μA (max) at Ta = 25°C
- Wide operating voltage range: V_{CC} = 2 to 5.5 V
- 5.5-V tolerant input
- 5.5-V power down protection output



Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	- 0.5 to 7	V
DC input voltage	\supset V_{IN}	-0.5 to 7	V
DC output voltage	Vout	-0.5 to 7 (Note 1)	٧
Input diode current	l _{IK}	- 20	mA
Output diode current	lok	20 (Note 2)	mA
DC output current	lout	25	mA
DC V _{CC} /ground current	Icc	± 50	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	- 65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

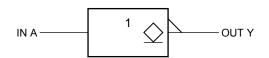
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Do not exceed IOUT of absolute maximum ratings.

Note 2: V_{OUT} < GND

Start of commercial production 2008-02

IEC Logic Symbol



Truth Table

Α	Υ
L	Z
Н	L

Z: High impedance

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 5.5	/ v
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to 5.5	V
Operating temperature	T _{opr}	- 40 to 85	°C (
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 \pm 0.3 V)	ns/V
input rise and fail time	dvav	0 to 20 (V _{CC} = 5.0 ± 0.5 V)	(IIS/V)

Electrical Characteristics

DC Characteristics

Characteristics	Cumbal	Toot C	andition		Ta = 25°C			Ta = -40	Unit		
Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Onit	
High-level		_		2.0	1.5	_	- <	1.5			
input voltage	V _{IH}			3.0 to 5.5	V _{CC} × 0.7			V _{CC} × 0.7		V	
Low-level							0.5		0.5		
input voltage V _{IL} -		_	3.0 to 5.5		/	VCC × 0.3	/	V _{CC} × 0.3	٧		
	V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 50 μA	2.0	_	0	0.1		0.1		
				3.0	_	0	0.)	_	0.1	٧	
Low-level output voltage				4.5	_	0	0.1	_	0.1		
			I _{OL} = 4 mA	3.0	_ <	<u></u>	0.36	- <	0.44		
			$I_{OL} = 8 \text{ mA}$	4.5	6	> <u>\</u>	0.36	78	0.44		
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	W))_	±0.1		±1.0	μΑ	
Output Z level leakage current	I _{LKG}	$V_{IN} = V_{IL}$ $V_{OUT} = 0$ to 5.5V		0 to 5.5	//		±0.25	76	±2.5	μΑ	
Power-off leakage current	loff	V _{IN} = 5.5V or V _{OUT} = 0 to 5.5V		0.0	> —		1.0		10.0	μА	
Quiescent supply current	Icc	$V_{IN} = V_{CC}$	or GND	5.5	7/		2.0		20	μΑ	

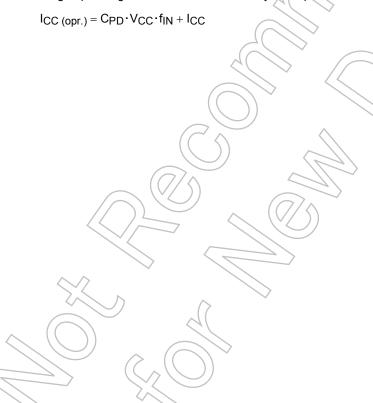


AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics	Syllibol		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	^t pZL	$R_L = 1k\Omega$	3.3 ± 0.3	15	_	5.5	7.9	1.0	9.5	ns
				50	_	8.0	11.4	1.0	13.0	
			5.0 ± 0.5	15	_	3.7	5.5	1.0	6.5	
				50	_	5.2	7.5	1.0	8.5	
	t _{pLZ} R	$R_L = 1k\Omega$	3.3 ± 0.3	15	_	5.5	7.9	1.0	9.5	
				50	_<	8.0	11.4	1.0	13.0	
			5.0 ± 0.5	15		3.7	5.5	1.0	6.5	
			3.0 ± 0.3	50	-(5.2	7.5	1.0	8.5	
Input capacitance	C _{IN}		_			4	10		10	pF
Output capacitance	C _{OUT}		_	/		6		£	_	pF
Power dissipation capacitance	C _{PD}			(Note3)		14	- (\	pF

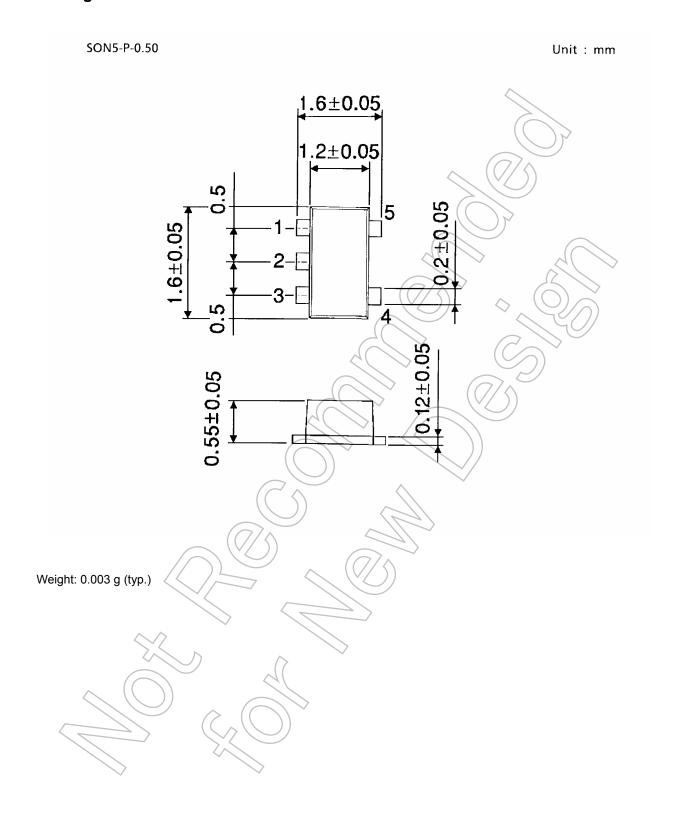
Note 3: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:



4 2014-03-01

Package Dimensions



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