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TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ126AFE

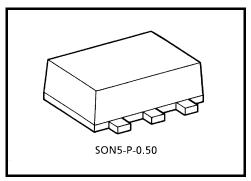
Bus Buffer with 3-STATE Output

Features

- High output drive: ±24 mA (min) at V_{CC} = 3 V
- Super high speed operation: t_{pd} = 2.6 ns (typ.)

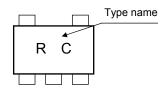
at $V_{CC} = 5 \text{ V}, 50 \text{ pF}$

- Operation voltage range: V_{CC (opr)} = 1.8~5.5 V
- 5.5-V tolerant inputs
- \bullet Matches the performance of TC74LCX series when operated at 3.3-V V_{CC}

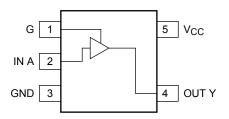


Weight: 0.003 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5~6	V
DC input voltage	V _{IN}	-0.5~6	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	l _{IK}	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	P _D	150	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10s)	TL	260	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



www.DataSheet4LLcom **Truth Table**

A	G	Y
Х	L	Z
L	Н	L
Н	Н	Н

X : Don't Care Z : High Impedance

Logic Diagram



Operating Ranges

Characteristics	Symbol	Rating	Unit		
Supply voltage	Voc	1.8~5.5	V		
Supply voltage	V _{CC}	1.5~5.5 (Note1)	V		
Input voltage	V _{IN}	0~5.5	V		
Output voltage	V _{OUT}	0~V _{CC}	V		
Operating temperature	T _{opr}	-40~85	°C		
		$0~20~(V_{CC} = 1.8~V, 2.5~V \pm 0.2~V)$	ns/V		
Input rise and fall time	dt/dv	$0 \sim 10 \; (V_{CC} = 3.3 \; V \pm 0.3 \; V)$			
		$0~5~(V_{CC} = 5.5~V \pm 0.5~V)$			

Note1: Data retention only



www.DataSheet4JJ.com Electrical Characteristics

DC Characteristics

Characteristics		Symbol	Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
Characteris	aucs	Symbol Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic	
	.,			1.8	0.75 × V _{CC}		_	0.75 × V _{CC}	_		
Input voltage	High level	V _{IH}		_		0.7 × V _{CC}	_	_	0.7 × V _{CC}	_	
Input voltage			_		1.8	_		0.25 × V _{CC}	_	0.25 × V _{CC}	· V
	Low level	V _{IL}			2.3~5.5	_	_	0.3 × V _{CC}	_	0.3 × V _{CC}	
					1.8	1.7	1.8	_	1.7	_	
				lou = 100 uA	2.3	2.2	2.3	_	2.2	_	
				$I_{OH} = -100 \mu A$	3.0	2.9	3.0	_	2.9	_	
High level	V _{OH}	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		4.5	4.4	4.5	_	4.4	_		
	i ligii level	VOH	V _{IN} = V _{IH}	$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9	_	
				I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	_	
Output voltage			I _{OH} = -24 mA	3.0	2.3	2.68	_	2.3	_		
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	V	
Output voltage	put voitage			1.8	_	0	0.1	_	0.1] v	
		l VoL	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	2.3	_	0	0.1		0.1	
					3.0	_	0	0.1	_	0.1	
	Low level				4.5	_	0	0.1		0.1	
Low level	LOW ICVCI			$I_{OL} = 8 \text{ mA}$	2.3	_	0.1	0.3		0.3]
				I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55	
				$I_{OL} = 32 \text{ mA}$	4.5	_	0.22	0.55	_	0.55	
Input leakage curre	ent	I _{IN}	$V_{IN} = 5.5$	v or GND	0~5.5	_	_	±1	_	±10	μΑ
3-state output off-si	tate current	loz	$V_{IN} = V_{IH} \text{ or } V_{IL},$ $V_{OUT} = 0 \sim 5.5 \text{ V}$		1.8~5.5	_	_	±1	_	±10	μΑ
Quiescent supply c	urrent	Icc	V _{IN} = 5.5 V or GND		5.5	_	_	2	_	20	μΑ



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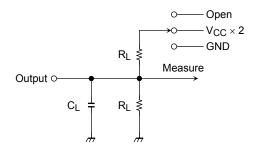
Characteristics Symbol		Test Condition V _{CC} (V)		Ta = 25°C			Ta = -40~85°C		Unit
				Min	Тур.	Max	Min	Max	Offic
			1.8	2.0	5.3	11.0	2.0	11.5	
		$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	2.5 ± 0.2	0.8	3.4	7.5	0.8	8.0	
Propagation delay time	t _{pLH}		3.3 ± 0.3	0.5	2.5	5.2	0.5	5.5	
Tropagation delay time	t _{pHL}		5.0 ± 0.5	0.5	2.1	4.5	0.5	4.8	
		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	3.3 ± 0.3	1.5	3.2	5.7	1.5	6.0	
			5.0 ± 0.5	0.8	2.6	5.0	0.8	5.3	
Output enable time t _{pZH}		t_{pZH} t_{pZL} $C_L = 50 \text{ pF, } R_L = 500 \Omega$	1.8	2.0	6.5	11.5	2.0	12.0	ns
	t _{pZH}		2.5 ± 0.2	1.5	3.8	8.0	1.5	8.5	110
	t _{pZL}		3.3 ± 0.3	1.5	3.2	5.7	1.5	6.0	
			5.0 ± 0.5	8.0	2.3	5.0	0.8	5.3	
		$\begin{array}{c} t_{pLZ} \\ t_{pHZ} \end{array} C_L = 50 \; pF, \; R_L = 500 \; \Omega \label{eq:classical_phz}$	1.8	2.0	5.6	11.0	2.0	12.0	
Output disable time	t _{pLZ}		2.5 ± 0.2	1.0	4.0	8.0	1.5	8.5	
	t _{pHZ}		3.3 ± 0.3	1.0	3.5	5.7	1.0	6.0	
			5.0 ± 0.5	0.5	2.7	4.7	0.5	5.0	
Input capacitance	C _{IN}	_	0~5.5	_	4	_	_	_	pF
Power dissipation	C _{PD}	(Note2)	3.3	_	20	_	_	_	pF
capacitance	OPD	(140162)	5.5	_	27	_	_	_	pΓ

Note2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

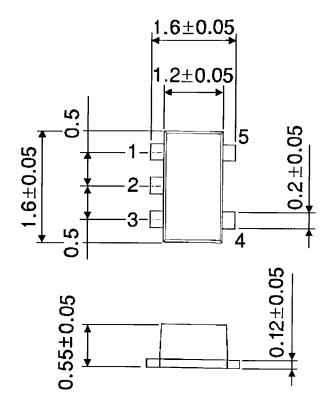
AC Characteristics Measurement Circuit



Characteristics	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	V _{CC} × 2
t _{pHZ} , t _{pZH}	GND

www.DataSheet4U.com Package Dimensions

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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20070701-EN GENERAL

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