TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WG02FC

Dual 2-Input NOR Gate

Features

High output current : ±8 mA (min) at V_{CC} = 3 V

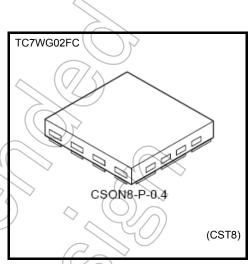
• Super high speed operation: tpd = 2.8 ns (typ.)

at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

• Operating voltage range : V_{CC} = 0.9 to 3.6 V

• 5.5-V tolerant inputs

• 3.6-V power down protection outputs



Weight: 0.002 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

		71	
Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	-0.5 to 4.6	\\/
DC input voltage	V _{IN}	-0.5 to 7.0	/>
DC output voltage	V0.17	-0.5 to 4.6 (Note1)	\ \
DC output voltage	Vout	-0.5 to V _{CC} +0.5 (Note2)	
Input diode current	l _{IK}	-20	mA
Output diode current	lok (-20 (Note3)	mA
DC output current	lout	±25	mA
DC V _{CC} /GND current	Icc	2±50 (±50)	mA
Power dissipation	P_{D}	150 (Note4)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High or Low State.

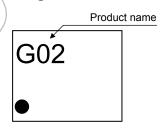
Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: VOUT < GND

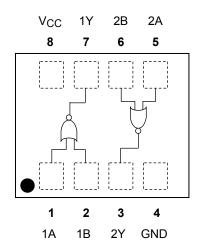
Note 4: Mounted on an FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 11.56 \text{ mm}^2)$

Marking



Pin Assignment (top view)



Start of commercial production 2006-04

IEC Logic Symbol

Truth Table



Α	В	Υ
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	0.9 to 3.6	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to 3.6 (Note 5)	V
	VOU1	0 to V _{CC} (Note 6)	(
	I _{OH} /I _{OL}	±8.0 (Note 7)	
		± 4.0 (Note 8)	
Output current		± 3.0 (Note 9)	mA
Output current		± 1.7 (Note 10)	
		± 0.3 (Note 11)	2)
		± 0.02 (Note 12)	
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 13)	ns/V

Note 5: $V_{CC} = 0V$

Note 6: High or Low state.

Note 7: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 8: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 9: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 10: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 11: $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$

Note 12: $V_{CC} = 0.9 V$

Note 13: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

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Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics	Symbol	rest condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
				0.9	V _{CC}	_	4	V _{CC}		
				1.1 to 1.3	V _{CC} × 0.7	_		V _{CC} × 0.7	_	
High-level input VIH			_		V _{CC} × 0.65	-(7/6	V _{CC} × 0.65	_	V
				1.65 to 1.95	V _{CC} × 0.65			V _{CC} × 0.65	_	
				2.3 to 2.7	1.7	(-)	>-	1.7	_	
				3.0 to 3.6	2.0		_	2.0	_	
				0.9	9	\searrow	GND	4	GND	
				1.1 to 1.3	775)	>	V _{CC} × 0.3	5	V _{CC} × 0.3	
Low-level input voltage	V _{IL}		_	1.4 to 1.6		_ `	V _{CC} × 0.35	H	V _{CC} × 0.35	٧
. Chage				1.65 to 1.95	_	- (V _{CC} × 0.35	_	V _{CC} × 0.35	
				2.3 to 2.7	_		0.7		0.7	
				3.0 to 3.6	_	\ \ \	0.8		0.8	
			I _{OH} =-0.02 mA	0.9	0.75	1/-	_	0.75	_	
			$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V _{CC} × 0.75)	_	V _{CC} × 0.75	_	
High-level output voltage	V _{OH}	V _{IN} = V _{IL} /	I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75		V
Voltage			I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0,45	_	_	V _{CC} -0.45		
			$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0	_	_	2.0	1	
			$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48	_	_	2.48	_	
			$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
		\rightarrow	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
Low-level output voltage	Vol	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	٧
			I _{OL} = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	
))		$I_{OL} = 4.0 \text{ mA}$	2.3 to 2.7	_	_	0.4	_	0.4	
		> ((I _{OL} = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current	I _{IN}	$V_{IN} = 0$ to	5.5 V	0 to 3.6	_	_	±0.1	_	±1.0	μΑ
Power off leakage current	l _{OFF}	V _{IN} = 0 to 5.5 V V _{OUT} = 0 to 3.6 V		0	_	_	1.0	_	10.0	μΑ
Quiescent supply current	Icc	$V_{IN} = V_{CC}$	or GND	3.6	_	_	1.0	_	10.0	μΑ

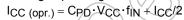
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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	Ta = 25°C		;	Ta = -40 to 85°C		Unit	
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Onit
		C_L = 10 pF, R_L = 1 M Ω	0.9	_	19.8	_	_	_	ns
			1.1 to 1.3	_	10.1	18.7	1.0	34.5	
			1.4 to 1.6	_	5.9	8.9	1.0	10.8	
			1.65 to 1.95	1	4.5	6.4	1.0	6.9	
			2.3 to 2.7	1	3.1	4.2	1.0	4.7	
			3.0 to 3.6	- <	2.3	3.4	1.0	4.0	
Propagation delay time	t _{pLH} t _{pHL}	$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	22.5		_		
			1.1 to 1.3	_	11.6	21.5	1.0	37.2	
			1.4 to 1.6	70	6.6	9.8	1.0	12.0	
			1.65 to 1.95	4	5.0	7.1	1.0	7.3	
			2.3 to 2.7		3.5	4.5	2 1.0	5.1	
			3.0 to 3.6	//-\)	2.8	3.8).0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9))	30.0		~(/ /)	_	
			1.1 to 1.3	\rightarrow	15.0	29.6	1.0	56.0	
			1.4 to 1.6	· —	8.5	(13.1)	1.0	15.9	
			1.65 to 1.95	_	6.3	9.2	1.0	9.6	
			2.3 to 2.7		4.3	5.7	1.0	6.1	
			3.0 to 3.6		3.5	4.4	1.0	4.8	
Input capacitance	C _{IN}	F	3.6	1	3	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note 14)	0.9 to 3.6	_	\ /11	_	_	_	pF

Note 14: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

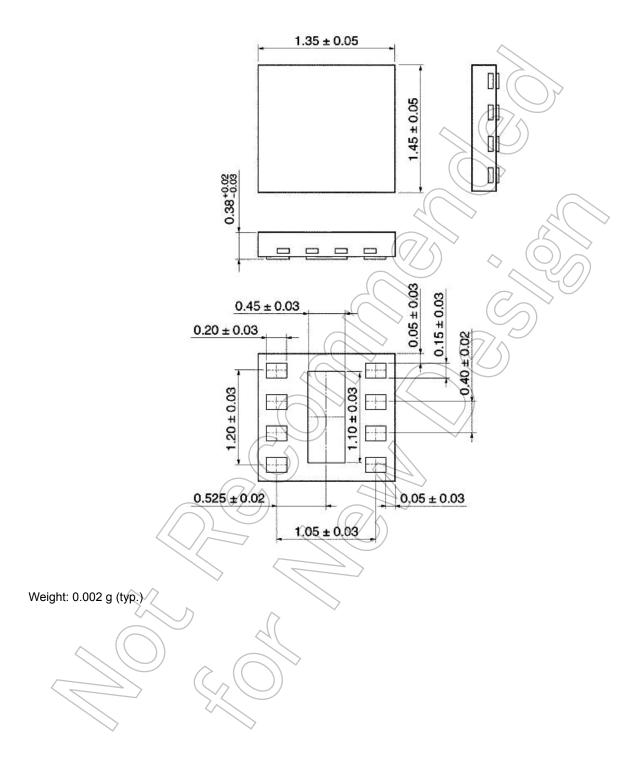
Average operating current can be obtained by the equation:





Package Dimensions

CSON8-P-0.4 Unit: mm



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