TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WG14FU, TC7WG14FK

Triple Schmitt Inverter

### **Features**

• High output current : ±8 mA (min) at V<sub>CC</sub> = 3 V

• Super high speed operation: tpd = 4.0 ns (typ.)

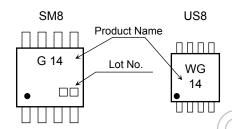
at  $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$ 

• Operating voltage range : V<sub>CC</sub> = 0.9 to 3.6 V

• 5.5-V tolerant inputs

• 3.6-V power down protection outputs

### Marking



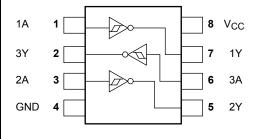
## Absolute Maximum Ratings (Ta = 25°C)

			///	
Characteristics	Symbol	Rating	Unit	
Supply voltage	Vcc	-0.5 to 4.6	<i>(</i> *)	
DC input voltage	VIN	-0.5 to 7.0	\ \ \	
DC output voltage	Vout	-0.5 to 4.6 (Note1)	/ v	
		-0.5 to V <sub>CC</sub> +0.5 (Note2)		
Input diode current	I <sub>IK</sub>	-20	mA	
Output diode current	Іок	-20 (Note3)	mA	
DC output current	OUT	±25	mA	
DC V <sub>CC</sub> /GND current	Icc	±50	mA	
Davier dissipation		300 (SM8)	\^/	
Power dissipation	Po	200 (US8)	mW	
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	

# TC7WG14FU SSOP8-P-0.65 (SM8) TC7WG14FK SSOP8-P-0.50A (US8)

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

### Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V<sub>CC</sub> = 0 V

Note 2: High or Low State. Do not exceed IOUT of absolute maximum ratings.

Start of commercial production 2006-02

Note 3: V<sub>OUT</sub> < GND

# **IEC Logic Symbol**



### **Truth Table**

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# **Operating Ranges**

Characteristics	Symbol	Rating
Supply voltage	V <sub>CC</sub>	0.9 to 3.6
Input voltage	V <sub>IN</sub>	0 to 5.5
Output voltage	V <sub>OUT</sub>	0 to 3.6 (Note 4)
	VOUI	0 to V <sub>CC</sub> (Note 5)
Output current	I <sub>OH</sub> /I <sub>OL</sub>	± 8.0 (Note 6)
		± 4.0 (Note 7)
		± 3.0 (Note 8) mA
		± 1.7 (Note 9)
		± 0.3 (Note 10)
		± 0.02 (Note 11)
Operating temperature	Topr	-40 to 85 °C

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Note 4:  $V_{CC} = 0V$ 

Note 5: High or Low state.

Note 6:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 7:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ 

Note 8:  $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$ 

Note 9:  $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$ 

Note 10:  $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$ 

Note 11: V<sub>CC</sub> = 0.9 V





# **Electrical Characteristics**

### **DC Characteristics**

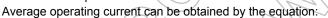
Characteristics Symbol Test Cond		Symbol Test Condition		Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
		Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic		
					0.9	_	-<	0.73	_	0.80	
				1.1	_	- /	0.86	_	0.93		
	Positive	\/-			1.4	_	_ '	1.07	Y —	1.12	
	threshold voltage	V <sub>P</sub>		_	1.65	_	(7)	1.23	_	1.25	
					2.3	7	$\mathbb{W}$	1.66	_	1.68	
Threshold					3.0	+	1	2.14		2.15	V
Voltage					0.9	0.18	)		0.07	_	V
					1.1	0.26		_	0.18	_	
	Negative threshold	\/			1.4	0.36	_	-0	0.31	> _	
	voltage	V <sub>N</sub>		_	1.65	0.45	_	6	0.41	_	
					2.3	0.69			0.64	_	
					3.0	0.96			0.91	_	
				\(\lambda\)	0.9	0.20	+C	0.38	0.15	0.53	
					1.1	0.25	) ((	_0.41	0.21	0.53	
Hysteresis Voltag	76	V <sub>H</sub>			1.4	0.35	$\mathbb{Z}$	0.48	0.34	0.57	V
Trysteresis voltag	ge .	VH		7	1.65	0.42	))	0.56	0.40	0.60	V
			,		2.3	0.60	_	0.74	0.60	0.76	
			(		3.0	0.79	_	0.93	0.79	0.94	
				I <sub>OH</sub> =–0.02 mA	0.9	0.75		1	0.75		
			I <sub>OH</sub> = -0.3 mA	1.1 to 1.3	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_		
	High level	VOH	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	V <sub>CC</sub> × 0.75	_		V <sub>CC</sub> × 0.75	_	
		7	I <sub>OH</sub> = -3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.45	_	_	V <sub>CC</sub> -0.45	_		
			_	$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0	_	_	2.0	_	
Output voltage				I <sub>OH</sub> = -8.0 mA	3.0 to 3.6	2.48	_	_	2.48	_	V
Voltage	<\?			$I_{OL} = 0.02 \text{ mA}$	0.9			0.1		0.1	
			I <sub>OL</sub> = 0.3 mA	1.1 to 1.3	-		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		V <sub>CC</sub> × 0.25		
	Low level	V <sub>OL</sub>	VIN=VIH	lo⊾ = 1.7 mA	1.4 to 1.6	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
	>	(()	$\sim$	$I_{OL} = 3.0 \text{ mA}$	1.65 to 1.95	_		0.45		0.45	
		7,		I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	_	_	0.4		0.4	
				I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_	_	0.4		0.4	
Input leakage cu	rrent	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V		0 to 3.6	_		±0.1		±1.0	μА
Power off leakag	e current	l <sub>OFF</sub>	V <sub>IN</sub> = 0 to 5.5 V or V <sub>OUT</sub> = 0 to 3.6 V		0	_	_	1.0	_	10.0	μΑ
Quiescent supply	/ current	Icc	$V_{IN} = V_{CC}$ or GND		3.6	_	_	1.0	_	10.0	μА

### AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
Characteristics			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
		$C_L$ = 10 pF, $R_L$ = 1 M $\Omega$	0.9	_	41.3	_	_	_	ns
			1.1 to 1.3	_	18.0	25.4	1.0	40.8	
			1.4 to 1.6	_	9.5	12.2	1.0	13.5	
			1.65 to 1.95	_	7.0	8.7	1.0	9.3	
			2.3 to 2.7	_	4.7	5.7	1.0	6.2	
			3.0 to 3.6	1	3.7	4.5	1.0	4.7	
Propagation delay time	<sup>t</sup> pLH <sup>t</sup> pHL	$C_L = 15  pF$ , $R_L = 1  M\Omega$	0.9	->	44.4				
			1.1 to 1.3	-((	19.3	> 27.7	1.0	46.9	
			1.4 to 1.6		)0.2	13.1	1.0	14.7	
			1.65 to 1.95	1	7.5	9.3	1.0	9.9	
			2.3 to 2.7	1	5.0	5.9	1.0	6.4	
			3.0 to 3.6	$\langle \rangle$	4.0	4.8	1.0	5.2	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		55.8	4	4)	_	
			1.1 to 1.3	_	24.7	36.3	1.0	59.6	
			1.4 to 1.6	_	12.9	16.8	1.0	19.2	
			1.65 to 1.95	_	9.2	11.5	1.0	12.9	
			2.3 to 2.7		5.9	7.1	1.0	8.3	
			3.0 to 3,6		4.9	5.7	1.0	6.6	
Input capacitance	C <sub>IN</sub>		3.6	_	) ]3	_	_		pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 12)	0.9 to 3.6	-	/11	_	_		pF

Note 12: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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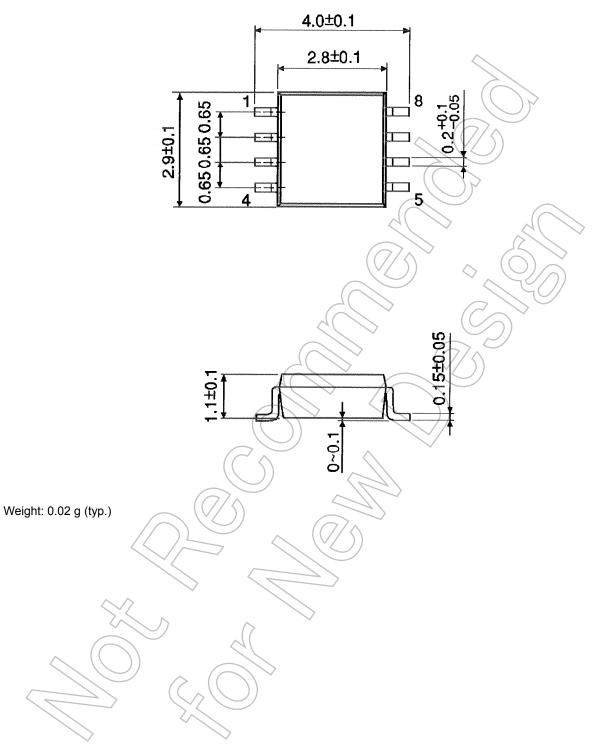
ICC (opr.) = CPD·VCC·fIN + ICC/3





# **Package Dimensions**

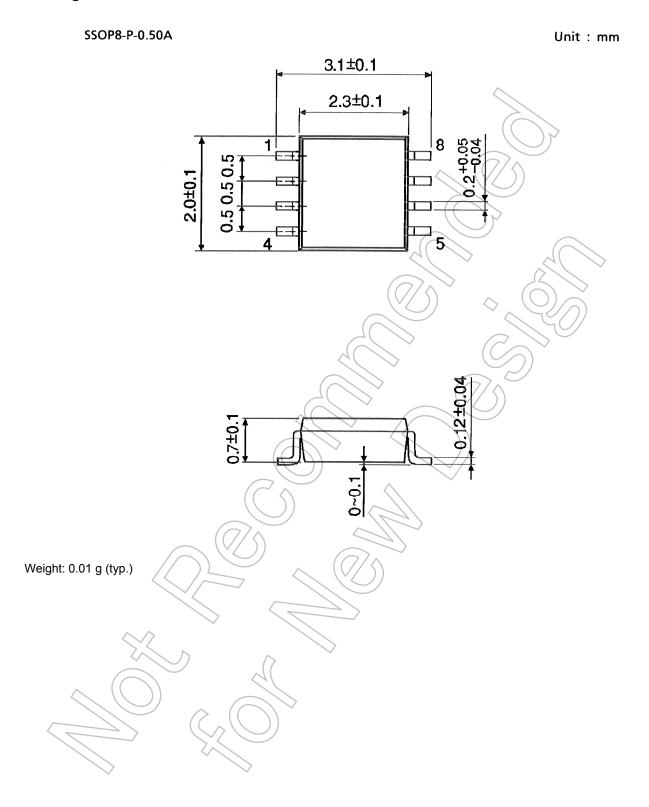
SSOP8-P-0.65 Unit: mm



5 2014-03-01



# **Package Dimensions**



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