TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WG17FC

Triple Schmitt Buffer

Features

High output current : ±8 mA (min) at V_{CC} = 3 V

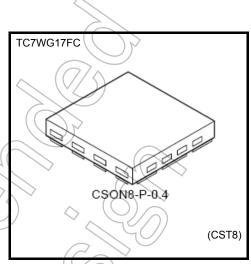
Super high speed operation: t_{pd} = 4.0 ns (typ.)

at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

Operating voltage range : V_{CC} = 0.9 to 3.6 V

• 5.5-V tolerant inputs

• 3.6-V power down protection outputs



Weight: 0.002 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	-0.5 to 4.6	\\
DC input voltage	V _{IN}	-0.5 to 7.0	\ \
DC output voltage	Vout	-0.5 to 4.6 (Note1)	^ V
DC output voltage	VOU1	-0.5 to V _{CC} +0.5 (Note2)	
Input diode current	I _{IK}	-20	mA
Output diode current	lok (-20 (Note3)	mA
DC output current	ЮИТ	±25	mA
DC V _{CC} /GND current	/lcc	±50	mA
Power dissipation	PD	150 (Note4)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High or Low State.

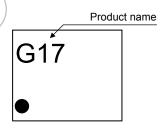
Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: Vout < GND

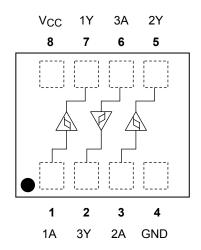
Note 4: Mounted on an FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 11.56 \text{ mm}^2)$

Marking



Pin Assignment (top view)



Start of commercial production 2006-04

IEC Logic Symbol

Truth Table



А	Y
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating
Supply voltage	V _{CC}	0.9 to 3.6
Input voltage	V _{IN}	0 to 5.5
Output voltage	Vour	0 to 3.6 (Note 5)
	Vout	0 to V _{CC} (Note 6)
		± 8.0 (Note 7)
	I _{OH} /I _{OL}	±4.0 (Note 8)
Output current		± 3.0 (Note 9)
Output current		±1,7 (Note 10)
		± 0.3 (Note 11)
		± 0.02 (Note 12)
Operating temperature	T _{opr}	-40 to 85 °C

Note 5: $V_{CC} = 0V$

Note 6: High or Low state.

Note 7: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 8: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 9: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 10: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 11: $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$

Note 12: $V_{CC} = 0.9 \text{ V}$



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Electrical Characteristics

DC Characteristics

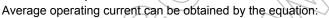
Characteristics Symbol Test Condition		Symbol	Toot	Condition	Ta = 25°C				Ta = -40 to 85°C		Unit
		Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic		
					0.9	_	-<	0.73	_	0.80	
				1.1	_	_ /	0.86	_	0.93		
	Positive	.,,			1.4	_	_ \	1.07	7 –	1.12	
	threshold voltage	V _P		_	1.65	_	(7)	1.23	_	1.25	
					2.3	4		1.66	_	1.68	
Threshold					3.0	+0	1	2.14	_	2.15	V
Voltage					0.9	0.18	7)	_	0.07	_	V
					1.1	0.26	-	_	0.18	_	
	Negative	.,			1.4	0.36	_	->	0.31	> _	
	threshold voltage	V _N		_	1.65	0.45	_	4	0.41	_	-
					2.3	0.69		7	0.64	_	
					3.0	0.96	_(0.91	_	
	•			A((0.9	0.20	+C	0.38	0.15	0.53	
					1.1	0.25) (0.41	0.21	0.53	
Llyatarasia Valtar	~~	V			1.4	0.35	//-\f\	0.48	0.34	0.57	V
Hysteresis Volta	ge	V _H		7	1.65	0.42		0.56	0.40	0.60	V
				2.3	0.60	\ —	0.74	0.61	0.76		
			(3.0	0.79	/ —	0.93	0.80	0.94	
				I _{OH} =–0.02 mA	0.9	0.75			0.75	_	
		Voн	VIN = VIH	$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
	High level			I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
				I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45	_	
			_	$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0	_	_	2.0	_	
Output voltage			\supset	I _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	_	2.48	_	V
voltage	<\?			$I_{OL} = 0.02 \text{ mA}$	0.9			0.1		0.1	
				I _{OL} = 0.3 mA	1.1 to 1.3			V _{CC} × 0.25		V _{CC} × 0.25	
	Low level	VoL	VIN=VIL	I _{OL} = 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
		((\)	()	I _{OL} = 3.0 mA	1.65 to 1.95	_	_	0.45		0.45	
		7,		I _{OL} = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	
				I _{OL} = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage cu	rrent	I _{IN}	V _{IN} = 0 to 5.5 V		0 to 3.6	_	_	±0.1	_	±1.0	μА
Power off leakag	e current	l _{OFF}	V _{IN} = 5.5 V or V _{OUT} = 3.6 V		0	_	_	1.0	_	10.0	μΑ
Quiescent supply	/ current	I_{CC} $V_{IN} = V_{CC}$ or GND		3.6	_	_	1.0	_	10.0	μА	

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
Characteristics			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time		C _L = 10 pF,	0.9	_	41.3	_	_	_	
			1.1 to 1.3		18.0	25.4	1.0	40.8	
			1.4 to 1.6		9.5	12.2	1.0	13.5	ns
		$R_L = 1 M\Omega$	1.65 to 1.95		7.0	8.7	1.0	9.3	
			2.3 to 2.7		4.7	5.7	1.0	6.2	
	^t pLH ^t pHL		3.0 to 3.6	1	3.7	4.5	1.0	4.7	
		C_L = 15 pF, R_L = 1 M Ω	0.9	-	44.4)	_	_	
			1.1 to 1.3	_((19.3	> 27.7	1.0	46.9	
			1.4 to 1.6)0.2	13.1	1.0	14.7	
			1.65 to 1.95		7.5	9.3	1.0	9.9	
			2.3 to 2.7	1	5.0	5.9	1.0	6.4	
			3.0 to 3.6	\(\frac{1}{2}\)	4.0	4.8	1.0	5.2	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		55.8	4	4)	_	
			1.1 to 1.3		24.7	36.3	1.0	59.6	
			1.4 to 1.6		12.9	16.8	1.0	19.2	
			1.65 to 1.95		9.2	11.5	1.0	12.9	
			2.3 to 2.7		5.9	7.1	1.0	8.3	
			3.0 to 3.6		4.9	5.7	1.0	6.6	
Input capacitance	C _{IN}		3.6	_) 3		_	_	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 to 3.6	\ _	/11	_	_	_	pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.



 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$

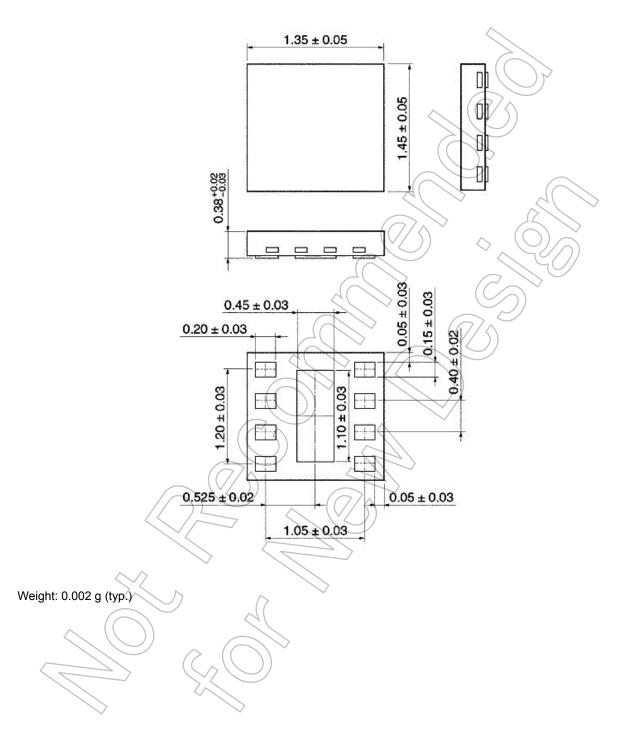


TC7WG17FC



Package Dimensions

CSON8-P-0.4 Unit: mm



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