TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WG34FU, TC7WG34FK

#### Triple NON-Inverter

#### **Features**

High output current : ±8 mA (min) at V<sub>CC</sub> = 3 V

• Super high speed operation: t<sub>pd</sub> = 2.7 ns (typ.)

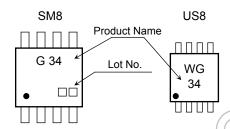
at  $V_{CC} = 3.3 \text{ V},15\text{pF}$ 

Operating voltage range : V<sub>CC</sub> = 0.9 to 3.6 V

5.5-V tolerant inputs

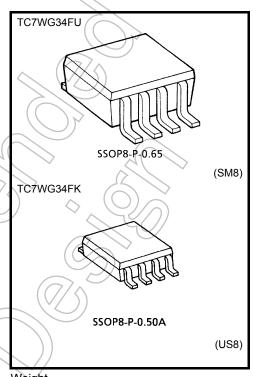
• 3.6-V power down protection outputs

#### Marking



#### Absolute Maximum Ratings (Ta = 25°C)

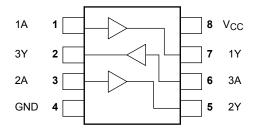
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Characteristics	Symbol	Rating	Unit			
Supply voltage	Acc	-0.5 to 4.6	$\sum X$			
DC input voltage	VIN	-0.5 to 7.0	(			
DC output voltage	VOLIT	-0.5 to 4.6 (Note1)				
DC output voltage	VOUT	-0.5 to VCC+0.5 (Note2)	<b>V</b>			
Input diode current	l <sub>IK</sub>	-20	mA			
Output diode current	lok	-20 (Note3)	mA			
DC output current	lout	±25	mA			
DC V <sub>CC</sub> /GND current	Icc	±50	mA			
Power dissipation	PD	300 (SM8) 200 (US8)	mW			
Storage temperature	T <sub>stg</sub>	-65 to 150	°C			



Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

#### Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or Low State. Do not exceed  $I_{\mbox{\scriptsize OUT}}$  of absolute maximum ratings.

Note 3: V<sub>OUT</sub> < GND

Start of commercial production 2005-09

## **IEC Logic Symbol**

#### **Truth Table**



Α	Υ
L	L
Н	Н

## **Operating Ranges**

Characteristics	Symbol	Rating
Supply voltage	$V_{CC}$	0.9 to 3.6
Input voltage	V <sub>IN</sub>	0 to 5.5
Output voltage	Vour	0 to 3.6 (Note 4)
	V <sub>OUT</sub>	0 to V <sub>CC</sub> (Note 5)
Output current	I <sub>OH</sub> /I <sub>OL</sub>	± 8.0 (Note 6)
		± 4.0 (Note 7)
		± 3.0 (Note 8)
		± 1.7 (Note 9)
		± 0.3 (Note 10)
		± 0.02 (Note 11)
Operating temperature	T <sub>opr</sub>	-40 to 85 °C
Input rise and fall time	dt/dv	0 to 10 (Note 12) ns/V

Note 4:  $V_{CC} = 0V$ 

Note 5: High or Low state.

Note 6:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 7:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ 

Note 8:  $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$ 

Note 9:  $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$ 

Note 10:  $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$ 

Note 11:  $V_{CC} = 0.9 \text{ V}$ 

Note 12:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V



## **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	ool Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
Symbol rest con		Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic	
High-level input VIH				0.9	V <sub>CC</sub>		4	V <sub>CC</sub>	1	
			_		V <sub>CC</sub> × 0.7	_		V <sub>CC</sub> × 0.7	_	V
					V <sub>CC</sub> × 0.65	-(	7/6	V <sub>CC</sub> × 0.65	_	
vollago	ntage """			1.65 to 1.95	V <sub>CC</sub> × 0.65			V <sub>CC</sub> × 0.65	_	
				2.3 to 2.7	1.7	(-)	> -	1.7	_	
				3.0 to 3.6	2.0			2.0	_	
				0.9	4	$\rightarrow$	GND	H).	GND	
				1.1 to 1.3	775	>	V <sub>CC</sub> × 0.3	3	V <sub>CC</sub> × 0.3	V
Low-level input voltage		_	1.4 to 1.6		_	V <sub>CC</sub> × 0.35	H	V <sub>CC</sub> × 0.35		
			1.65 to 1.95	_	- (	V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35		
		6	2.3 to 2.7	_		0.7		0.7		
			2	3.0 to 3.6		\ <del>\</del> \	0.8		0.8	
			I <sub>OH</sub> =-0.02 mA	0.9	0.75	1	_	0.75	_	
			I <sub>OH</sub> = -0.3 mA	1.1 to 1.3	V <sub>CC</sub> × 0.75	)	_	V <sub>CC</sub> × 0.75	_	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75		V
voltage		I <sub>OH</sub> = -3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0,45	_	_	V <sub>CC</sub> -0.45			
			I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0	_	_	2.0	-	
			$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48	_	_	2.48		
			$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
		$\supset$	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
Low-level output voltage	VOL	$V_{IN} = V_{IL}$	I <sub>OL</sub> = 1.7 mA	1.4 to 1.6	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	V
			I <sub>OL</sub> = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	
$\langle \langle \langle \langle \rangle \rangle \rangle$		I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4		
		> ((	I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current	I <sub>IN</sub>	$V_{IN} = 0$ to	5.5 V	0 to 3.6	_	_	±0.1	_	±1.0	μΑ
Power off leakage current	l <sub>OFF</sub>	V <sub>IN</sub> = 0 to 5.5 V V <sub>OUT</sub> = 0 to 3.6 V		0	_	_	1.0	_	10.0	μА
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		3.6	_	_	1.0	_	10.0	μΑ

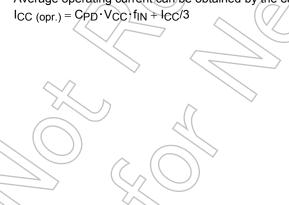
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## AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics S	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
	Test Condition		V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Offic
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	24.4	_	_	_	
			1.1 to 1.3		11.6	21.7	1.0	40.5	
			1.4 to 1.6		6.5	9.8	1.0	11.6	
			1.65 to 1.95		4.9	7.0	1.0	7.6	
			2.3 to 2.7		3.2	4.4	<b>1.0</b>	4.9	
			3.0 to 3.6	- <	2.4	3.5	1.0	4.1	
Propagation delay time		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		26.9		_	_	ns
	<sup>t</sup> pLH <sup>t</sup> pHL		1.1 to 1.3		12.7	24.2	1.0	42.1	
			1.4 to 1.6	7	7.1	10.7	1.0	12.9	
			1.65 to 1.95	4	5.3	7.5	1.0	7.7	
			2.3 to 2.7	77	3.5	4.8	1.0	5.5	
			3.0 to 3.6		2.7	3.8	1.00	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	)	37.0			_	
			1.1 to 1.3		17.1	33.9	1.0	64.1	
			1.4 to 1.6		9.3	14.3	1.0	17.4	
			1.65 to 1.95	_	6.9	9.8	1.0	10.2	
			2.3 to 2.7		4.6	6.2	1.0	6.6	
			3.0 to 3.6 <	<u> </u>	3.7	4.8	1.0	5.2	
Input capacitance	C <sub>IN</sub>		3.6	+	/3	_	_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note13)	0.9 to 3.6	_	10	_	—	_	pF

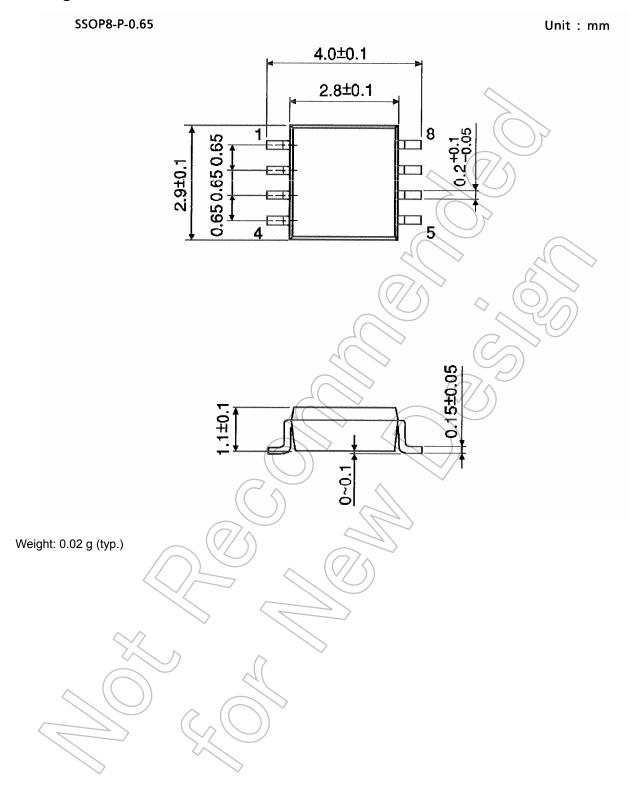
Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:





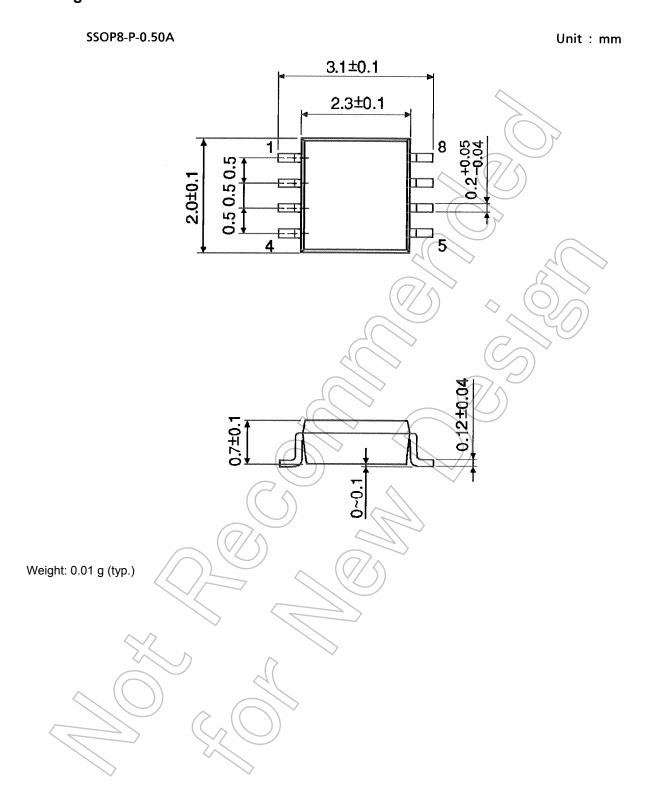
## **Package Dimensions**



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## **Package Dimensions**



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