

STEREO DEMODULATOR FOR CAR- AND MAINS OPERATED RADIOS

Technology: Bipolar

Features:

- Supply voltage range
 $V_S = 8 \dots 18 \text{ V}$
- FM mpx. switch or matrix operation possible
- Excellent rejection of ARI* subcarrier and pilot tone harmonics

Case:

18 pin dual inline plastic

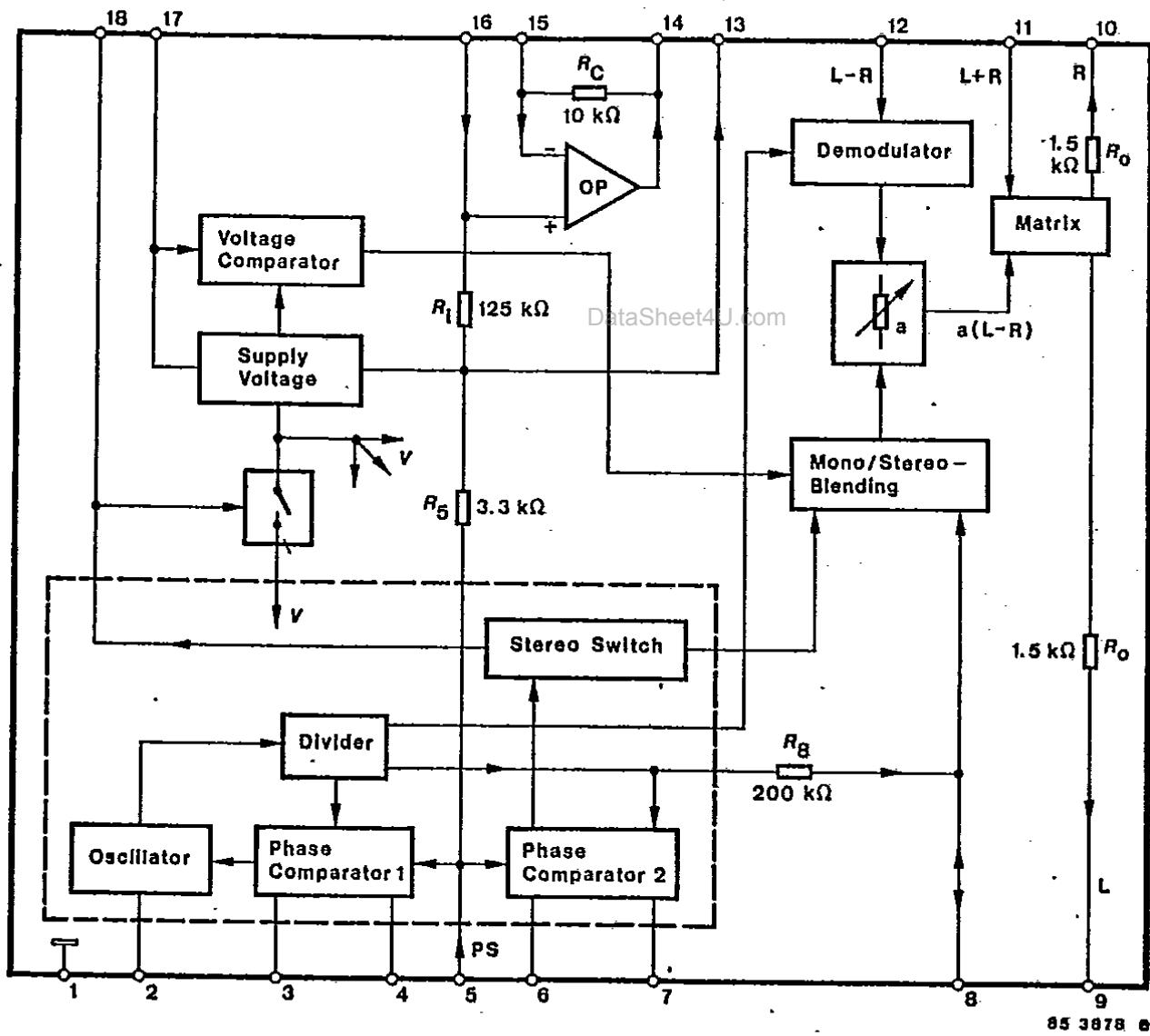


Fig. 1 Block diagram and pin connections

Pin configuration

Pin	Function
1	Ground
2	Oscillator RC
3	TP phase comparison 1
4	TP phase comparison 1
5	Pilot tone (PT) input
6	TP phase comparison 2
7	TP phase comparison 2
8	f_{osc} output/St-Mo blending V_H
9	Output L

Pin	Function
10	Output R
11	(L+R) input
12	(L-R) input
13	Reference voltage
14	Output op amp
15	-input op amp
16	+input op amp
17	Supply voltage
18	Lamp connection/oscillator switch

Description

The TCA 4511 decodes the transmitter side stereo information in both L and R channels. Stereo transmission is shown by means of an indicator lamp. A continual blending of mono and stereo signals is possible. The switching frequencies are controlled by a phase-locked loop. The stereo decoder can be used in time multiplex (switching) or in frequency multiplex (matrix) mode of operation.

Switching operation

The MPX input signal is corrected in amplitude and phase by an operational amplifier. For this purpose an RC circuit is connected at Pin 15.

Subsequently, the (L+R) and (L-R) signals are processed in separate stages. The (L-R) signal is demodulated and can be reduced by the factor a through mono/stereo blending. In the final matrix circuit the aggregate signal (L+R) is added to the demodulated signal a (L-R) according to the following formula:

$$(L-R) + a(L-R) = L(1+a) + R(1-a)$$

$$(L+R) - a(L-R) = L(1-a) + R(1+a)$$

$$0 \leq a \leq 1$$

Mono Blending Stereo

The generated output signals are then forwarded to two external RC low-passes for deemphasis. The required frequency to demodulate the L-R signal is obtained by a phase-locked loop (PLL) from the divider. By means of a pilot tone applied to Pin 5, the oscillator is synchronized by phase comparison 1. An additional phase comparison 2 provides mono or stereo information. Based on this information, the indicator lamp is activated and lights up when a sufficiently strong signal is present at the input. Moreover, the (L-R) reduction is eliminated.

If switch S1 is open, the IC switches the oscillator off, whereby the stereo switch and the mono/stereo blending suppress the L-R signal. The supply current is thus reduced. Also, since the oscillator does not resonate when switch S1 is open, AM receiver signals can be forwarded without interference via the IC.

If Pin 8 is not connected, the oscillator frequency can be measured. For normal operating functions, the blending voltage V_H is applied to Pin 8 or Pin 8 must be blocked by a capacitor. Otherwise, cross-talk is affected by the oscillator frequency.

Absolute maximum ratings

Reference point Pin 1

Supply voltage	Pin 17	V_S	18	V
Lamp voltage	Pin 18	V_{LP}	18	V
Current for stereo indicator lamp $V_{18} \cdot I_{LP} \leq 300$ mW	Pin 18	I_{LP}	50	mA
Minimum values at all terminals		V_{ext}	≥ 0	V
Junction temperature		T_J	+150	°C
Storage temperature range		T_{stg}	-40...+150	°C
Ambient temperature range		T_{amb}	-25...+85	°C

Maximum thermal resistance

Junction ambient	R_{thJA}	78	K/W
Electrical characteristics for switching operation			
$V_S = 8 \text{ V}$, reference point Pin 1, $T_{amb} = 25^\circ\text{C}$, unless otherwise specified			
Supply voltage range	Pin 17	V_S	8
Total supply current (FM-operation) S1 closed	Pin 17	I_S	14
Total supply current (AM-operation) S1 open	Pin 17	I_S	10
Lamp current control range $V_{18} \cdot I_{LP} \leq 300 \text{ mW}$	Pin 18	I_{LP}	10
Lamp current short circuited $V_{18} \cdot I_{LP} \leq 300 \text{ mW}$	Pin 18	I_{LP}	50
Input amplifier			
Input signal	Pin 16	V_i	1.6
Output signal	Pin 14	$V_o^{1)}$	V_{16}
Input resistance	Pin 16	R_i	90
Feedback resistance	Pin 15/14	R_f	125
Reference voltage	Pin 13	V_{Ref}	10
			kΩ
			1.75
			V
Stereo matrix			
Output voltage (Stereo) modulated	Pin 9, 10	$V_{oAF}^{1) 6)}$	0.9
Output voltage (Mono) L or R modulated	Pin 9, 10	$V_{oAF}^{2) 6)}$	0.45
Output resistance	Pin 9, 10	R_o	1.5
Separation Fig. 2 $f_{AF} = 1 \text{ kHz}$	Pin 9, 10	$\alpha_v^{1)}$	40
Rejection ratio 19 kHz		α_{19}	30
38 kHz		α_{38}	32
57 kHz		α_{57}	40
76 kHz		α_{76}	45
			dB
Supply voltage rejection ratio	Pin 9, 10	$V_{ripple}^{3)}$	30
Noise voltage	Pin 9, 10	$V_{on}^{4)}$	40
Total harmonic distortion $f_{AF} = 1 \text{ kHz}$	Pin 9, 10	THD ^{1) 6)}	80
Channel balance	Pin 9, 10	$B_{al}^{2)}$	0.5
Switching noise mono/stereo S1 closed-open	Pin 9, 10	ΔV_o	dB
			60
			mV

Oscillator				Min.	Typ.	Max.
Output resistance for f_{osc} -measuring	Pin 8	R_o		200		kΩ
Oscillator basic frequency		f_{osc}		19		kHz
Capture and hold range	Pin 2	$f_{C/H}^{(1)}$	±0.4	±1	±2.0	kHz
Balancing resistor $f_{osc} = 19 \text{ kHz}$	Pin 2/1	R_{osc}	13	18	18	kΩ
Oscillator-“ON” S1 closed $I_o = 10 \text{ mA}$	Pin 18	V_L	1.0			V
	Pin 18	V_L	0.9			V
Oscillator-“OFF” S1 open or $V_{18} \leq 0.4 \text{ V}$	Pin 18	V_L		0.4		V
Phase comparison						
Input voltage	Pin 5	$V_i^{(1)}$	0.5	0.7	0.9	V_{pp}
Input resistance	Pin 5	R_i		3.3		kΩ
Input voltage	Pin 5	V_i		1.6		V_{pp}
Stereo switch						
$f = 19 \text{ kHz}, S1 \text{ closed}$						
Switch threshold “ON” “OFF”	Pin 5	V_{iPT}		30	55	mV_{pp}
Hysteresis	Pin 5	V_{iPT}	12	15		mV_{pp}
		H		3	6	9
Mono/stereo blending						
Mono $V_H = V_8 = 0.5 \text{ V}$		$\alpha_{blend}^{(7)}$		3	6	9
Stereo $V_H = V_8 = 0.9 \text{ V}$		$\alpha_{blend}^{(7)}$		34		dB

¹⁾ $V_i = 1.2 V_{pp} \text{ MPX}, V_H \geq 1 \text{ V}, S1 \text{ closed}, f_{AF} = 1 \text{ kHz}$ ²⁾ $V_i = 1.2 V_{pp} \text{ MPX}, S1 \text{ open}, f_{AF} = 1 \text{ kHz}$ ³⁾ $V_S = 12 \text{ V} + V_{St}, V_{St} = 200 \text{ mV}, 200 \text{ Hz}$ ⁴⁾ CCIR DIN 45405, unweighted, S1 open⁵⁾ S1 closed⁶⁾ after LP $f_a = 6.5 \text{ kHz}$, reduction 36 dB/octave⁷⁾ $V_{16} = 0.75 V_{pp} \text{ MPX}, S1 \text{ closed}, f_{AF} = 1 \text{ kHz}$

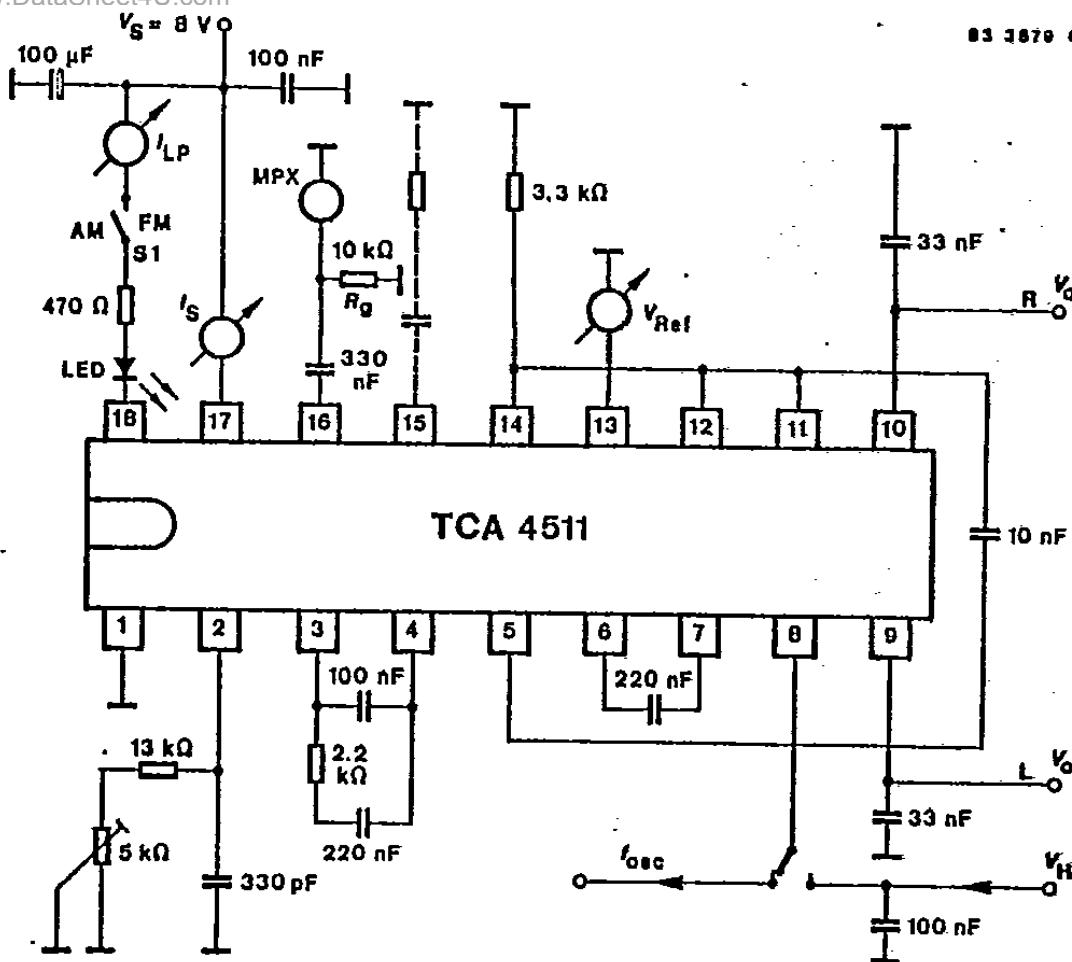


Fig. 2 Test circuit

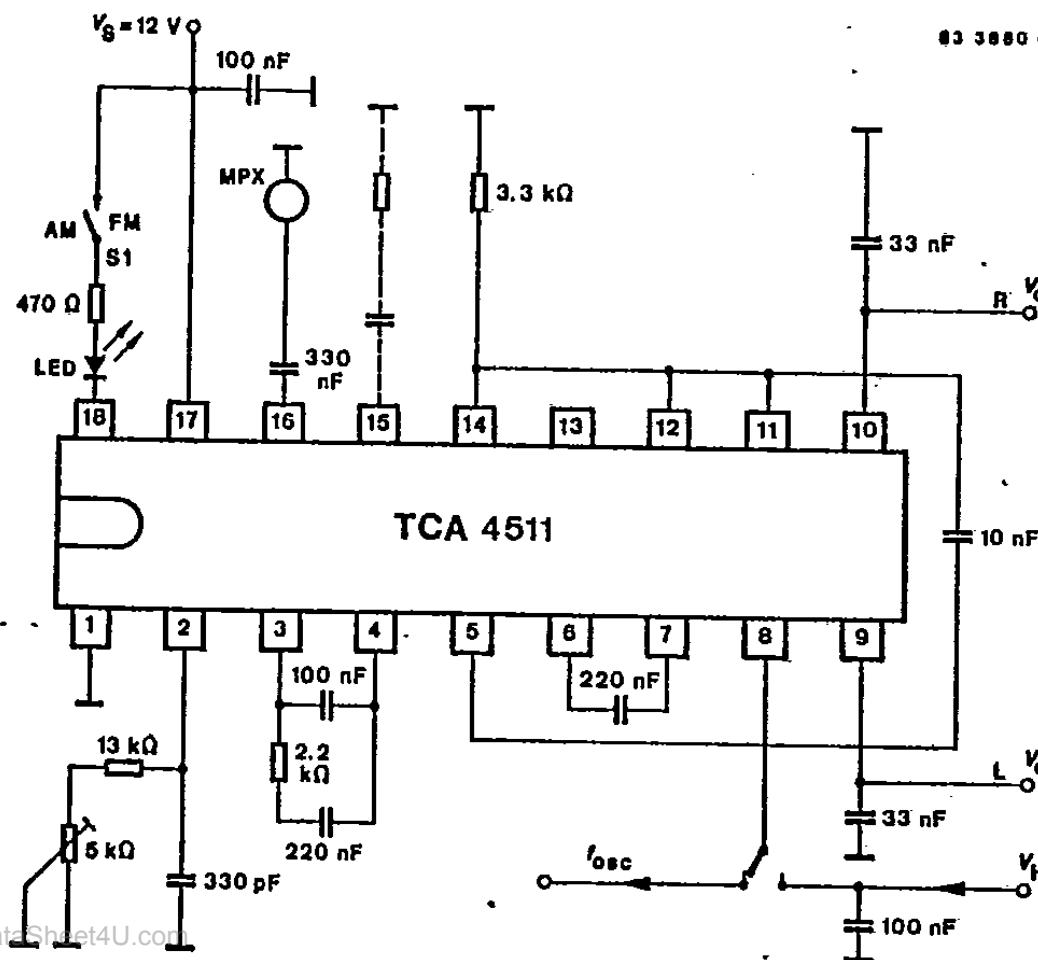
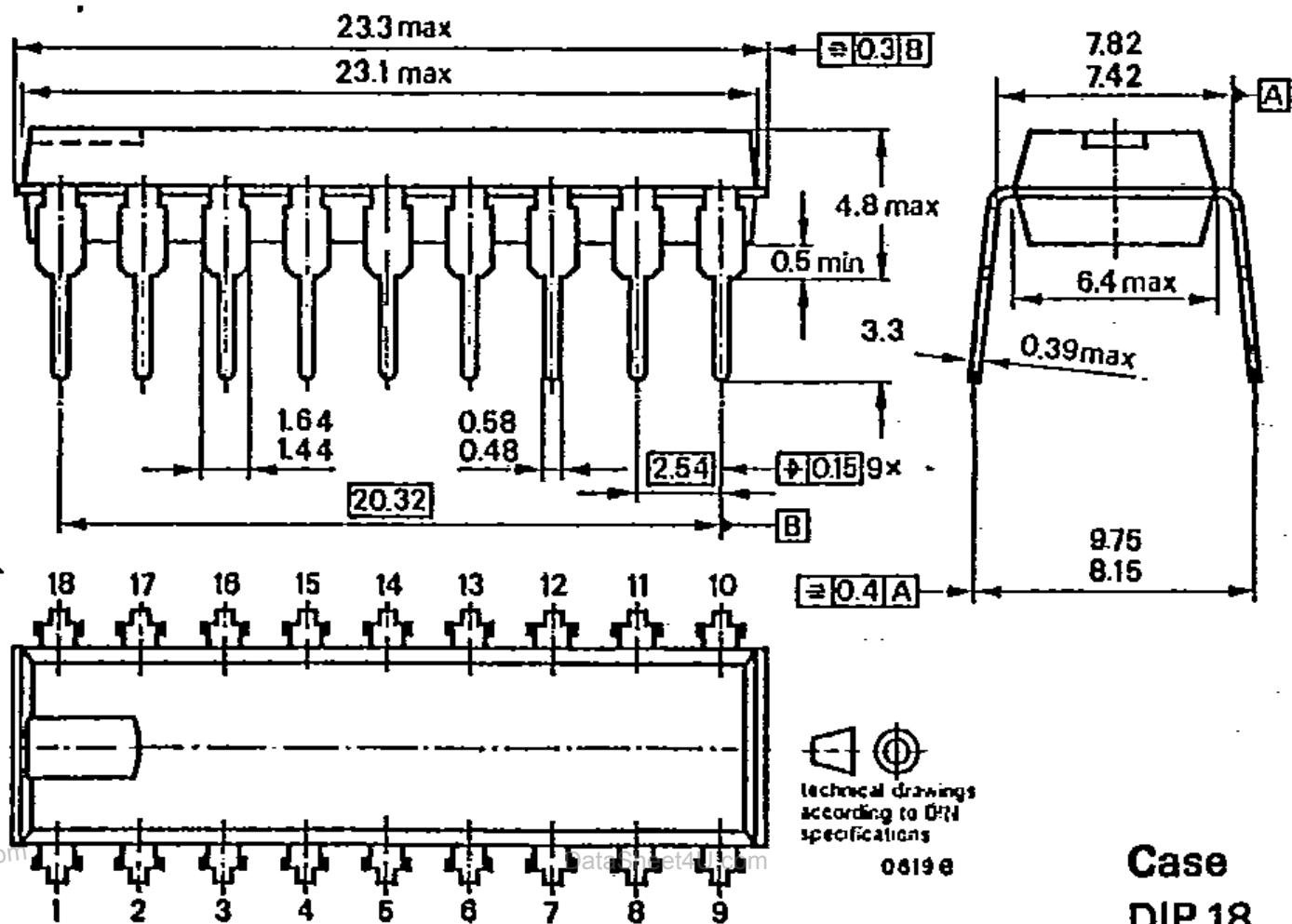


Fig. 3 Application switching operation

Dimensions in mm

Case
DIP 18