SPEC. NO.	TQ3C-8EAF0-E1DDE55-00
DATE	February 15, 2007

SPEC

FOR	•	
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<u>TYPE: TCG057QV1AC-G11</u>

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KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by :Engineering Dept. Confirmed by :QA D				
Issue Date	Prepared	Checked	Approved	Checked	Approved
February 15, 2007	O. Ajisaka	J. Yanazahi	4. Matsuno to	J. Sakaguchi	To Sul

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

Date		Designed by: Engineering Dept. Confirmed by: QA Dep		QA Dept.			
рате	Prepared		red	Checked	Checked Approved Checked Approved		
Rev. No.	Date		Page		Descriptio	ons	

1. Application

This data sheet defines the specification for a $(320 \times R.G.B) \times 240$ dot, amorphous silicon TFT transmissive color dot matrix type Liquid Crystal Display with CFL backlight. [RoHS Compliant]

2. Construction and Outline

 $(320 \times R.G.B) \times 240$ dots, COG type LCD with CFL backlight.

Backlight system : "U" figured type CFL (1 tube).

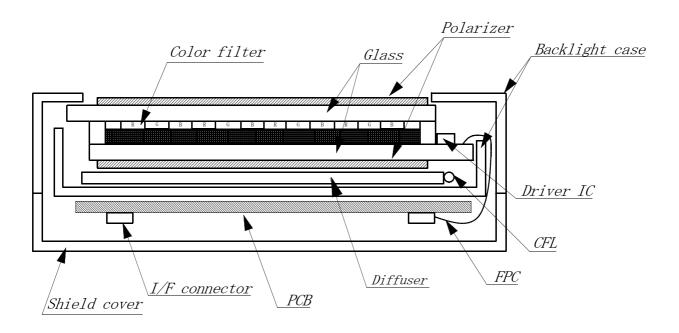
Inverter : Option.

Recommended Inverter : CXA-L0612A-VJL(TDK)

or Equivalent.

Polarizer : Glare treatment.

Additional circuits: Timing controller, Power supply (3.3V input)



This drawing is showing conception only.

3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	144 (W) \times (104.8) (H) \times 13.0 (D)	mm
Effective viewing area	117.2 (W) × 88.4 (H)	mm
Dot number	$(320 \times R. G. B)$ (W) \times 240 (H)	Dots
Dot pitch	0.12 (W) × 0.36 (H)	mm
Display mode *1	Normally white	_
Mass	220	g

^{*1} Due to the characteristics of the LCD material, the color vary with environmental temperature.

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	Min.	Max.	UNIT
Power input voltage	VDD	0	4. 0	V
Input signal voltage *1	Vin	-0.3	6. 0	V

^{*1} Input signals : CK, RO~R5, GO~G5, BO~B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

4-2. Environmental absolute maximum ratings

ITEM		SYMBOL	Min.	Max.	UNIT
Operating temperature	*1	Тор	-10	70	$^{\circ}\!\mathbb{C}$
Storage temperature	*2	Tsto	-30	80	$^{\circ}\! \mathbb{C}$
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		_	*5	*5	_
Shock		_	*6	*6	_

- *1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25° C, another temperature range should be confirmed.
- *2 Temp. = -30 °C < 48 h , Temp = 80 °C < 168 h Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock. LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard. (Please refers to 12. Precautions for use as detail).
- *3 Non-condensation.
- *4 Temp. \leq 40°C, 85%RH Max. Temp. > 40°C, Absolute Humidity shall be less than 85% RH at 40°C.

*5 Frequency $10\sim55~\mathrm{Hz}$ Converted to acceleration value : $(0.3\sim9~\mathrm{m/s^2})$ Interval $10-55-10~\mathrm{Hz}~1~\mathrm{minute}$

2 hours in each direction $\,$ X/Y/Z (6 hours as total) EIAJ ED-2531

*6 Acceleration: 490m/s^2 Pulse width: 11 ms

3 times in each direction : $\pm X/\pm Y/\pm Z$.

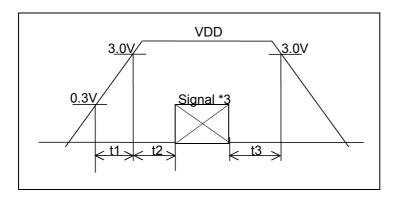
EIAJ ED-2531

5. Electrical Characteristics

 $VDD = +3.3V \pm 0.3V$, Temp. = $-10 \sim 70^{\circ}C$

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	
Power input voltage *1	VDD=3.3V	VDD	3. 0	3. 3	3. 6	V
Current consumption *2	۷۷۷–3. 3۷	IDD	_	130	160	mA
Permissive input ripple v	Vrp	-	_	100	mVp-p	
Input signal voltage (Low) *3		VIL	0	_	0. 3VDD	V
Input signal voltage (High) *3		VIH	0. 7VDD	_	+5. 5	V

*1 VDD-turn-on conditions



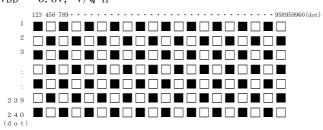
 $0 < t \ 1 \leq 2 \ 0 \, \text{ms}$

 $0 < t \ 2 \le 5 \ 0 \, \mathrm{ms}$

 $0 < t \ 3 \le 1 s$

*2 Power consumption Black & White pattern:

VDD = 3.3V, V/Q=H



*3 Input signals : CK, R0 \sim R5, G0 \sim G5, B0 \sim B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

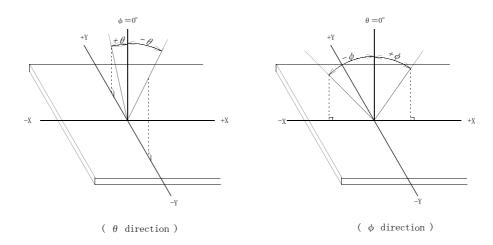
6. Optical Characteristics

Measuring points = ϕ 6.0mm , Temp. = 25°C

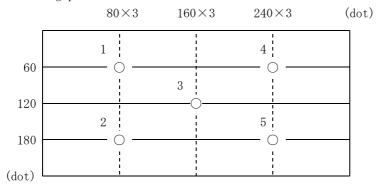
ITEN	1	SYMBOL	CONDITION		MIN	TYP	MAX	UNIT
Response	Rise	τr	$\theta = \phi$	=0°	_	10	_	ms
time	Down	τd	$\theta = \phi$	=0°	_	25	_	ms
		0		Upper	_	(70)	_	1
W: : 1		θ	CD > 5	Lower	_	(70)	_	deg.
Viewing angle	range	,	CR≧ 5	Left	_	(70)	_	1
		φ		Right	_	(70)	_	deg.
Contrast rati	0	CR	$\theta = \phi = 0^{\circ}$		300	450	_	_
Brightness		L	IL=4.0mArms		300	400	_	cd/m^2
	D I	Х	$\theta = \phi = 0^{\circ}$		0. 557	0.607	0.657	
	Red	у	$\theta = \phi$	=0	0. 293	0. 343	0. 393	
	C.	X	$\theta = \phi = 0^{\circ}$		0. 253	0.303	0. 353	
Charantisitas	Green	у	θ – φ	-0	0. 495	0. 545	0. 595	_
Chromaticity coordinates	D1	X	0 - 1	-0°	0.099	0. 149	0. 199	
	Blue	у	$\theta = \phi$	-0	0.070	0. 120	0. 170	
	WI. : + -	Х	0 = 1	-0°	0. 261	0. 311	0.361	
	White	у	$\theta = \Phi$	$\theta = \phi = 0^{\circ}$		0.318	0.368	

6-1. Contrast ratio is defined as follows:

6-2. Definition of viewing angle



6-3. Measuring points



- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after CFL is turned on. (Ambient Temp.=25°C)
- 3) The inverter should meet the rating of the CFL;
 -Sine, symmetric waveform without spike in positive and negative.

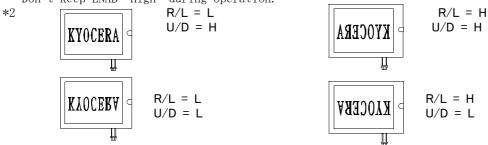
7. Interface signals

7-1. LCD

PIN NO.	SYMBOL	DESCRIPTION	I/0	Note
1	GND	GND	_	
2	CK	Clock signal for sampling each data signal	I	
3	Hsync	Horizontal synchronous signal (negative)	I	
4	Vsync	Vertical synchronous signal (negative)	I	
5	GND	GND	_	
6	R0	RED data signal (LSB)	Ι	
7	R1	RED data signal	I	
8	R2	RED data signal	Ι	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND		
13	GO	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	_	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	В5	BLUE data signal (MSB)	I	
26	GND	GND	_	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	VDD	3.3V power supply	_	
29	VDD	3.3V power supply	_	
30	R/L	Horizontal display mode select signal	Ι	*2
		L : Normal , H : Left / Right reverse mode		
31	U/D	Vertical display mode select signal	Ι	*2
		H : Normal , L : Up / Down reverse mode		
32	V/Q	VGA / QVGA mode select signal(H : VGA, L : QVGA)	I	
33	GND	GND	_	

LCD side connector : 08-6210-033-340-800+(ELCO)
Recommended matching FFC or FPC : P = 0.5mm

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2. Don't keep ENAB "High" during operation.



7-	-2.	С	F	L

PIN NO.	SYMBOL	DESCRIPTION
1	HOT	Inverter output high voltage side
2	NC	_
3	COLD	Inverter output low voltage side

LCD side connector : BHR-03VS-1 (JST) Recommended matching connector : SM02(8.0)B-BHS-1 (JST)

: SMO2(8.0)B-BHS-1-TB-(LF)(SN) (JST) · · · (RoHS Compliant)

* Please be careful NOT to connect inversely an inverter-output high voltage side to the CFL low voltage side. It may result in damage or electric shock.

8. Timing Characteristics of input signals

8-1. Timing characteristics

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
	F	1 /7	_	25. 18	28. 33	MIT-	V/Q=H
Clock	Frequency	1/Tc		6. 3	7. 0	MHz	V/Q=L
	Duty ratio	Tch/Tc	40	50	60	%	
D-+-	Set up time	Tds	5	_	_	ns	
Data	Hold time	Tdh	10	_	_	ns	
		TH	30. 0	31. 8	_	μs	V/Q=H
H:1	C1-	IΠ	770	800	900	clock	V / Ø−H
Horizontal sync. signal	Cycle	TH	50. 0	63. 6	_	μs	V/Q=L
		ТН	360	400	450	clock	V/Q-L
	Pulse width	ТНр	2	96	200	clock	
	Cycle	TV	515	525	560	1.	V/Q=H
Vertical sync.		TV	251	262	280	line	V/Q=L
signal	Pulse width	TVp	2	_	34	line	
Horizontal displa	y period	THd	320			clock	
HsyncClock phas	HsyncClock phase difference		10	_	Tc-10	ns	
HsyncVsync. phase difference		TVh	0	_	ТН-ТНр	ns	
Vertical sync.signal start position		TV	34			line	V/Q=H
		TVs	7				V/Q=L
Vertical display	period	TVd		240		line	

^{*}In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

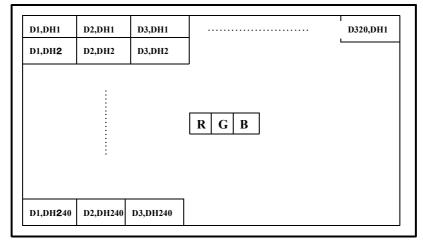
8-2. Horizontal display position The horizontal display position is determined by ENAB signal.

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
F 1.1 1	Set up time	Tes	5	_	Tc-10	ns	
Enable signal	Pulse width	Тер	2	320	TH-10	clock	
HsyncEnable signal phase difference		T1	44	_	TH-664	-11-	V/Q=H
		The	2	_	TH-340	clock	V/Q=L

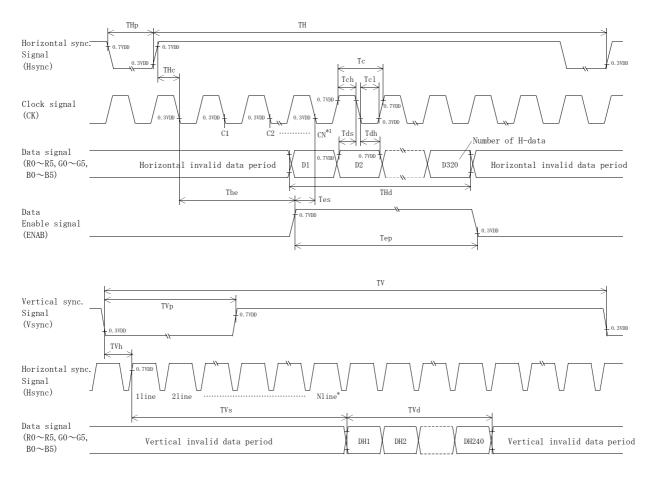
^{*}When ENAB is fixed at "V/Q=H", the display starts from the data of C104(clock) as shown in 8-5. *When ENAB is fixed at "V/Q=L", the display starts from the data of C52(clock) as shown in 8-5.

8-3. Vertical display position The vertical display position (TVs) is fixed at 34th line (V/Q=H) and 7th line (V/Q=L). Note) ENAB signal is independent of vertical display position.

8-4. Input Data Signals and Display position on the screen



8-5. Input Timing Characteristics



^{*1} When ENAB is fixed at V/Q="H", the display starts from the data of C104(Clock). When ENAB is fixed at V/Q="L", the display starts from the data of C52(Clock).

^{*2} The vertical display position(TVs) is fixed at 34^{th} line(V/Q=H) and 7^{th} line(V/Q=L).

9. Backlight Characteristics

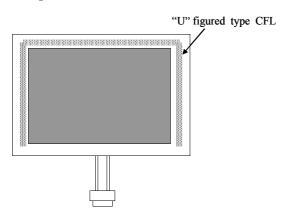
Temp. = 25° C

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting discharge Voltage	VS			1550 Vrms	0 ℃
*1	VS	_	_	1035 Vrms	25 ℃
Discharging tube current *2	IL	3.0 mArms	4.0 mArms	5.0 mArms	1
Discharging tube voltage	VL	_	685 Vrms	_	-
Operating life *3 (IL=4.0 mArms)	Т	60,000 h	75, 000 h	_	_
Operating frequency *4	F	30 kHz	_	100 kHz	_

- *1 The Non-load output voltage (VS) of the inverter should be 1.3 times the maximum VS at the low temperature to provide margin to assure that the CFL will start, because actual VS may increase due to leakage current from the CFL cables. (Reference value: 2015 Vrms Min)
- *2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.
- *3 End of life is defined as when the illuminance or quantity of light has decreased to 50% of the initial value. Illuminance of light will drastically decrease when LCD is operated at lower temperature for long hours.
- *4 The driving frequency of the CFL may interfere with the horizontal synchronous signal, leaving interference stripes on the display. So please evaluate LCD panels beforehand.

 To avoid interference stripes, we recommend to separate as far as possible the CFL frequency from the horizontal synchronous signal and its high harmonic frequency.
- * There may be cases where interface noise on LCD PCB, generated by high-voltage products such as inverters, may leave stripes on the display. Please be careful when designing a mold to take into consideration that the inverter shall be located as far as possible from PCB. Shield protection may be effective.
- * Prolonged storage in darkness and/or low temperature may slow the ignition and rise to full brightness of the CFL in an LCD Module. Please use an inverter designed to provide sufficient driving voltage for more than 1 second. Also a decreased Starting Discharge Voltage or shortened ignition time may not turn ON the CFL lamp.

* CFL arrangement figure



10. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

$\begin{array}{c ccccccccccccccccccccccccccccccccccc$										
①YEAR ②MONTH ③DATE ④Version Number ⑤Country of origin(Japan or China										
YEAR	2007	2008	2009	2010	2011	2012				
CODE	7	8	9	0	1	2				
MONTH	JAN.	FEB.	MAR.	APR.	MAY.	JUN.				
CODE	1	2	3	4	5	6				
MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.				
CODE	7	8	9	X	Y	Z				

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

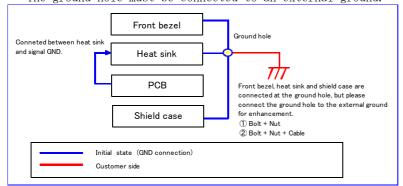
11-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

12. Precautions for use

12-1. Installation of the LCD

1. The LCD's bezel must be grounded. The heat sink and shield cover are connected at the ground hole. The ground hole is located on the right side of the LCD when viewed from the front. The ground hole must be connected to an external ground.



- 2. A transparent protection sheet shall be added to protect the LCD and its polarizers.
- 3. The LCD shall be installed so that there is no pressure on the LSI chips.
- 4. The LCD shall be installed flat, without twisting or bending.
- 5. The display window size should be the same as the effective viewing area.
- 6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 7. Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque:3.3 \pm -0.3kgf.cm Please set up'SPEED-LOW', 'SOFT START-SLOW' when using electric driver .

Recommendable screw : P-TITE screw nominal dia.3.0mm

installing boss hole depth 3.5 ± 0.5 mm

Please be careful not to use high torque which may damage LCD module in installation.

- 8. A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.
- 9. Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be designed to protect CFL lead wires from external stress.
- 10. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.

Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

12-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

12-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.

It may also change the characteristics of the liquid crystal.

This phenomenon may not recover. The LCD shall be operated within the temperature limits specified.

12-4. Storage

- 1. The LCD shall be stored within normal temperature and humidity. Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. Always store the LCD so that it is free from external pressure onto it.

12-5. Caution items when handling the LCD

- 1. <u>DO NOT</u> store in a high humidity environment for extended periods. <u>Image</u> degradation, bubbles, and/or peeling off of polarizer may result.
- 2. The front polarizer is easily scratched or damaged.

 Prevent touching it with any hard material, and from being pushed or rubbed.
- 3. The LCD screen may be cleaned with a soft cloth or cotton pad.

 Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
- 4. Water may cause damage or discoloration of the polarizer. Clean any condensation or moisture from any source immediately.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.
- 6. Do not disassemble LCD module because it will result in damage.
- 7. Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend to use screen saver etc. in cases where a solid-base image pattern must be used.
- 8. Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body, rinse it off right away with water and soap.

13. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	80°C	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	−30°C	240 h	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40℃ 90 %RH	240 h	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-30°C 0.5 h R.T. 0.5 h 80°C 0.5 h	10 cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	70°C	500 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect

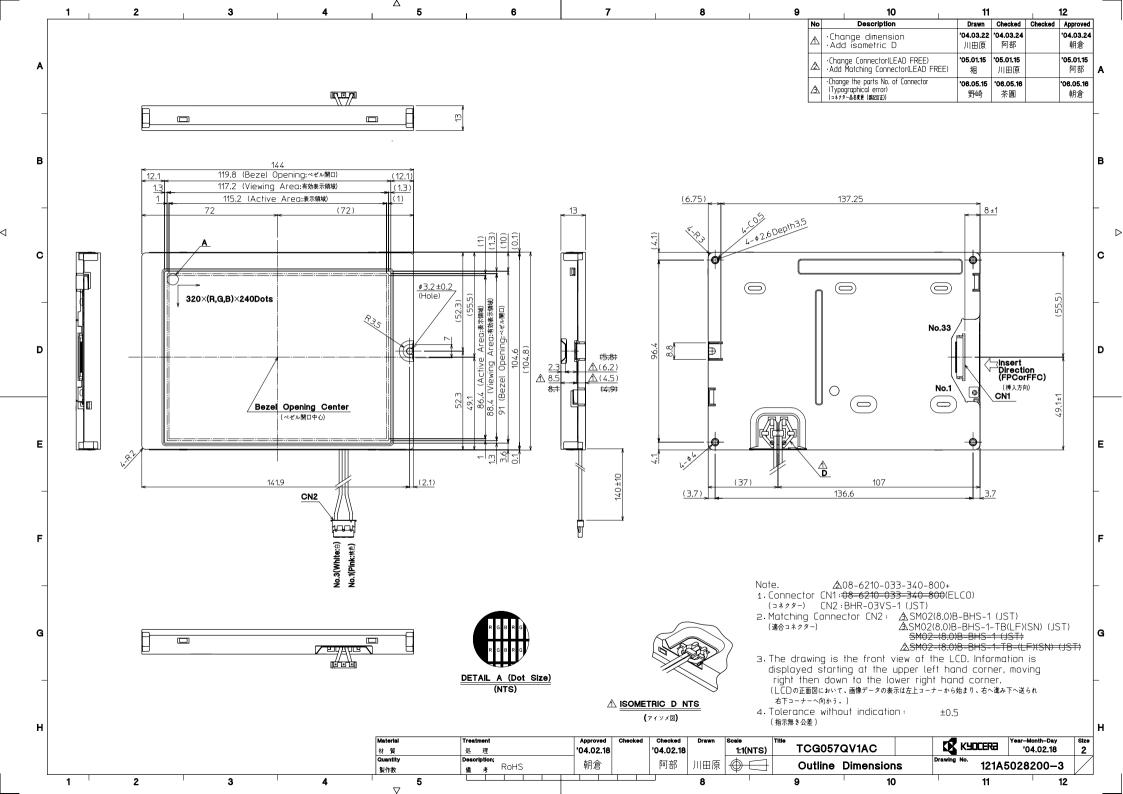
st Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

st The LCD is tested in circumstances in which there is no condensation.

st The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.

^{*} The reliability test is not an out-going inspection.

^{*} The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.



SPEC. NO.	TQ3C-8EAF0-E2DDE52-00				
DATE	February 15, 2007				

$\Gamma \cap D$	•		
FOR	•		

KYOCERA INSPECTION STANDARD

<u>TYPE</u>: TCG057QV1AC-G11

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed	by :Engineer	ing Dept.	Confirmed by :QA Dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved	
February 15, 2007	O. Ajisaka	J. Yomazohi	4 Matsunoto	J. Sakoguchi	To Suf	

Revision Record

Date		Design	ed by:	y: Engineering Dept. Confirmed by: Q		QA Dept.	
рате	-	Prepa	red	Checked	Approved	Checked	Approved
Rev. No.	Date		Page		Descriptio	ons	

1) Note

		Note							
General	1. Should any de additional st customer and	andard shall be deter	specified in this standard happen, rmined by mutual agreement between						
	2. Inspection Co Luminance Inspection d Temperature Direction	:500 Lux minimum							
Definition of Inspection item	Dot defect	Bright dot	Defect constantl yappears bright, even in display of all "Black" pixels. Count: Visible though 5% transparency of filter. No count: Not visible though 5% transparency of filter. RGBRGBRGB RGBRGBRGB RGBRGBRGB RGBRGBRGB						
		Black dot	Defect constantly appears black, even in "White" pixels, Size is based on bright dot.						
		Two dots join	Dot join defect is defined as two or more dots which always display a matching brightness, even when each of them is set to different brightness value. RGBRGBRGB RGBRGB RGBRGBRGB RGBRGBRGB						
			As the above shows, two dot join is defined as defects of two adjoing dots like 'R' and 'G'.						
	External inspection	Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixcels "Black" or "White") and non operating.						
	Others	CFL lead wires	Damaged CFL lead wires, functional failure, appearance failure.						
	Definition of size	Definition of cir a $d = \frac{(a+b)}{2}$	Definition of linear size						

2) Standard

Classification	Inanast	ion itom		Indaomor t	a t ar-	dard		
Classification	Inspection item		Judgement standard					
Dot defect	Bright dot		Acceptable number : 4 bright dots Bright dot spacing : 5 mm or more					
	Black dot		Acceptable number : 5 black dots Black dot spacing : 5 mm or more					
	2 data jain	Bright dot	Acceptable numb	er : 2				
	2 dots join	Black dot	Acceptable number : 3					
	3 or more dot	s join	Acceptable numb	er : 0				
	Total dot def	ects	Acceptable numb	er : 5	Max			
	White dot, Bl	ack dot	Size (mm	1)	٨	acceptable Number		
	(Circle)		d<		В	(neglected)		
			0.2 <d≤0< td=""><td></td><td></td><td>5</td></d≤0<>			5		
			0. 2 < d ≤ 0 0. 4 < d ≤ 0			3		
			0.4 <d=0.5<d< td=""><td>U. U</td><td></td><td>0</td></d=0.5<d<>	U. U		0		
			0.5\u			U		
External inspection	Polarizer (Scr	atches)	Width (mm)	Length (r	nm)	Acceptable Number		
			W≤0.1	_		(neglected)		
			0.1 (₩ < 0.0	L≦5.0		(neglected)		
			0.1<₩≦0.3	5.0 <l< td=""><td>0</td></l<>		0		
			0.3 <w -<="" td=""><td colspan="2">0</td></w>		0			
	Polarizer (Bubble, Dent)		G: ()			A		
			Size (mm)			Acceptable Number		
			d<0.2		(neglected)			
			0.2 <d≤0.3< td=""><td colspan="2">5</td></d≤0.3<>		5			
			0.3 <d≦< td=""><td>0.5</td><td></td><td>3</td></d≦<>	0.5		3		
			0.5 <d< td=""><td colspan="2">0</td></d<>			0		
	Foreign Parti shape)	cle(Circular	Size (mm)			Acceptable Number		
			d<0.2		(neglected)			
			0.2 <d≤0.4< td=""><td colspan="2">5</td></d≤0.4<>		5			
			0.4 <d≤< td=""><td>0.5</td><td colspan="2">3</td></d≤<>	0.5	3			
			0.5 <d< td=""><td colspan="2">0</td></d<>		0			
	Foreign Parti shape),Scratc	cle (Linear	Width (mm)	Length (r	nm)	Acceptable Number		
	Shape, , betate	******	W≦0.03	- Longth (I	/	(neglected)		
			n=0.00	1 <	2 0	(neglected)		
			 0.03 <w≤0.1< td=""><td colspan="2">L≦2.0 2.0<l≦4.0< td=""><td>(Hegrecteu)</td></l≦4.0<></td></w≤0.1<>	L≦2.0 2.0 <l≦4.0< td=""><td>(Hegrecteu)</td></l≦4.0<>		(Hegrecteu)		
			0.00 (11=0.1	2.0 <l≤4.0 4.0<l< td=""><td>0</td></l<></l≤4.0 		0		
			0.1 <w< td=""><td>-</td><td></td><td>(According to Circular shape)</td></w<>	-		(According to Circular shape)		