			SPEC. NO.	TQ3C-8EAFO	-E1DDE09-00
			DATE	September	29, 2004
SPEC					
	<u>F 0</u>	<u>R</u> :			
	<u> </u>	<u>CG057Q</u> \	<u>7 1 A D - G 0</u>	0	
		CONTENT	S		
 LAPPLICATION Application Construction and Outline Mechanical Specifications Absolute Maximum Ratings Electrical Characteristics Optical Characteristics Optical Characteristics Interface Signals Timing Characteristics of input signals Backlight Characteristics Design Guidance for Analog Touch-Panel(T/P) Lot Number Identification Warranty Precautions for Use Reliability Data / Environmental Test Outline Drawing 					7. 2004 ivision
Consult K	yocera before	ordering.			
Uriginai Issue Data	Prepared	Checked	Approved	Checked	Approved
September 29, 2004	7. Onodera	Y. Yamaza ki	M.Fujitani	y, josh ju	7. minani

.

Caution

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices in the areas of audio control, office automation, industrial control, home appliances, etc. The modules should not be used in applications where module failure could result in physical harm or loss of life, and Kyocera expressly disclaims any and all liability relating in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, losses, damages, liabilities, awards, costs, and expenses, including legal fees, resulting from or arising out of Customer's use, or sale for use, of Kyocera modules in applications.
- 3. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

D - 4		Design	ed by:	by: Engineering Dept. Confirmed by: QA Dept.		: QA Dept.	
Date		Prepa	red	Checked	Checked Approved Checked		
Rev. No.	Date		Page		Descriptio	ons	

1. Application

This data sheet defines the specification for a $(320 \times R. G. B) \times 240$ dot, amorphous silicon TFT transmissive color dot matrix type Liquid Crystal Display with CFL backlight.

2. Construction and Outline

$(320 \times \text{R. G. B}) \times 240 \text{ dc}$	ot	s, COG type LCD with CFL backlight.
Backlight system	:	$^{\prime\prime}\text{U}^{\prime\prime}$ figured type CFL (1 tube).
Inverter	:	Option. Recommended Inverter : CXA-L0612A-VJL(TDK) or Equivalent.
Polarizer	:	Glare treatment.
Additional circuits	:	Timing controller, Power supply (3.3V input)
Touch Panel	:	Analog type. Non-Grare treatment.



This drawing is showing conception only.

3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	144.0 (W) \times (104.8) (H) \times 14.8 (D)	mm
Effective viewing area	117.2 (W) \times 88.4 (H)	mm
Dot number	$(320 \times R. G. B)$ (W) \times 240 (H)	Dots
Dot pitch	0.12 (W) × 0.36 (H)	mm
Display mode *1	Normally white	_
Mass	260	g

*1 Due to the characteristics of the LCD material, the color vary with environmental temperature.

3-2. Mechanical Specifications of touch panel

ITEM	SPECIFICATION	UNIT
Input	Radius-0.8 stylus or Finger	_
Actuation Force	$0.5N\pm0.3N$	g
Transmittance	Тур. 80	%
Surface hardness	pencil hardness 2H or more according	—

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	Min.	Max.	UNIT
Power input voltage	VDD	0	4.0	V
Input signal voltage *1	Vin	-0.3	6. 0	V
Touch panel supply voltage	Vtp	0	6. 0	V
Touch panel Input current	Itp	0	0.5	mA

*1 Input signals : CK, R0 \sim R5, G0 \sim G5, B0 \sim B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

4-2. Environmental absolute maximum ratings

ITEM		SYMBOL	Min.	Max.	UNIT
Operating temperature *1		Тор	-10	70	°C
Storage temperature *2		Tsto	-30	80	°C
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		—	*5	*5	—
Shock		—	*6	*6	_

*1 Operating temperature means a temperature which operation shall be guaranteed.

Since display performance is evaluated at 25°C, another temperature range should be confirme

*2 Temp. = -30 °C < 48 h , Temp = 80 °C < 168 h Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock. LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard. (please refer to 13. Precautions for use as detail).

*3 Non-condensation.

*4 Temp. ≤ 40°C, 85%RH Max. Temp. > 40°C, Absolute Humidity shall be less than 85% RH at 40°C.

*5

d.

Frequency	10~55 Hz	Converted to
Vibration width	0.15 mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10 Hz	1 minute

2 hours in each direction $\rm X/Y/Z$ (6 hours as total) EIAJ ED-2531

*6 Acceleration: $490m/s^2$ Pulse width : 11 ms 3 times in each direction : $\pm X/\pm Y/\pm Z$. EIAJ ED-2531

5. Electrical Characteristics

5-1. LCD

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	
Power input voltage *1	VDD-9 9V	VDD	3.0	3.3	3.6	V
Current consumption *2	۷ <i>DD</i> =3, 3۷	IDD	_	130	160	mA
Permissive input ripple v	Vrp	_	_	100	mVp-p	
Input signal voltage (L	VIL	0	_	0.3VDD	V	
Input signal voltage (High) *3		VIH	0.7VDD	_	+5.5	V

*1 VDD-turn-on conditions



 $0 < t \ 1 \leq 2 \ 0 \text{ ms}$ $0 < t \ 2 \leq 5 \ 0 \text{ ms}$ $0 < t \ 3 \leq 1 \ s$

*2 Power consumption Black & White pattern :

*3 Input signals : CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

5-2. Touch Panel

5-2-1. Terminal resistance
Between xL and xR : 200 \sim 1000 Ω
Between yU and yL : 200 \sim 1000 Ω5-2-2. Linearity
 $\pm 1.5\%$ 5-2-3. Insulation resistance
100M Ω or more at DC25V

6. Optical Characteristics

Measuring points = ϕ 6.0mm , Temp. = 25° C

ITEM		SYMBOL	CONDITION		MIN	ТҮР	MAX	UNIT
Response Rise		τr	$\theta = \phi$	$\theta = \phi = 0^{\circ}$		10	—	ms
time	Down	τd	$\theta = \phi$	=0°	_	25	—	ms
		0		Upper	_	70	—	1
V 1		θ		Lower	_	50	—	deg.
viewing angle	range		$CK \leq D$	Left	_	70	—	1
		φ		Right	_	70	_	deg.
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$		300	450	—	_
Brightness(IL=4.0mArms.)		L			240	370	_	cd/m^2
	ות	х	$\theta = \phi = 0^{\circ}$		0.557	0.607	0.657	
	кеа	У			0.293	0.343	0.393	
		х	$\theta = \phi = 0^{\circ}$		0.253	0.303	0.353	
	Green	У			0.495	0.545	0. 595	-
coordinates	D1	х		٥°	0.099	0.149	0. 199	
	Blue	У	$\theta = \phi$	$\theta = \phi = 0^{\circ}$		0.120	0.170	
	WI + + -	х	0 = 1	-0°	0.261	0.311	0.361	
	wnite	$\theta = \phi = 0^{\circ}$		-0	0.268	0.318	0.368	

6-1. Contrast ratio is defined as follows:

6-2. Definition of viewing angle



6-3. Measuring points



1) Rating is defined as the average brightness inside the viewing area.

2) 30 minutes after CFL is turned on. (Ambient Temp.=25°C)

The inverter should meet the eccentric conditions;
 -Sine, symmetric waveform without spike in positive and negative.

PIN NO	SYMBOL	DESCRIPTION	T/0	Note
1	GND	GND	-	note
2	CK	Clock signal for sampling each data signal	T	
3	Hsvnc	Horizontal synchronous signal (negative)	T	
4	Vsvnc	Vertical synchronous signal (negative)	T	
5	GND	GND	_	1
6	RO	RED data signal (LSB)	T	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND	-	
13	GO	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	Ι	
15	G2	GREEN data signal	Ι	
16	G3	GREEN data signal	Ι	
17	G4	GREEN data signal	Ι	
18	G5	GREEN data signal (MSB)	Ι	
19	GND	GND	_	
20	BO	BLUE data signal (LSB)	Ι	
21	B1	BLUE data signal	Ι	
22	B2	BLUE data signal	Ι	
23	B3	BLUE data signal	Ι	
24	B4	BLUE data signal	Ι	
25	B5	BLUE data signal (MSB)	Ι	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	Ι	*1
28	VDD	3.3V power supply		
29	VDD	3.3V power supply	-	
30	R/L	Horizontal display mode select signal	Ι	*2
	(-	L : Normal , H : Left / Right reverse mode	<u> </u>	
31	U/D	Vertical display mode select signal	Ι	*2
		H : Normal , L : Up / Down reverse mode		
32	V/Q	VGA / QVGA mode select signal		
33	GND	GND		

7. Interface signals

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2. Don't keep ENAB "High" during operation.



7-2. CFI		
PIN NO.	SYMBOL	DESCRIPTION
1	НОТ	Inverter output high voltage side
2	NC	—
3	COLD	Inverter output low voltage side
LCD side connector		: BHR-03VS-1 (JST)
Recommended matching connector		connector · SMO2-(8.0)B-BHS-1 (IST)

Recommended matching connector : SM02-(8.0)B-BHS-1 (JST)
* Please be careful NOT to connect inversely an inverter-output high voltage side to the CFL low
voltage side. It may result in damage or electric shock.

7-3. Touch panel

PIN No.	SYMBOL	DESCRIPTION
1	уU	y-Upper terminal
2	xL	x-Left terminal
3	уL	y-Lower terminal
4	xR	x-Right terminal

Timing Characteristics of input signals 8-1. Timing characteristics

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
	P	1 /T	_	25. 18	28.33	MI	V/Q=H
Clock	Frequency	1/1c		6.3	7.0	MHZ	V/Q=L
	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	—	_	ns	
Data	Hold time	Tdh	10	—	_	ns	
		TU	30.0	31.8	_	μs	V /0-U
		IH	770	800	900	clock	V/Q=H
signal	Cycle	TH	50.0	63.6	_	μs	V /0-I
			360	400	450	clock	V/Q−L
	Pulse width	THp	2	96	200	clock	
		TV	515	525	560	1.	V/Q=H
Vertical sync.	Cycle	TV	251	262	280	line	V/Q=L
signal	Pulse width	TVp	2	—	34	line	
Horizontal displa	y period	THd		320		clock	
HsyncClock phase difference		THc	10	_	Tc-10	ns	
HsyncVsync. phase difference		TVh	0	—	TH-THp	ns	
Vertical sync.signal start posi tion		TU I	34			1.	V/Q=H
		IVS	7			line	V/Q=L
Vertical display period		TVd		240		line	

*In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

SYMBOL ITEM MIN TYP UNIT NOTE MAX _ Tc-10 Set up time Tes 5 ns Enable signal Pulse width 2 TH-10 Tep 320 clock 44_ TH-664 V/Q=H Hsvnc.-Enable The clock signal phase difference 2 TH-340 V/Q=L _

8-2. Horizontal display position The horizontal display position is determined by ENAB signal.

*When ENAB is fixed at "V/Q=H", the display starts from the data of C104 (clock) as shown in 8-5. *When ENAB is fixed at "V/Q=L", the display starts from the data of C52 (clock) as shown in 8-5.

8-3. Vertical display position The vertical display position (TVs) is fixed at 34th line (V/Q=H) and 7th line (V/Q=L). Note) ENAB signal is independent of vertical display position.

8-4. Input Data Signals and Display position on the screen

D1,DH1	D2,DH1	D3,DH1		D320,DH1
D1,DH2	D2,DH2	D3,DH2		
			R G B	
D1,DH240	D2,DH240	D3,DH240		

8-5. Input Timing Characteristics



*1 When ENAB is fixed at V/Q="H", the display starts from the data of C104(Clock). When ENAB is fixed at V/Q="L", the display starts from the data of C52(Clock).

*2 The vertical display position(TVs) is fixed at 34^{th} line(V/Q=H) and 7^{th} line(V/Q=L).

9. Backlight Characteristics

Temp. = $25^{\circ}C$

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting discharge Veltege	VS	_	_	(1,550) Vrms.	−10 °C
*1	٧S	_		(1,035) Vrms.	25 °C
Discharging tube current *2,*3	IL	(3.0) mArms.	(4.0) mArms.	(5.0) mArms.	
Discharging tube voltage	VL	_	(685) Vrms.	_	
Operating life *4 (IL=4.0 mArms.)	Т	(60,000 h)	(75,000 h)	—	
Operating frequency	F	(30) kHz		(100) kHz	

- *1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables. (Reference value : (2,015) Vrms Min.)
- *2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.
- *3 Do not apply more than 6.0 mA discharging tube current. Because CFL maybe broken due to over current.
- *4 When the illuminance or quantity of light has decreased to 50 % of the initial value. Average life time of CFL will be decreased when LCD is operating at lower and higher temperature.
- *5 There may be a case that an interference noise between an input signal frequency of LCD and a brightness adjustment frequency of a backlight inverter is appeared. Please set the brightness adjustment frequency of a backlight inverter not to appear the interference noise.
- * CFL arrangement figure



1 O. Design Guidance for Analog Touch-Panel(T/P)

10-1. Electrical

In customer's design, please remember the following considerations.

- 1. Do not use the current regulated circuit.
- 2. Keep the current limit with top and bottom layer. (See Sec, 4-1)
- 3. Analog T/P can not sense two point touching separately.
- 4. A contact resistance is appeared at the touch point between top and bottom layer.
- After this resistance has stable read the T/P position data. 5. Analog T/P is also a "Capacitor" in an equivalent circuit.
 - Design your sensing circuit and low-pass filter with considering this "Capacitor" value.
- 6. Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

10-2. Software

- 1. Do the "User Calibration".
- "User Calibration" may be needed with long term using. 2. Include "User Calibration" menu in your software.
- 3. When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

10-3. Mechanical Design

10-3-1. Each "Area"

Please confirm the following information before starting your design.

(a) Key Area

"Key Area" is an area where T/P specifications(Linearity,Durability,Actuation force, etc.) are guaranteed.

- 1) Do a touch data sensing and calibration inside this area.
- 2) In normal cases it is a same size as your flat display's "active area".
- 3) The ITO layer durability near the edge of Key area is less stronger than the center.
- (b) Transparent Insulation-paste Area.

Insulation-paste is printed with 1.0 mm distance outer from "Key Area."

- 1) The purpose is to avoid potential shorting problem from the bezel housing edge from or housing "stick" when molding.
- 2) Consider your housing edge position to keep 1.0 mm distance from this paste line. (See. Fig. 1)
- 3) The cross section of this edge is taper shape. So if it is over the display's active area, it will be shining as a prism.
- (c) Prohibition Area

Input by pen and finger is prohibited in this area.

- Because of the thickness around T/P, the ITO layer on the PET film will be expanded and as a conclusion it will be cracked if pressed. (See. Fig. 2)
- 1) We strongly recommend that the bezel should protect this area.
- 2) An exposure of this area and stylus contact should be avoided.
- 3) When assembling at the customer, do not press this area with tools.
- 4) Consider your design to avoid the pressure by the housing bezel.

10-3-2. Example of Housing Design.

- 1) If an consumer will put a palm on housing in normal usage care should be taken as follows. 2) Keep the gap, for example 0.3 to 0.7 mm, between bezel edge and T/P surface.
- The reason is to avoid the bezel edge from contacting T/P surface that may cause a "short" with bottom layer. (See.Fig.1)
- 3) Insertion a cushion material is recommended.
- 4) The cushion material should be limited just on the bulbar insulation past area.
- If it is over the transparent insulation paste area a "short" may be occurred.
- 5) If there is a probability of the touch panel surface being exposed to water, steam or other liquids, then please take measures to ensure that the bezel / touch panel gap and housing are "water tight".
- 6) There is a vent channel to equalize air pressure between the inner space of the touch panel and the atmosphere. Please make sure it is not blocked by your housing and mounting method. There is also the possibility that moisture could percolate into the touch panel if moisture is allowed to accumulate around the air vent channel. Furthermore, avoid high air pressures inside your housing which could cause the touch panel outer surface to swell out from inflation.

10-3-3. Mounting on display and housing bezel

- 1) In all cases, the T/P should be supported form the backside of the glass.
- 2) Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.3) Never expand the T/P top layer (PET-film) like a balloon by internal air pressure.
- The life of the T/P will be extremely short.
- 4) If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.

If your final product will used in a humid circumstance or will be moved from humid, warm environments to cold ones, a dew condensation can occur.

Consider a water seal with your housing bezel.



Fig.1



Fig.2

1 1. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

YEAR	2004	2005	2006	2007	2008	2009
CODE	4	5	6	7	8	9
MONTH	JAN.	FEB.	MAR.	APR.	MAY.	JUN.
CODE	1	2	3	4	5	6

MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	Х	Y	Z

12. Warranty

12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

12-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

1 3. Precautions for use

- 13-1. Installation of the LCD
- 1. In case of necessity due to environment to use LCD and countermeasure of noise, LCD is structured to have GND connection available. We recommend to use LCD to connect GND in order to stabilize a display performance.



- 2. The LCD shall be installed so that there is no pressure on the LSI chips.
- 3. The LCD shall be installed flat, without twisting or bending.
- 4. The display window size should be the same as the effective viewing area.
- 5. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 6. Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be designed to protect CFL lead wires from external stress.
- This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 13-2. Static Electricity
- 1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operation should wear ground straps.
- 13-3. LCD Operation
- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Adjust "LCD driving voltage" to obtain optimum viewing angle and contrast.
- 3. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.

It may also change the characteristics of the liquid crystal.

<u>This phenomenon may not recover.</u> The LCD shall be operated within the temperature limits specified.

13-4. Storage

- 1. The LCD shall be stored within the temperature and humidity limits specified.
- Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. Always store the LCD so that it is free from external pressure onto it.

13-5. Screen Surface

1. DO NOT store in a high humidity environment for extended periods.

Image degradation, bubbles, and/or peeling off of polarizer may result.

- 2. Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3. When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4. Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.5. Always keep the LCD free from condensation during testing.
- Condensation may permanently spot or stain the polarizers.

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	80°C	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	−30°C	240 h	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40°C 90 %RH	240 h	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-30℃ 0.5 h R.T. 0.5 h 80℃ 0.5 h	10 cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	70°C	500 h	Display Quality : No defect Current Consumption : No defect
Point Activation life	Polyacetal stylus (RO.8) Hitting force 3N Hitting speed 2 time/s	one million times	Satisfy the spec • Terminal resistance • Insulation resistance • Linearity • Actuation Force

1 4. Reliability Data / Environmental Test

* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

- \ast The LCD is tested in circumstances in which there is no condensation.
- * The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.
- * The reliability test is not an out-going inspection.
- * The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.



			SPEC. NO.	TQ3C-8EAF0	-E2DDE08-00				
			DATE	September	· 29, 2004				
<u>K Y O</u>	<u>FC</u> CERA IN	OR: NSPECTI	ON STAN	N D A R D					
<u>TYPE : TCG057QV1AD-G00</u>									
KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION									
Original	Designed	by :Engineer:	ng Dept.	Confirmed b	oy :QA Dept.				
Issue Data	Prepared	Checked	Approved	Checked	Approved				
September 29, 2004	7. Onodera	Y Yamasaki	M.FujiTanj	y. Joshinta	7. miname				

Revision Record

Date		Designed by: Engineering Dept.			ept.	Confirmed by	: QA Dept.
Date		Prepa	red	Checked	Approved	Checked	Approved
Rev. No.	Date		Page		Descriptio	ons	

1) Note

	Note				
General	1. Customer iden shall be revi determined by	tified anomalies not ewed by Kyocera, and mutual consent.	defined within this inspection standard an additional standard shall be		
	2. Inspection Co Luminance Inspection d Temperature Direction	nditions : 500 Lux m istance : 25 ± 5 % : directly	ninimum Trom the sample) C above		
Definition of Inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool:5% Transparency neutral density filter. Count dot:If the dot is visible through the filter Don't count dot:If the dot is not visible through the filter. RGBRGBRGB RGBRGBRGB RGBRGBRGB		
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "white data sent to the screen.		
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.		
			R G B R G B R G B R G B R G B R G B		
	External inspection	Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixcels "Black" or "White") and non operating.		
		Appearance inspection	Does not satisfy the value at the spec.		
	Others	CFL wires	Damaged to the CFL wires, connector, pin, functional failure or appearance failure.		
	Definition of size	Definition of ci a $d = \frac{(a+b)}{2}$	Definition of linear size		

2) Standard

Classification		Inspection item		Judgement standard					
defect (in LCD	Dot defect	Bright dot de	fect	Acceptable numb Bright dot spac	er : 4 ing : 5	brigh mm or	t dots defects more		
glass)		Black dot def	ect	Acceptable number : 5 black dots defects Black dot spacing : 5 mm or more					
		2 dots join	Bright dot defect	Acceptable number : 2					
			Black dot defect	Acceptable numb	er : 3				
		3 or more dot	s join	Acceptable numb	er : 0				
		Total dot def	ects	Acceptable numb	er : 5	Max			
	Others	White dot, Da	rk dot	Sizo(m		٨	acontoble Number		
		(Circle)		512e (iiii	0.2	A	(poglocted)		
				$\begin{array}{c c} & d \\ \hline & 0 & 2 \leq d \\ \hline \end{array}$	0.2		5		
				0.2 < d \le 0	0.5		3		
				0.5 <d< td=""><td></td><td></td><td>0</td></d<>			0		
							-		
External ins	pection	Polarizer(Scr	atches)	Width(mm)	Length(m	nm)	Acceptable Number		
(Defect o Polarize	n r or			₩≦0.1	-		(neglected)		
between -er and	Polariz LCD				L≦5.0		(neglected)		
glass)				$0.1 < W \le 0.3$	5.0 <l< td=""><td></td><td>0</td></l<>		0		
				0.3 <w -<="" td=""><td colspan="2">0</td></w>			0		
		Polarizer Touch panel (Bubble, Dent)							
				Size(mm)		Acceptable Number			
				$\begin{array}{c c} d < 0.2 \\ \hline 0.2 < d \leq 0.3 \\ \hline 0.3 < d \leq 0.5 \\ \hline 0.5 < d \end{array}$			(neglected) 5 3		
							0		
		Foreign Parti	cle(Circular			1			
		shape)	,	Size(mn	n)	A	acceptable Number		
				d <	0.2	(neglected)			
				0.2 <d≦< td=""><td>0.4</td><td></td><td>5</td></d≦<>	0.4		5		
				0.4 <d≦< td=""><td>0.5</td><td></td><td>3</td></d≦<>	0.5		3		
				0.5 <d< td=""><td></td><td></td><td>0</td></d<>			0		
		Foreign Parti	cle						
		(Linear shape Scratches),	Width(mm)	Length(n	nm)	Acceptable Number		
				₩≦0.03	-	0.0	(neglected)		
						2.0	(neglected)		
				$0.03 \le W \ge 0.1$	$2.0 < L \le 4.0$		3		
				0.1 <w< td=""><td>4. U \ L</td><td></td><td>(According to</td></w<>	4. U \ L		(According to		
							offourar Shape)		

Classification	Inspection item		Judgement stand	lard		
Touch Screen portion	Scratch	Width(mm)	Length(mm)	Acceptable number		
		W<0.05		neglected		
		$0.05 \le W < 0.10$	10 < L	3		
		$0.10 \leq W$		0		
				·		
	Glass crack (Corner crack)	A Z				
			Y OV C D			
		$0K \leq 3 \qquad 0K \leq 3 \qquad 0K \leq t$				
		 If one of X, Y, Z is not satisfied, it is regarded as NG. Regarding the corner crack, within 0.5 mm depth is regarded as OK. (t= thickness of Touch panel) 				
	Glass crack (Cracks in other area than in corner)			Z		
		Х	Y	Z		
		OK ≦ 3	$OK \leq 3$	$OK \leq t$		
		•If one of X,Y,Z as NG. •Regarding the c is regarded as	is not satisfi orner crack, wi OK. (t= thickr	ed, it is regarded thin 0.5 mm depth less of Touch panel)		