SPEC

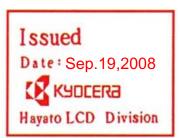
Spec No.	TQ3C-8EAF0-E1DEG23-00
Date	September 16, 2008

## TYPE: TCG057QVLBB-H50

< 5.7 inch QVGA transmissive color TFT with LED backlight and touch panel >

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KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by: l	Engineering dep	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
September 16,2008	y deano	H.Tokumeri	G. Matsumoto	J. Sakaguchi	S. Heyoshir

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# Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

#### Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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## Revision record

Date		Design	Engineering of		Confirmed by	: QA dept.
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# 1. Application

This document defines the specification of TCG057QVLBB-H50. (RoHS Compliant)

#### 2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Glare Low-reflection treatment

Additional circuit : Timing controller, Power supply (3.3V input)

(without constant current circuit for LED backlight)

Touch panel : Analog type, Non-Glare treatment

#### 3. Mechanical specifications

#### 3-1. Mechanical specifications of LCD

Item	Specification	
Outline dimensions	127.2 (W)× 100.4 (H) ×6.75 (D)	
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	
Dot format	$320 \times (B,G,R) (W) \times 240 (H)$ d	
Dot pitch	0.12 (W) × 0.36 (H)	
Base color *1	Normally White	-
Mass	(TBD)	g

<sup>\*1</sup> Due to the characteristics of the LCD material, the color varies with environmental temperature.

# 3-2. Mechanical specifications of touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation Force	0.05 ~ 0.8	N
Transmittance	Typ. 79	%
Surface hardness	Pencil hardness 2H or more according	-
Anti newton's ring treatment	None	-



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### 4. Absolute maximum ratings

#### 4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	0	4.0	V
Input signal voltage *1	$V_{\rm IN}$	-0.3	6.0	V
LED forward current *2	IF	-	30	mA
Reversed voltage *2	VR	-	5	V
Supply voltage for touch panel	$V_{\mathrm{TP}}$	0	6.0	V
Input current of touch panel	${ m I}_{ m TP}$	0	0.5	mA

<sup>\*1</sup> Input signal: CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, Hsync, Vsync, ENAB, R/L, U/D

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	Тор	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	*2	Tsto	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	*3	$H_{\mathrm{OP}}$	10	*4	%RH
Storage humidity	*3	Нѕто	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

<sup>\*1</sup> Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

\*5

Frequency	10 ~ 55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total)

**EIAJ ED-2531** 

\*6 Acceleration: 490 m/s², Pulse width: 11 ms

3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ 

**EIAJ ED-2531** 



<sup>\*2</sup> For each "AN1-CA1", "AN2-CA2", "AN3-CA3" and "AN4-CA4" Temp. = 25°C

<sup>\*2</sup> Temp. = -30°C < 48h, Temp. = 80°C < 168h

<sup>\*3</sup> Non-condensing

<sup>\*4</sup> Temp. 40°C, 85%RH Max.

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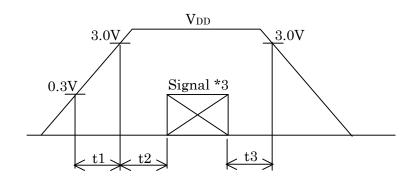
## 5. Electrical characteristics

#### 5-1. LCD

Temp. =  $-20 \sim 70$ °C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic *1	$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption for logic	${ m I}_{ m DD}$	*2	-	60	80	mA
Permissive input ripple voltage	$V_{\mathrm{RP}}$	-	-	-	100	mVp-p
I	$V_{\mathrm{IL}}$	"Low" level	0	-	$0.3V_{\mathrm{DD}}$	V
Input signal voltage *3	V <sub>IH</sub>	"High" level	$0.7V_{\mathrm{DD}}$	-	$V_{ m DD}$	V

## \*1 $V_{\rm DD}$ -turn-on conditions



0 < t1 20ms

0 < t2 50ms

0 < t3 1s

\*2 Display pattern:

$$V_{DD}$$
 = 3.3V, Temp. = 25°C

1 2 3

:

239

240 (dot)

\*3 Input signal : CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, Hsync, Vsync, ENAB, R/L, U/D

#### 5-2. Touch panel

Item	Specification		
Supply voltage for touch panel	5.0V		
m	$xL \sim xR : 200\Omega \sim 1,000\Omega$		
Terminal resistance	$yU \sim yL : 200\Omega \sim 800\Omega$		
Linearity	less than ±2.0%		
Insulation resistance	$100 \mathrm{M}\Omega$ or more at DC25V		



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# 6. Optical characteristics

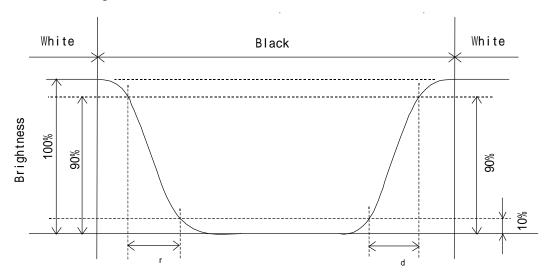
Measuring spot = 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D	Rise	τr	= =0°	-	10	-	ms
Response time	Down	τd	= =0°	-	25	-	ms
T7 1		UPPER		-	80	-	1
Viewing angle View direction	_	LOWER	CR 5	-	80	-	deg.
: 12 o'clo		LEFT	CK 5	-	80	-	1
(Gray in	version)	φ right		-	80	-	deg.
Contrast ratio		CR	= =0°	300	500	-	-
Brightness	Brightness		IF=15mA/Line	230	330	-	cd/m²
	Red	x	= =0°	0.57	0.62	0.67	
		У	0	0.32	0.37	0.42	
	C	x	= =0°	0.28	0.33	0.38	
Chromaticity	Green	У	0	0.54	0.59	0.64	
coordinates	Dl	x	= =0°	0.09	0.14	0.19	-
	Blue	У	0	0.04	0.09	0.14	
	<b>VX71</b> - '4 -	x	= =0°	0.28	0.33	0.38	
	White	У	<b>– –</b> 0°	0.30	0.35	0.40	

## 6-1. Definition of contrast ratio

CR(Contrast ratio) = Brightness with all pixels "White"
Brightness with all pixels "Black"

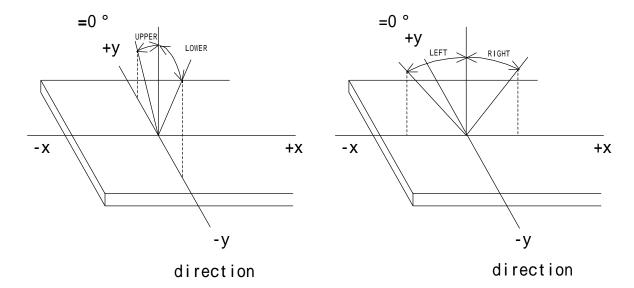
# 6-2. Definition of response time



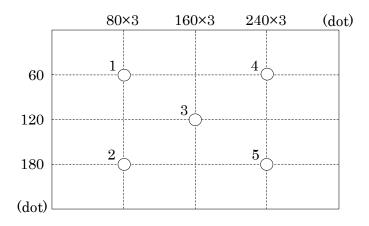


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# 6-3. Definition of viewing angle



## 6-4. Brightness measuring points



- 1) Rating is defined on the average in the viewing area.
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)



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# 7. Interface signals

# 7-1. Pin assignment of LCD panel and LED $\,$

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	H <sub>SYNC</sub>	Horizontal synchronous signal (negative)	I	
4	V <sub>SYNC</sub>	Vertical synchronous signal (negative)	I	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	$V_{ m DD}$	3.3V power supply	-	
29	$V_{ m DD}$	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L : Normal , H : Left / Right reverse mode	I	*2
31	U/D	Vertical display mode select signal H: Normal, L: Up / Down reverse mode	I	*2
32	NC	No connect	-	
33	CA1	Cathode 1	-	
34	CA2	Cathode 2	-	
35	CA3	Cathode 3	-	
36	CA4	Cathode 4	-	
37	AN1	Anode 1	-	
38	AN2	Anode 2	-	
39	AN3	Anode 3	-	
40	AN4	Anode 4	-	

LCD connector : IMSA-9681S-40A-GF (IRISO)

Recommended matching FFC or FPC : 0.5mm pitch

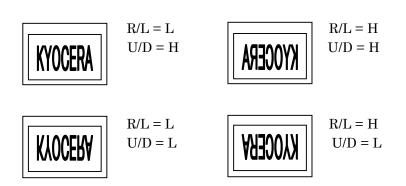


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\*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined.

Don't keep ENAB "High" during operation.

\*2



## 7-2. Pin assignment of touch panel

No.	Symbol	Description
1	уL	y-Lower terminal
2	xL	x-Left terminal
3	уU	y-Upper terminal
4	xR	x-Right terminal

Touch panel side connector : 1.25mm pitch

Recommended matching connector : Series FE, FFS (JST)

: KCA-K4R (DMC)



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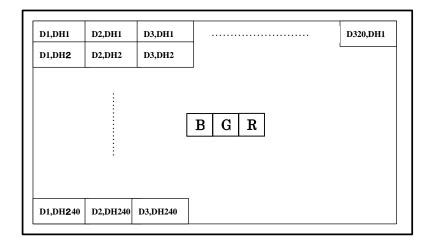
# 8. Input timing characteristics

#### 8-1. Timing characteristics

	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clock	Frequency	1/Tc	-	6.3	7.0	MHz	
Clock	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	12	-	-	ns	
Data	Hold time	Tdh	12	-	ı	ns	
	Cyalo	TH	50.0	63.6	-	μs	
	Cycle	1П	360	400	450	clock	
Horizontal sync. signal	Pulse width	ТНр	5	30	ı	clock	
Signar	Set up time	THs	12	-	-	ns	
	Hold time	THh	12	-	ı	ns	
	Cycle	TV	251	262	280	line	
Vertical sync.	Pulse width	TVp	1	3	5	line	
signal	Set up time	TVs	12	-	-	ns	
	Hold time	TVh	12	-	ı	ns	
	Pulse width	ТЕр		320		clock	
Enable signal (ENAB)	Set up time	TEs	12	-	-	ns	
(Ertilb)	Hold time	TEh	12	-	-	ns	
H <sub>SYNC</sub> - Enable signal phase difference		THE	36	68	88	clock	
Vertical sync. signal start position		TVE	2	18	38	line	
Horizontal display period		THd	320			clock	
Vertical display	period	TVd	240		line		

<sup>\*</sup>When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock) as shown in 8-3.

# 8-2. Input Data Signals and Display position on the screen

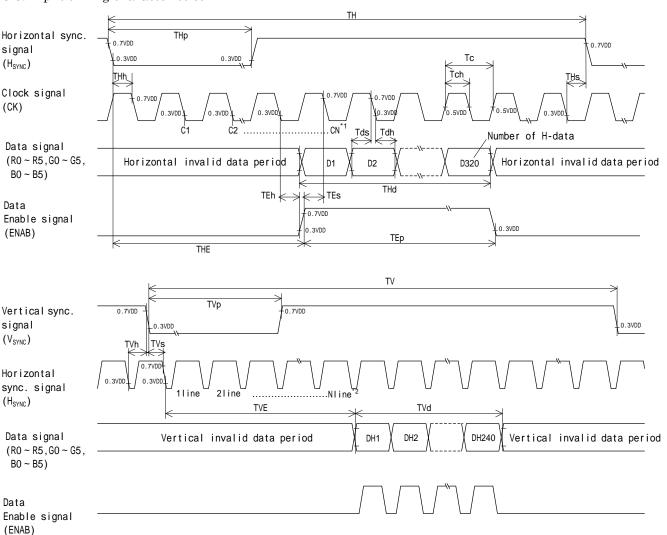




<sup>\*</sup>When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line) as shown in 8-3.

<sup>\*</sup>In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

### 8-3. Input timing characteristics



- \*1 When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock).
- \*2 When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line).

# 9. Backlight characteristics

LED ratings

LED latings							
Item		Symbol	Min.	Тур.	Max.	Unit	Note
Forward current	*1	IF	-	15	-	mA	Ta=-20 ~ 70°C
			1	22.1	25.0	V	IF=15mA, Ta=-20
Forward voltage	*1	VF	1	21.7	24.5	V	IF=15mA, Ta=25
			-	21.3	24.1	V	IF=15mA, Ta=70
Operating life time	*2, *3	Т	-	37,000	-	h	IF=15mA, Ta=25

<sup>\*1</sup> For each "AN1-CA1", "AN2-CA2", "AN3-CA3" and "AN4-CA4"

<sup>\*</sup> An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.



<sup>\*2</sup> When brightness decrease 50% of initial brightness.

<sup>\*3</sup> Life time is estimated data. .(Condition: IF=15mA, Ta=25 in chamber).

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# 10. Design guidance for analog touch panel (T/P)

10-1 Electrical (In customer's design, please remember the following considerations.)

- 1 Do not use the current regulated circuit.
- 2 Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maxim um ratings" for details.)
- 3 Analog T/P can not sense two points touching separately.
- 4 A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the T/P position data.
- 5 Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

#### 10-2 Software

- 1 Do the "User Calibration".
- 2 "User Caribration" may be needed with long term using. Include "User Caribration" menu in your software.
- 3 When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

#### 10-3 Mounting on display and housing bezel

- 1 Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- 2 Never expand the T/P top layer (PET-film) like a balloon by internal air pressure. The life of the T/P will be extremely short.
- 3 If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.



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#### 11. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2008	2009	2010	2011	2012	2013
Code	8	9	0	1	2	3

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

	Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Ī	Code	7	8	9	X	Y	Z

#### 12. Warranty

# 12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 12-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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#### 13. Precautions for use

#### 13-1. Installation of the LCD

- 1) The LCD shall be installed so that there is no pressure on the LSI chips.
- 2) The LCD shall be installed flat, without twisting or bending.
- 3) Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.

#### 13-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

#### 13-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

#### 13-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

#### 13-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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# 14. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	<ul><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul>
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	<ul><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul>
High temp. operation	70°C	500h	Display function Display quality Current consumption	<ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>
Point Activation life	Polyacetal stylus (R0.8) Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	<ul><li>No defect</li><li>No defect</li><li>No defect</li><li>No defect</li></ul>

<sup>\*</sup> Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

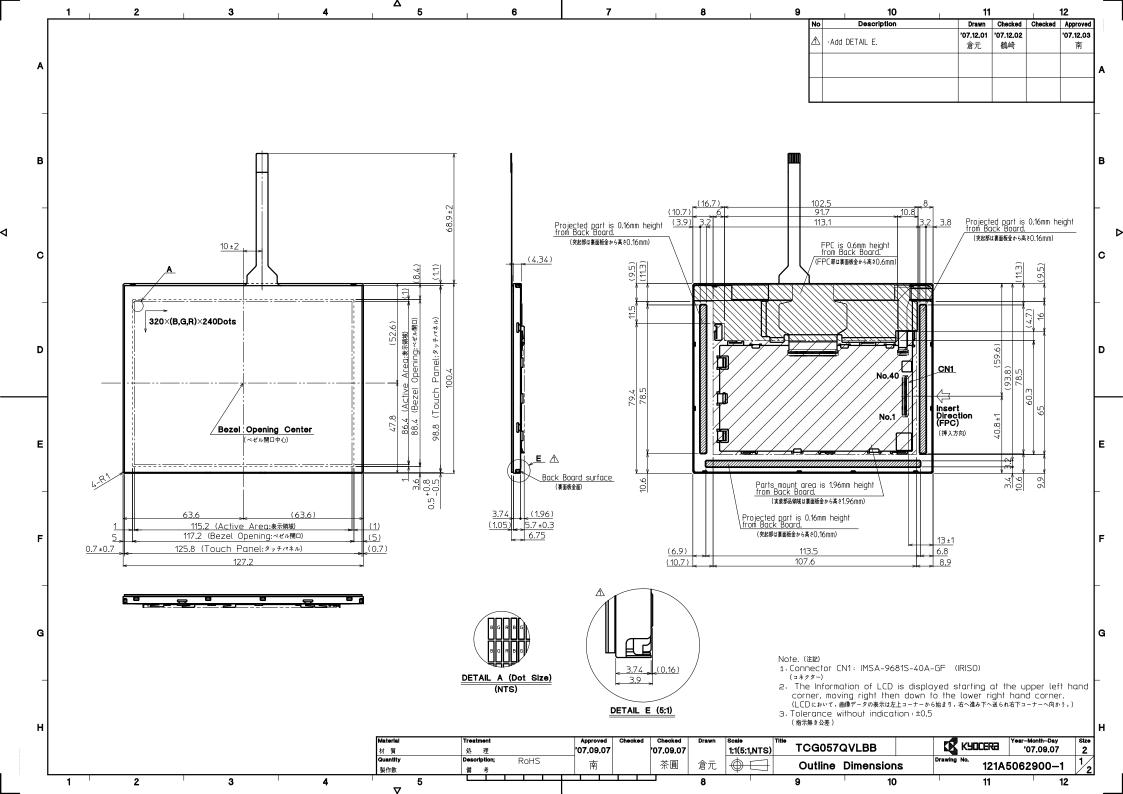


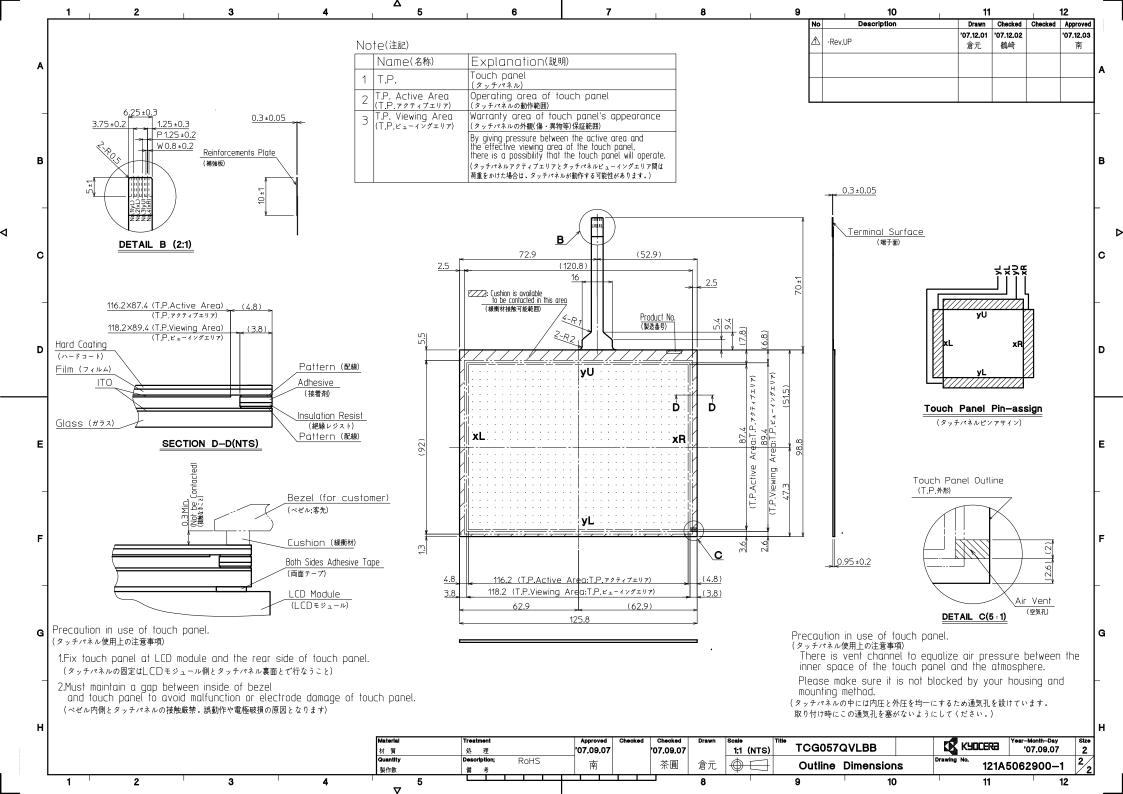
<sup>\*</sup> The LCD is tested in circumstances in which there is no condensation.

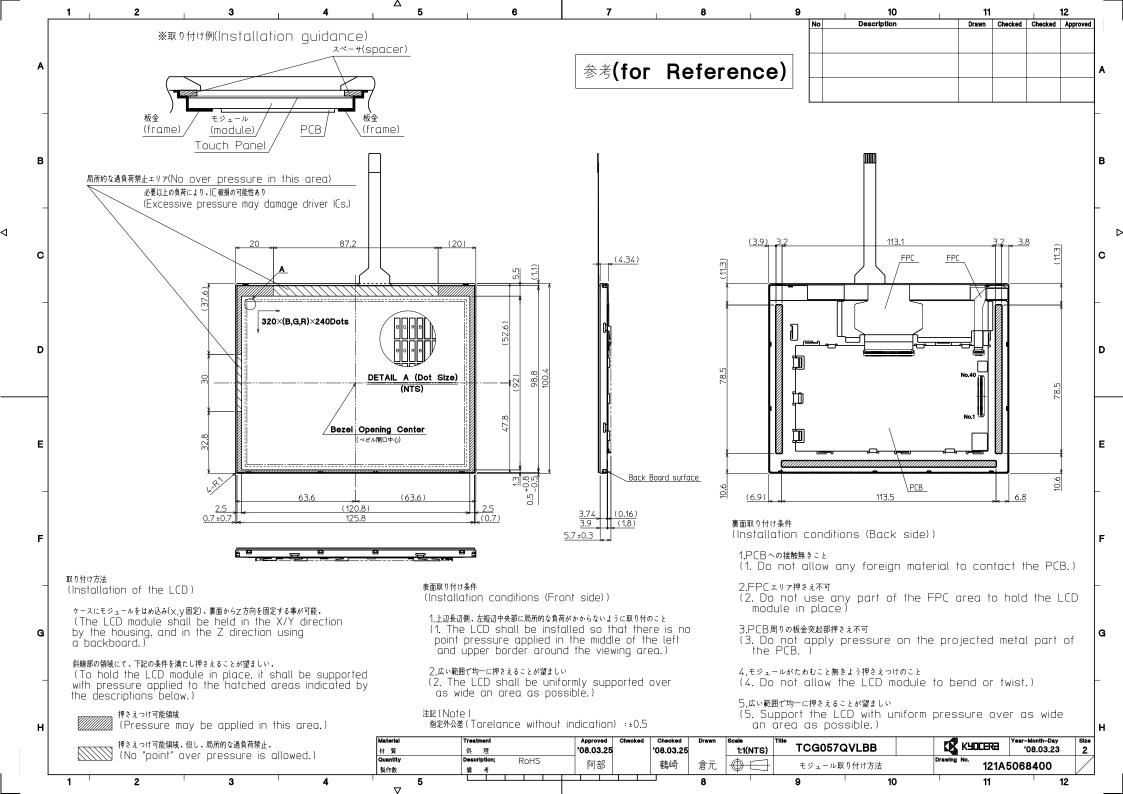
<sup>\*</sup> The reliability test is not an out-going inspection.

<sup>\*</sup> The result of the reliability test is for your reference purpose only.

The reliability test is conducted only to examine the LCD's capability.







Spec No.	TQ3C-8EAF0-E2DEG23-00
Date	September 16, 2008

# KYOCERA INSPECTION STANDARD

 ${\bf TYPE: TCG057QVLBB-H50}$ 

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by:	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
September 16,2008	y drano	H.Tokumuri	G. Matriemoto	J. Sakaguchi	S. Hejosko



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# Revision record

Date		Designed by : Eng		Engineering of	ngineering dept.		Confirmed by : QA dept.	
		Prepa	red	Checked	Approved	Checked	Approved	
Rev.No.	Date	Page			Description	ons		



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# Visuals specification

# 1) Note

1) Note			Note					
General	1. Custom	not defined within this inspection standard shall be						
General	reviewed by Kyocera, and an additional standard shall be determined by mutual							
	consent.							
	consent.  2. This inspection standard about the image quality shall be applied to any defect within							
		=						
	the active area and shall not be applicable to outside of the area.							
	3. Inspection conditions							
	Lumina		: 500 Lux min.					
		ion distance	: 300 mm.					
	Temper		$:25 \pm 5$					
	Direction	on	: Directly above					
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the					
inspection item			LCD, even when all "Black" data sent to the screen.					
			Inspection tool: 5% Transparency neutral density filter.					
			Count dot: If the dot is visible through the filter.					
			Don't count dot: If the dot is not visible through the					
			filter.					
			RGBRGB					
			R G B R G B R G B dot defect					
			RGBRGBRGB United					
		Black dot defect	The dot is constantly "off" when power applied to the					
			LCD, even when all "White" data sent to the screen.					
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot					
			defects or black dot defects.					
			RGBRGB					
			R G B R G B R G B					
			R G B R G B B G B dot defect					
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non					
	inspection	Foreign particle	operating.					
		(Polarizer, Cell,						
		Backlight)						
		Appearance	Does not satisfy the value at the spec.					
		inspection						
	Others	LED wire	Damaged to the LED wires, connector, pin, functional					
			failure or appearance failure.					
	Definition	Definition of	circle size Definition of linear size					
	of size							
		<del>                                  </del>						
		<mark>∗ ª</mark>	►l					
	d = (a + b)/2							



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#### 2) Standard

2) Standa		_						
Classification		Inspection item		Judgement standard				
Defect	Dot	Bright dot defect		Acceptable number : 4				
(in LCD			Bright dot spacing			or more		
glass)		Black dot defect		Acceptable number : 5				
				Bright dot spacing : 5 mm or more			or more	
			Bright dot	Acceptable number : 2				
			defect	*				
			Black dot defect	Acceptable number		: 3		
		3 or more	dots join	Acceptable number : 0				
		Total dot d	lefects	Acceptable number : 5 Max			X	
	Others	White dot,	Dark dot					
		(Circle)		Size (mm	<u>1</u> )	Ac	ceptable number	
				d 0.2			(Neglected)	
				0.2 < d	0.4		5	
				0.4 < d	0.5		3	
				0.5 < d			0	
External	inspection	Polarizer (	Scratch)					
(Defect on		,		Width (mm)	Length (	mm)	Acceptable number	
Polarizer	or			W 0.1	-		(Neglected)	
between I	Polarizer			0.1 < W 0.3	L 5.0		(Neglected)	
and LCD				0.1 \ W 0.5	5.0 < L		0	
and LOD glassy				0.3 < W	-		0	
		Polarizer (	Bubble)					
				Size (mm)		Acceptable number		
				d 0.2		(Neglected)		
				0.2 < d 0.3		5		
				0.3 < d 0.5		3		
				0.5 < d			0	
		Foreign pa	ırticle					
		( Circular shape )		Size (mm)		Ac	ceptable number	
				d 0.2		(Neglected)		
				0.2 < d 0.4		5		
				0.4 < d 0.5		3		
				0.5 < d		0		
		Foreign pa	rticle					
		(Linear shape) Scratch		Width (mm)	Length	Length (mm) Accepta		
				W 0.03		/	(Neglected)	
					L	2.0	(Neglected)	
				0.03 < W 0.1	2.0 < L	4.0	3	
					4.0 < L		0	
				0.1 < W	-		(According to	
							circular shape)	



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	1						
Inspection item	Judgement standard						
Scratch,	( W = Width, L = Length, D = Diameter = (major axis + minor axis)/ 2)						
Foreign particle	Item	Width(mm)	Acceptable number				
(Touch screen		d 0.03	L 20		Neglected		
portion)	Comptal	0.03 < d 0.05	L 10	2pce	s within φ20mm		
	Scratch	0.05 < d 0.08	L 6	2pce	s within φ20mm		
		0.08 < d 0.1	L 4	1pce	s within φ30mm		
	Foreign	W 0.05	Neglected		Neglected		
	( line like )	0.05 < W  0.1	L 5	2pces	s within 30mm		
	Foreign	D	0.2		Neglected		
	( circle like )	0.2 < D	0.3	2pces	s within 30mm		
	Above are applied	d to the visible area.					
	Unless there ar	re foreign particle and o	damage affected	seriou	sly to the electrical		
	performance out	of the active area, we appro	ove of this produc	et.			
Glass crack	T.	Size (m	,		Acceptable		
(Touch screen	Item	Size (m		number			
portion)			X	3			
	Conner crack	/	2/ "		0		
		XX X / /	Y	3	2 pcs		
					/panel		
		Z					
	G 1.	>/v	_ X	5			
	Crack in other area than in	× * * * * * * * * * * * * * * * * * * *	\$   <u>^</u>		2 pcs		
			Y	Y 1.5 /side			
					/side		
	corner	3	Z	< t			
		· / <del>†</del>	1 1				
	Progressive	_		0 pcs			
	crack			(NG even 1pcs)			
	Crack	$\overline{}$			(IVG even Tpcs)		
				- ,			
		• 10 10 10 10 10 10 10 10					
		d to the visible area.					
	Unless there are foreign particle and damage affected seriously to the el						
	performance out	of the active area, we appr	ove of this produ	ct.			
Newton's ring	Neglected.						
	Newton's ring						

