SPEC

Spec No.	TQ3C-8EAF0-E1DEG60-00
Date	April 14, 2009

## TYPE: TCG057QVLCL-G00

< 5.7 inch QVGA transmissive color TFT with LED backlight and constant current circuit for LED backlight>

#### **CONTENTS**

- 1. Application
- 2. Construction and outline
- 3. Mechanical specifications
- 4. Absolute maximum ratings
- 5. Electrical characteristics
- 6. Optical characteristics
- 7. Interface signals
- 8. Input timing characteristics
- 9. Backlight characteristics
- 10. Lot number identification
- 11. Warranty
- 12. Precautions for use
- 13. Reliability test data
- 14. Outline drawing



KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by:	Engineering de	pt.	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved	
April 14, 2009	S. Kajima	H-Tokumeri	G Matricmoto	J. Sakaguchi	To Sul	

Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	-

# Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

#### Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	-

## Revision record

Date		Design	Engineering of		Confirmed by	: QA dept.
		Prepa	Checked	Approved	Checked	Approved
<del></del>				T		
Rev.No.	Date	Page		Descripti	ons	



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	1

# 1. Application

This document defines the specification of TCG057QVLCL-G00. (RoHS Compliant)

#### 2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Timing controller, Power supply (3.3V input)

(with constant current circuit for LED Backlight)

## 3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	144(W)×(104.8)(H)×12.7(D)	mm
Active area	115.2(W)×86.4(H) (14.4cm/5.7 inch(Diagonal))	mm
Dot format	320×(B,G,R)(W)×240(H)	dot
Dot pitch	0.12(W)×0.36(H)	mm
Base color 2)	Normally White	-
Mass	180	g

- 1) Projection not included. Please refer to outline for details.
- 2) Due to the characteristics of the LCD material, the color varies with environmental temperature.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	2

#### 4. Absolute maximum ratings

#### 4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	0	4.0	V
Input signal voltage 1)	$V_{\rm IN}$	-0.3	6.0	V
Supply voltage for backlight	$V_{\rm IN}B$	0	6.0	V
Backlight ON-OFF	BLEN	0	$V_{\mathrm{IN}}\mathrm{B}$	V
Brightness adjust voltage	VBRT	0	$V_{\mathrm{IN}}\mathrm{B}$	V

1) Input signal: CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, H<sub>SYNC</sub>, V<sub>SYNC</sub>, ENAB, R/L, U/D

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	$T_{OP}$	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	2)	Tsto	-30	80	°C
Operating humidity	3)	Нор	10	4)	%RH
Storage humidity	3)	$H_{\mathrm{STO}}$	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Temp. = -30°C < 48h, Temp. = 80°C < 168h Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)
- 3) Non-condensing
- 4) Temp.  $40^{\circ}$ C, 85%RH Max. Temp.  $> 40^{\circ}$ C, Absolute humidity shall be less than 85%RH at  $40^{\circ}$ C.

5)

Frequency	10 ~ 55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

6) Acceleration: 490 m/s², Pulse width: 11 ms 3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$  EIAJ ED-2531



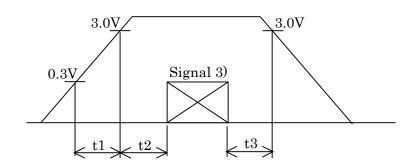
Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60	-00 TCG057QVLCL-G00	3

## 5. Electrical characteristics

Temp. =  $-20 \sim 70$ °C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption	$I_{ m DD}$	2)	-	60	80	mA
Permissive input ripple voltage	$V_{\mathrm{RP}}$	-	-	-	100	mVp-p
Innut signal maltage 2)	$ m V_{IL}$	"Low" level	0	•	$0.3V_{\mathrm{DD}}$	V
Input signal voltage 3)	$V_{\mathrm{IH}}$	"High" level	$0.7 V_{\mathrm{DD}}$	-	+5.5	V

## 1) $V_{DD}$ -turn-on conditions



0 < t1 20ms

0 < t2 50 ms

0 < t3 1s

2) Display pattern:

$$V_{DD} = 3.3V$$
, Temp. = 25°C

2

3

239

240 (dot)

3) Input signal : CK, R0 ~ R5, G0 ~ G5, B0 ~ B5,  $H_{SYNC}$ ,  $V_{SYNC}$ , ENAB, R/L, U/D



Ī	Spec No.	Part No.	Page
	TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	4

# 6. Optical characteristics

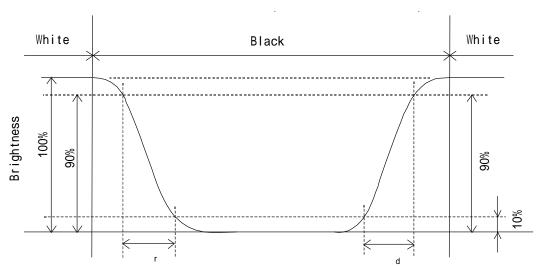
Measuring spot = 6.0mm, Temp. = 25°C

					0 1		1	
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
D	Rise	τг	= =0°	-	10	-	ms	
Response time	Down	τd	= =0°	-	25	-	ms	
77' ' 1		UPPER		-	80	-	1	
Viewing angle View direction	_	LOWER	CR 5	-	80	-	deg.	
: 12 o'clo		LEFT	Ch 9	-	80	-	1	
(Gray inversion)		φ right		-	80	-	deg.	
Contrast ratio		CR	= =0°	300	500	-	-	
Brightness		L	IL=25mA/Line	240	350	-	cd/m²	
	D a d	X	= =0°	0.57	0.62	0.67		
	Red	У	0	0.31	0.36	0.41		
	Consen	X	= =0°	0.29	0.34	0.39		
Chromaticity	Green	У	0	0.55	0.60	0.65		
coordinates	D1	X	= =0°	0.09	0.14	0.19	-	
	Blue	У	<b>− −</b> 0	0.05	0.10	0.15		
	White	X	= =0°	0.29	0.34	0.39		
	wnite	У	0	0.31	0.36	0.41		

## 6-1. Definition of contrast ratio

 $CR(Contrast ratio) = \frac{Brightness with all pixels "White"}{Brightness with all pixels "Black"}$ 

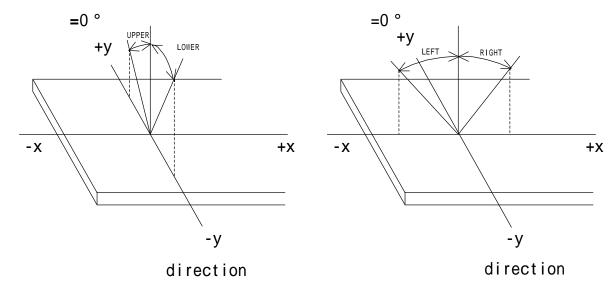
# 6-2. Definition of response time



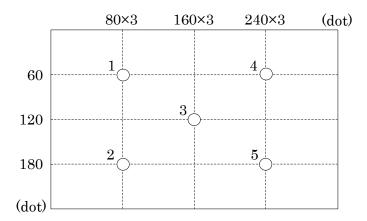


Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	5

# 6-3. Definition of viewing angle



## 6-4. Brightness measuring points



- 1) Rating is defined on the average in the viewing area. (measured point  $1\sim5$ )
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	6

# 7. Interface signals

## 7-1. LCD

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	Hsync	Horizontal synchronous signal (negative)	I	
4	$V_{ m SYNC}$	Vertical synchronous signal (negative)	I	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	1)
28	$V_{ m DD}$	3.3V power supply	-	
29	$V_{ m DD}$	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L: Normal, H: Left / Right reverse mode	I	2)
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	I	2)
32	NC	No connect	I	
33	GND	GND	-	

LCD connector : 08-6210-033-340-800+ (ELCO)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	7

1) The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined. Don't keep ENAB "High" during operation.

2)



R/L = LU/D = H



R/L = HU/D = H



$$R/L = L$$
  
 $U/D = L$ 



$$R/L = H$$
  
 $U/D = L$ 

## 7-2. Pin assignment of LED

No.	Symbol	Description			
1	$V_{\rm IN}B$				
2	$V_{\rm IN}B$				
3	$V_{\rm IN}B$				
4	$V_{\rm IN}B$				
5	$V_{\rm IN}B$	Supply voltage			
6	$V_{\mathrm{IN}}\mathrm{B}$				
7	$V_{\mathrm{IN}}\mathrm{B}$				
8	V <sub>IN</sub> B				
9	BLEN	Backlight ON-OFF(H:ON, L:OFF)			
10	VBRT	Brightness adjust voltage			
11	GND				
12	GND				
13	GND				
14	GND	GND			
15	GND				
16	GND				
17	GND				
18	GND				

LCD side connector : 08-6212-018-340-800+ (ELCO)

Recommended matching connector : 0.5mm pitch



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	8

# 8. Input timing characteristics

#### 8-1. Timing characteristics

	Item	Symbol	Min	Тур	Max	Unit	Note
Clarala	Frequency	1/Tc	-	6.3	7.0	m MHz	
Clock	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	-	-	ns	
Data	Hold time	Tdh	10	-	-	ns	
	Corolo	ТН	50.0	63.6	-	μs	
Horizontal sync. signal	Cycle	1 II	360	400	450	clock	
	Pulse width	ТНр	2	96	200	clock	
Vertical sync.	Cycle	TV	251	262	280	line	
signal	Pulse width	TVp	2	-	34	line	
Horizontal displa	ny period	THd	320			clock	
Hsync,-Clock pha	Hsync,-Clock phase difference		10	-	Tc-10	ns	
Hsync-Vsync. phase difference		TVh	Тс	-	ТН-ТНр	ns	
Vertical sync. signal start position		TVs	7			line	
Vertical display p	period	TVd		240		line	

1) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

## 8-2. Horizontal display position

Item		Symbol	Min	Тур	Max	Unit	Note
Enable signal	Set up time	Tes	5	-	Tc-10	ns	
Enable signal	Pulse width	Tep	2	320	TH-10	clock	
H <sub>SYNC</sub> – Enable signal phase difference		The	2	-	TH-340	clock	

- 1) When ENAB is fixed at "Low", the display starts from the data of C52(clock) as shown in 8-5.
- 2) The horizontal display position is determined by ENAB signal.

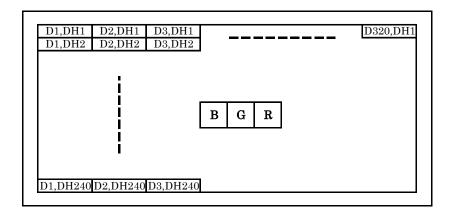
## 8-3. Vertical display position

- 1) The vertical display position (TVs) is 7th line.
- 2) ENAB signal is independent of vertical display position.

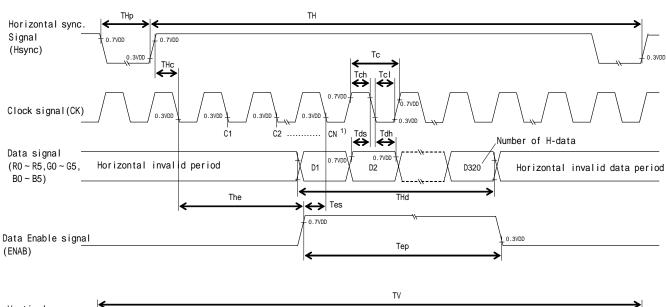


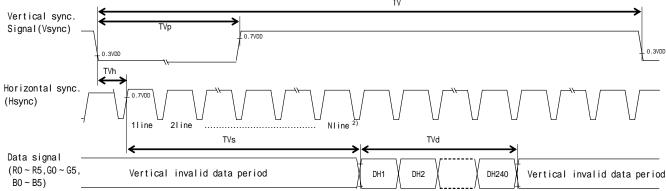
Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	9

## 8-4. Input Data Signals and Display position on the screen



## 8-5. Input timing characteristics





- 1) When ENAB is fixed at "Low", the display starts from the data of C52(Clock).
- 2) The vertical display position(TVs) is fixed at 7th line.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	${\tt TCG057QVLCL\text{-}G00}$	10

#### 9. Backlight characteristics

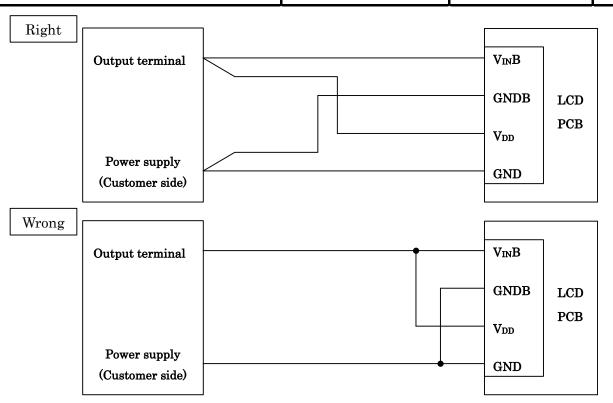
Temp.=25

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	$V_{\mathrm{IN}}\mathrm{B}$	3.0	-	5.5	V	Ta=-20 ~ 70
ON-OFF (H)	DIENI	$0.8 { m V_{IN} B}$	-	$V_{\mathrm{IN}}\mathrm{B}$	V	-
ON-OFF (L)	BLEN	0.0	-	$0.2 V_{\mathrm{IN}} \mathrm{B}$	V	-
LED forward current	IF	-	25.0	-	A	VBRT=0 ~ 1.4V
1) 2)	11	-	5.0	-	mA	VBRT=2.8V
Complex comment	ID	-	760	980	A	V <sub>IN</sub> B =3.3V, IF=25mA
Supply current	$I_{IN}B$	-	550	720	mA	V <sub>IN</sub> B =5.0V, IF=25mA
Operating life 3) 4)	Т	-	50,000	-	h	IF=25mA, Ta=25

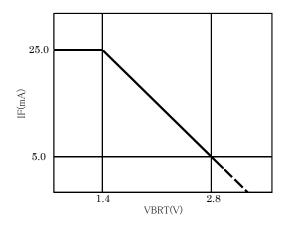
- 1) For each LED.
- 2) A forward current below 8.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.
- 3) When brightness decrease 50% of minimum brightness.
- 4) Life time is estimated data. (Condition: IF=25mA, Ta=25 in chamber).
- 5) When you start-up, please charge in sequence of  $V_{IN}B$ ->BLEN, or VBRT. When you shut-down, please stop in sequence of BLEN and/or VBRT-> $V_{IN}B$ .
- 6) Please do not connect the other than our backlight to this output connector on the PCB.
- 7) In case VDD and V<sub>IN</sub>B are supplied by a single power source, VDD & V<sub>IN</sub>B, and GND are connected directly and separately from the output on the power source. If the common wire are used for VDD & V<sub>IN</sub>B, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.



ſ	Spec No.	Part No.	Page
	TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	11



## 8) VBRT-IF characteristics





Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	12

#### 10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

	Year	2009	2010	2011	2012	2013	2014
Ī	Code	9	0	1	2	3	4

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

#### 11. Warranty

#### 11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	TCG057QVLCL-G00	13

#### 12. Precautions for use

#### 12-1. Installation of the LCD

- 1) The LCD has a grounding hole. Please ground the LCD to prevent noise and to stabilize its performance as circumstances demand.
- 2) A transparent protection plate shall be added to protect the LCD and its polarizer
- 3) The LCD shall be installed so that there is no pressure on the LSI chips.
- 4) The LCD shall be installed flat, without twisting or bending.
- 5) Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque: 0.32±0.03N· m

Please set up 'SPEED-LOW', 'SOFT START-SLOW' when using electric driver. Recommendable screw JIS tapping screw two types nominal dia.3.0mm installing boss hole depth  $3.5\pm0.5$ mm

Please be careful not to use high torque which may damage LCD in installation.

6) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

#### 12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

#### 12-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

#### 12-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

#### 12-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG60-00	${\tt TCG057QVLCL\text{-}G00}$	14

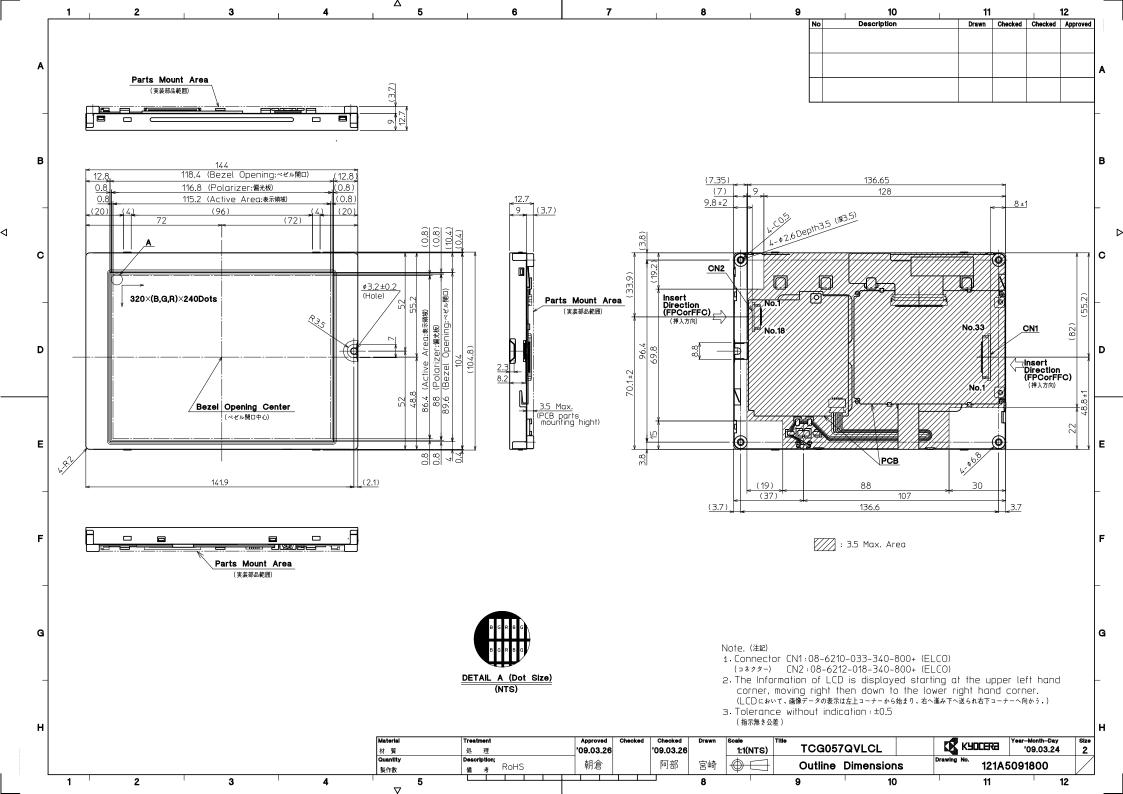
# 13. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	<ul><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul>
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	<ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

  The reliability test is conducted only to examine the LCD's capability.





Spec No.	TQ3C-8EAF0-E2DEG60-00
Date	April 14, 2009

# KYOCERA INSPECTION STANDARD

TYPE: TCG057QVLCL-G00

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by:	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
April 14, 2009	S. Kojima	H.Tokumeri	G. Matsumoto	J. Sakaguchi	Ho , Jul



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DEG60-00	${\tt TCG057QVLCL\text{-}G00}$	-

# Revision record

Date				Engineering dept.		Confirmed by : QA dept.		
		Prepared		Checked	Approved	Checked	Approved	
-		<del></del>						
Rev.No.	Date	Page			Descripti	ons		



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DEG60-00	TCG057QVLCL-G00	1

# Visuals specification

# 1) Note

1) Note			Note				
G 1	1 0	Note					
General	1. Customer identified anomalies not defined within this inspection standard shall be						
	reviewed by Kyocera, and an additional standard shall be determined						
	consent						
	the acti	the active area and shall not be applicable to outside of the area.					
	3. Inspection conditions						
	Lumina	ance	: 500 Lux min.				
	Inspect	ion distance	: 300 mm.				
	Temper	rature	$:25 \pm 5$				
	Direction	on	: Directly above				
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the				
inspection item			LCD, even when all "Black" data sent to the screen.				
			Inspection tool: 5% Transparency neutral density filter.				
			Count dot: If the dot is visible through the filter.				
			Don't count dot: If the dot is not visible through the				
			filter.				
			RGBRGB				
			R G B R G B R G B dot defect				
			RGBRGB				
		Black dot defect	The dot is constantly "off" when power applied to the				
			LCD, even when all "White" data sent to the screen.				
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot				
			defects or black dot defects.				
			RGBRGBRGB				
			R G B R G B R G B				
			R G B R G B B G B dot defect				
			Kalakalakala				
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non				
	inspection	Foreign particle	operating.				
		(Polarizer, Cell,					
		Backlight)					
		Appearance	Does not satisfy the value at the spec.				
		inspection					
	Others	LED wires	Damaged to the LED wires, connector, pin, functional				
			failure or appearance failure.				
	Definition	Definition of circle size Definition of linear size					
	of size						
		<del>                                  </del>					
		<del>                                     </del>					
		d =(a +	b)/2				



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DEG60-00	TCG057QVLCL-G00	2

#### 2) Standard

2) Standa	rd							
Classification Inspection item		Judgement standard						
Defect	Dot	Bright dot defect		Acceptable number : 4				
(in LCD	defect			Bright dot spacing : 5 mm or n			or more	
glass)		Black dot defect		Acceptable number : 5				
				Black dot spacing	ck dot spacing : 5 mm or more			
		2 dot join	Bright dot					
		defect		Acceptable number : 2				
			Black dot defect	Acceptable number		: 3		
		3 or more	dots join	Acceptable number		: 0		
		Total dot d	lefects	Acceptable number		: 5 Max		
	Others	White dot,	Dark dot					
		(Circle)		Size (mm	1)	Ac	ceptable number	
				d	0.2		(Neglected)	
				0.2 < d	0.4		5	
				0.4 < d	0.5		3	
				0.5 < d			0	
External	inspection	Polarizer (	Scratch)					
(Defect on				Width (mm)	Length (mm)		Acceptable number	
Polarizer				W 0.1	-		(Neglected)	
between I				0.1 < W 0.3	L	5.0	(Neglected)	
and LCD				0.1 \ W 0.3	5.0 < L		0	
				0.3 < W	-		0	
		Polarizer (	Bubble)					
				Size (mm)		Acceptable number		
				d	0.2	2 (Neglected)		
				0.2 < d	0.3		5	
				0.3 < d	0.5		3	
				0.5 < d			0	
		Foreign pa	ırticle					
		( Circular	shape)	Size (mm)		Acceptable number		
			d 0.2		(Neglected)			
			0.2 < d 0.4		5			
			0.4 < d 0.5		3			
		0.5 < d			0			
Foreign particle								
		( Linear shape )		Width (mm)	Length (mm)		Acceptable number	
Scratch		W 0.03 -		(Neglected)				
			L	2.0	(Neglected)			
				0.03 < W 0.1	2.0 < L	4.0	3	
				0.4	4.0 < L		0	
				0.1 < W	-		(According to	
							circular shape)	

