			SPEC.NO.	TQ3C-8EAF0	-E1DDP19-00
			DATE	February	15, 2007
SPEC					
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	ТҮРЕ:Т	C G 0 5 7 V 0	G 1 A C - G 5	0	
		CONTEN	r c		
1. Application					
2. Construction 3. Mechanical S					
4. Absolute Max 5. Electrical C	imum Ratings		_		
6. Optical Char	acteristics	5	Is	sued	
7. Interface Si 8. Timing Chara	cteristics of		s D	ate: Feb.16,20	07
9. Backlight Ch 10. Lot Number I			K	X KYDCERa	
11. Warranty 12. Precautions	for Use		Ha	ayato LCD Div	vision
13. Reliability 14. Outline Draw	Data / Enviro	nmental Test			
	Ū.		KYO	CERA CORPORAT	ION
			KAG	OSHIMA HAYATO DIVISION	
			200	DIVISION	
	ifing the state	aubia-t t			
	ification is yocera before		ange without	notice.	
Original	Designed by	:Engineering	Dept.	Confirmed by	:QA Dept.
Issue Date	Prepared	Checked	Approved	Checked	Approved
February 15, 2007	D. Ajisaka	J. Yomayohi	4- matsumo to	J. Şakaguchi	36. And

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

D		Design	ed by:	Engineering Dept. Confirmed by: QA Dept.			QA Dept.
Date		Prepa	red	Checked	Approved	Checked	Approved
Rev. No.	Date		Page		Descriptio	ns	

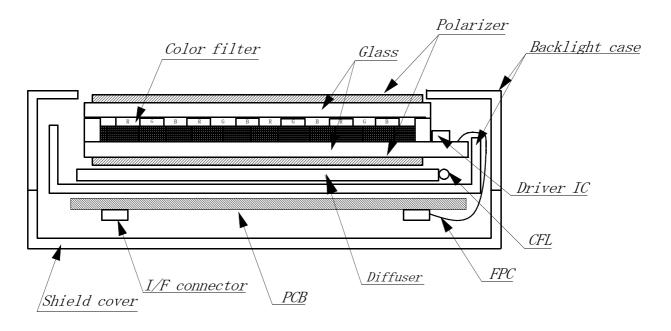
1. Application

This data sheet defines the specification for a $(640 \times R. G. B) \times 480$ dot, amorphous silicon TFT transmissive color dot matrix type Liquid Crystal Display with CFL backlight. [RoHS Compliant]

2. Construction and Outline

(640×R.G.B)×480 dots, COG type LCD with CFL backlight.
Backlight system : "U" figured type CFL (1 tube).
Inverter : Option.
Recommended Inverter : CXA-L0612A-VJL(TDK)
or Equivalent.
Polarizer : Glare Anti-reflection treatment.

Additional circuits : Timing controller, Power supply (3.3V input)



This drawing is showing conception only.

3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	144 (W) \times (104.8) (H) \times 13.0 (D)	mm
Effective viewing area	117.2 (W) \times 88.4 (H)	mm
Dot number	$(640 \times R. G. B)$ (W) \times 480 (H)	Dots
Dot pitch	0.06 (W) $ imes$ 0.18 (H)	mm
Display mode *1	Normally white	—
Mass	220	g

*1 Due to the characteristics of the LCD material, the color vary with environmental temperature.

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	Min.	Max.	UNIT
Power input voltage	VDD	0	4.0	V
Input signal voltage *1	Vin	-0.3	6.0	V

*1 Input signals : CK, R0 \sim R5, G0 \sim G5, B0 \sim B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

4-2. Environmental absolute maximum ratings

ITEM		SYMBOL	Min.	Max.	UNIT
Operating temperature *1		Тор	-10	70	°C
Storage temperature	*2	Tsto	-30	80	°C
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		_	*5	*5	_
Shock		_	*6	*6	—

*1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

*2 Temp. = -30 °C < 48 h , Temp = 80 °C < 168 h Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock. LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard. (Please refers to 12. Precautions for use as detail).

*3 Non-condensation.

*4 Temp. ≦ 40°C, 85%RH Max. Temp. > 40°C, Absolute Humidity shall be less than 85% RH at 40°C.

*5

Frequency	10~55 Hz	Converted to acceleration value :
Vibration width	0.15 mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10 Hz	1 minute

2 hours in each direction $\rm X/Y/Z$ (6 hours as total) EIAJ ED-2531

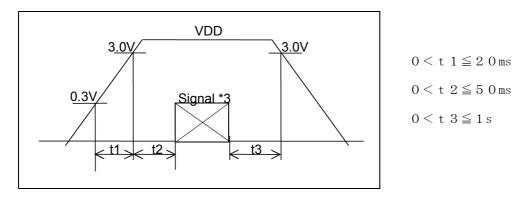
*6 Acceleration: $490m/s^2$ Pulse width : 11 ms 3 times in each direction : $\pm X/\pm Y/\pm Z$. EIAJ ED-2531

5. Electrical Characteristics

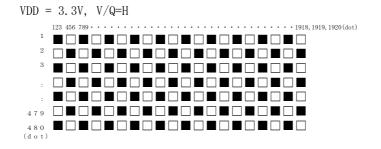
Temp. = $-10 \sim 70^{\circ}$ C

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	
Power input voltage *1	VDD	3.0	3.3	3.6	V	
Current consumption *2 VDD=3.3V Temp.=25°C		IDD	—	210	270	mA
Permissive input ripple v	Permissive input ripple voltage(VDD=3.3V)				100	mVp-p
Input signal voltage (L	VIL	0	-	0.3VDD	V	
Input signal voltage (H	Input signal voltage (High) *3			_	VDD	V

*1 VDD-turn-on conditions



*2 Power consumption Black & White pattern :



*3 Input signal : CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

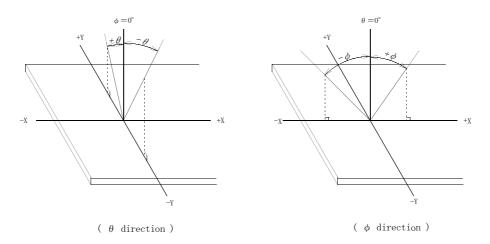
6. Optical Characteristics

Measuring points = ϕ 6.0mm , Temp. = 25° C

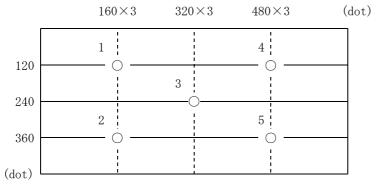
ITEM		SYMBOL	CONDI	TION	MIN	ТҮР	MAX	UNIT
Response	Rise	τ r $\theta = 0$		=0°	_	10	_	ms
time	Down	τ d	$\theta = \phi$	=0°		25	_	ms
		0		Upper	_	(80)	—	1
Vii		θ		Lower	_	(70)	—	deg.
Viewing angle	range	φ	$CR \ge 5$	Left	_	(80)	—	1
				Right	_	(80)	—	deg.
Contrast rati	Contrast ratio		$\theta = \phi = 0^{\circ}$		280	400	—	_
Brightness		L	IL=5.0mArms		(500)	(800)	—	cd/m^2
	Red	х	0	-0°	(0.557)	(0.607)	(0.657)	
		У	$\theta = \phi = 0^{\circ}$		(0. 293)	(0.343)	(0.393)	
	<u>C</u>	Х	$\theta = \phi$	-0°	(0. 253)	(0.303)	(0.353)	
Charactivita	Green	У	$\theta = \phi$	=0	(0. 495)	(0.545)	(0.595)	—
Chromaticity coordinates	ות	Х	0 - 1	-0°	(0.099)	(0.149)	(0.199)	
	Blue	У	$\theta = \phi$	-0	(0.070)	(0.120)	(0.170)	
		х		-0°	(0.261)	(0.311)	(0.361)	
	White	$\theta = \phi =$		-0	(0.268)	(0.318)	(0.368)	

6-1. Contrast ratio is defined as follows:

6-2. Definition of viewing angle



6-3. Measuring points



1) Rating is defined as the average brightness inside the viewing area.

2) 30 minutes after CFL is turned on. (Ambient Temp.=25°C)

 The inverter should meet the CFL rating condition. Sine, symmetric waveform without spike in positive and negative.

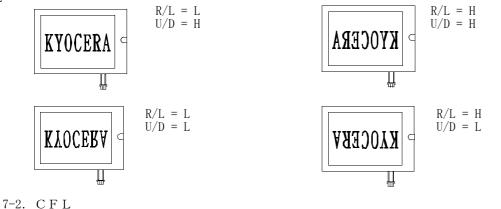
7-1. LCD	_			
PIN NO.	SYMBOL	DESCRIPTION	I/0	Note
1	GND	GND	-	
2	СК	Clock signal for sampling each data signal	Ι	
3	Hsync	Horizontal synchronous signal (negative)	Ι	
4	Vsync	Vertical synchronous signal (negative)	Ι	
5	GND	GND	-	
6	RO	RED data signal (LSB)	Ι	
7	R1	RED data signal	Ι	
8	R2	RED data signal	Ι	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND	-	
13	GO	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	Ι	
15	G2	GREEN data signal	Ι	
16	G3	GREEN data signal	Ι	
17	G4	GREEN data signal	Ι	
18	G5	GREEN data signal (MSB)	Ι	
19	GND	GND	-	
20	BO	BLUE data signal (LSB)	Ι	
21	B1	BLUE data signal	Ι	
22	B2	BLUE data signal	Ι	
23	B3	BLUE data signal	Ι	
24	B4	BLUE data signal	Ι	
25	B5	BLUE data signal (MSB)	Ι	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	Ι	*1
28	VDD	3.3V power supply	—	
29	VDD	3.3V power supply	-	
30	R/L	Horizontal display mode select signal	Ι	*2
		L : Normal , H : Left / Right reverse mode		
31	U/D	Vertical display mode select signal	Ι	*2
		H : Normal , L : Up / Down reverse mode		
32	V/Q	H : Normal	Ι	
33	GND	GND	-	
I CD conn	a a t a ta	08-6210-033-340-800+ (FLCO)		

7. Interface signals

LCD connector : 08-6210-033-340-800+ (ELCO) Recommended matching FFC or FPC: P = 0.5 mm

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2. Don't keep ENAB "High" during operation.





12.01									
PIN NO.	SYMBOL	DESCRIPTION							
1	HOT	Inverter output high voltage side							
2	NC	—							
3	COLD	Inverter output low voltage side							
LCD side	connector	: BHR-03VS-1 (JST)							
Recommend	led matching	connector : $SM02(8.0)B-BHS-1$ (JST)							

: SM02(8.0)B-BHS-1-TB(LF)(SN) (JST) · · · (RoHS Compliant)

* Please be careful NOT to connect inversely an inverter-output high voltage side to the CFL low voltage side. It may result in damage or electric shock.

8. Timing Characteristics of input signals

8-1. Timing characteristics

I	TEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Clock	Frequency	1/Tc	_	25. 18	28.33	MHz	V/Q=H
CIOCK	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	—	—	ns	
Data	Hold time	Tdh	10	—	—	ns	
Horizontal sync.	Cycle	TU	30.0	31.8	—	μs	V/0-U
signal		TH	770	800	900	clock	V/Q=H
	Pulse width	THp	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	V/Q=H
signal	Pulse width	TVp	2	—	34	line	
Horizontal displa	y period	THd	640			clock	
HsyncClock phas	se difference	THc	10	—	Tc-10	ns	
HsyncVsync. phase difference		TVh	0	—	TH-THp	ns	
Vertical sync.signal start position		TVs	34			line	V/Q=H
Vertical display	period	TVd		480		line	

*In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position

The horizontal display position is determined by ENAB signal.

ITEM		SYMBOL	MIN	ТҮР	MAX	UNIT	NOTE
F 11 1 1	Set up time	Tes	5		Tc-10	ns	
Enable signal	Pulse width	Тер	2	640	TH-10	clock	
HsyncEnable signal phase dif	ference	The	44	_	104	clock	V/Q=H

* When ENAB is fixed at "Low", the display starts from the data of C104 (clock) as shown in 8-5.

8-3. Vertical display position

The vertical display position (TVs) is fixed at 34th line. (V/Q=H) Note) ENAB signal is independent of vertical display position.

 D1,DH1
 D2,DH1
 D3,DH1

 D640,DH1

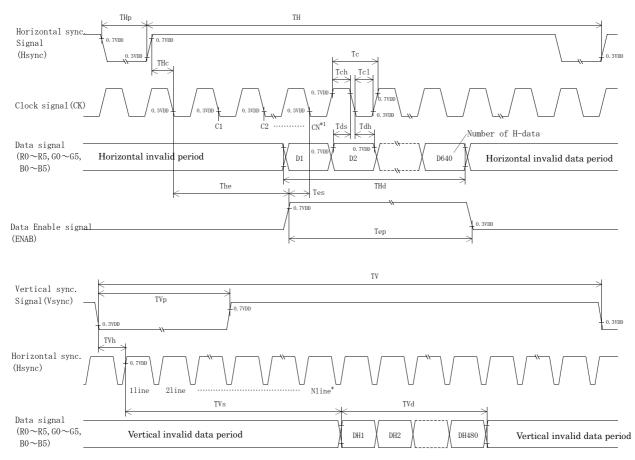
 D1,DH2
 D2,DH2
 D3,DH2

 Example
 Example

 Image: D1,DH2
 D2,DH2
 D3,DH2
 Image: D1,DH2
 Image: D1,DH2
 Image: D1,DH2
 Image: D1,DH480
 D3,DH480
 D3,DH480

8-4. Input Data Signals and Display position on the screen

8-5. Input Timing Characteristics



*1 When ENAB is fixed "V/Q=H" the display starts from the data of C104(Clock)

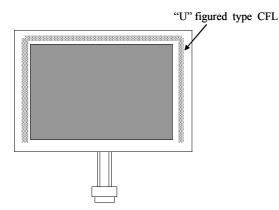
*2 The vertical display position(TVs) is fixed at $34^{\rm th}$ line.

9. Backlight Characteristics

Temp. = $25^{\circ}C$

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE	
Starting	VS			1,575 Vrms	0 °C	
discharge Voltage *1	٧S			1,050 Vrms	25 °C	
Discharging tube current *2	IL	(3.0) mArms	(5.0) mArms	(TBD) mArms	—	
Discharging tube voltage	VL	_	(655) Vrms	_	IL=5.0 mArms	
Operating life *3	Т	(50,000) h	(75,000) h	_	IL=5.0 mArms	
Operating frequency *4	F	30 kHz	_	100 kHz	—	

- *1 The Non-load output voltage (VS) of the inverter should be 1.3 times the maximum VS at the low temperature to provide margin to assure that the CFL will start, because actual VS may increase due to leakage current from the CFL cables. (Reference value: 2,015 Vrms Min)
- *2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.
- *3 End of life is defined as when the illuminance or quantity of light has decreased to 50% of the initial value. Illuminance of light will drastically decrease when LCD is operated at lower temperature for long hours.
- *4 The driving frequency of the CFL may interfere with the horizontal synchronous signal, leaving interference stripes on the display. So please evaluate LCD panels beforehand. To avoid interference stripes, we recommend to separate as far as possible the CFL frequency from the horizontal synchronous signal and its high harmonic frequency.
- * There may be cases where interface noise on LCD PCB, generated by high-voltage products such as inverters, may leave stripes on the display. Please be careful when designing a mold to take into consideration that the inverter shall be located as far as possible from PCB. Shield protection may be effective.
- * Prolonged storage in darkness and/or low temperature may slow the ignition and rise to full brightness of the CFL in an LCD Module. Please use an inverter designed to provide sufficient driving voltage for more than 1 second. Also a decreased Starting Discharge Voltage or shortened ignition time may not turn ON the CFL lamp.
- * CFL arrangement figure



1 O. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

Т	$\begin{array}{cccccccccccccccccccccccccccccccccccc$									
	①YEAR ②MONTH ③DATE ④Version Number ⑤Country of origin(Japan or China)									
	YEAR	2006	2007	2008	2009	2010	2011			
	CODE	6	7	8	9	0	1			
	MONTH	JAN.	FEB.	MAR.	APR.	MAY.	JUN.			
	CODE	1	2	3	4	5	6			
	MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.			
	CODE	7	8	9	Х	Y	Z			

1 1. Warranty

11-1. Incoming inspection

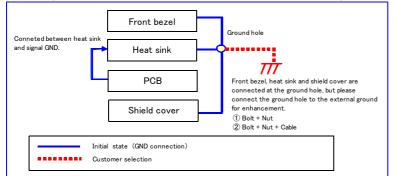
Please inspect the LCD within one month after your receipt.

11-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

1 2. Precautions for use

- 12-1. Installation of the LCD
- 1. The LCD's bezel must be grounded. The heat sink and shield cover are connected at the ground hole. The ground hole is located on the right side of the LCD when viewed from the front. The ground hole must be connected to an external ground.



- 2. A transparent protection sheet shall be added to protect the LCD and its polarizers.
- $\ensuremath{\mathsf{3.}}$ The LCD shall be installed so that there is no pressure on the LSI chips.
- 4. The LCD shall be installed flat, without twisting or bending.
- 5. The display window size should be the same as the effective viewing area.
- 6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 7. Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque:3.3±0.3kgf.cm Please set up'SPEED-LOW', 'SOFT START-SLOW' when using electric driver.
 - Recommendable screw P-TITE screw nominal dia.3.0mm
 - installing boss hole depth $3.5{\pm}0.5{\rm mm}$
- Please be careful not to use high torque which may damage LCD module in installation.
- 8. A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.
- 9. Do not pull the CFL lead wires and do not bend the root of the wires.
- Housing should be designed to protect CFL lead wires from external stress.
- 10. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

12-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

12-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- Adjust "LCD driving voltage" to obtain optimum viewing angle and contrast. It may also change the characteristics of the liquid crystal. <u>This phenomenon may not recover.</u> The LCD shall be operated within the temperature limits specified.

12-4. Storage

- 1. The LCD shall be stored within normal temperature and humidity.
- Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. Always store the LCD so that it is free from external pressure onto it.

12-5. Caution items when handling the LCD.

- 1. <u>DO NOT</u> store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
- 2. The front polarizer is easily scratched or damaged.
- Prevent touching it with any hard material, and from being pushed or rubbed. 3. The LCD screen may be cleaned with a soft cloth or cotton pad.
- Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed. 4. Water may cause damage or discoloration of the polarizer.
- Clean any condensation or moisture from any source immediately.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.
- 6. Do not disassemble LCD module because it will result in damage.
- 7. Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend to use screen saver etc. in cases where a solid-base image pattern must be used.
- 8. Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body, rinse it off right away with water and soap.

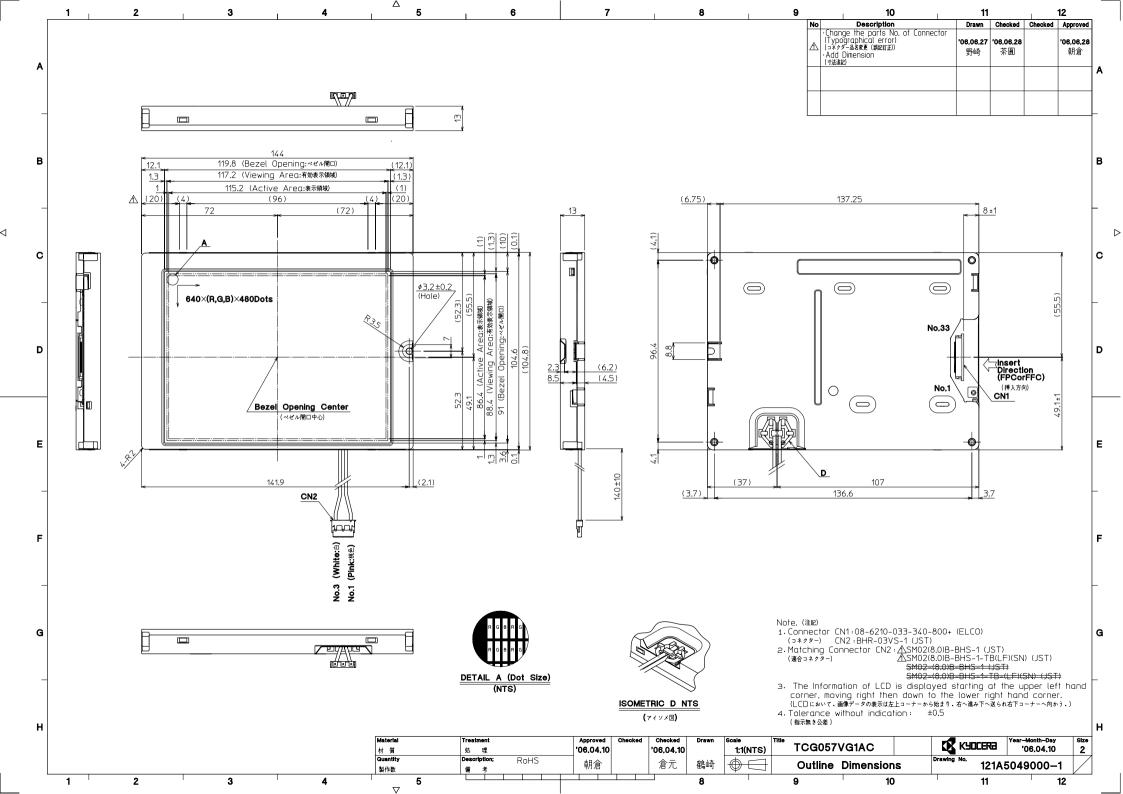
13. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	80℃	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	−30°C	240 h	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40°C 90%RH	240 h	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-30℃ 0.5 h R.T. 0.5 h 80℃ 0.5 h	10 cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	70℃	500 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect

 \ast Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

 \ast The LCD is tested in circumstances in which there is no condensation.

- * The reliability test is not an out-going inspection.
- * The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.



			SPEC.NO.	TQ3C-8EAF0	-E2DDP19-00
			DATE	February	15, 2007
	<u>FC</u>) <u>CERA</u> IN <u>TYPE</u> :				
	Designed	by :Engineer	KAG LCD	CERA CORPORAT OSHIMA HAYATC DIVISION Confirmed b	
Original		Chackad	Approved	Chackad	Approved
Original Issue Date February 15, 2007	Prepared D.Ajisaka	Checked Y. Yonoyohi	Approved Y: Matsunoto	Checked J. Sakaguchi	Approved

Revision Record

DateDesigned by: EngineerinDatePreparedChecked		Engineering D	ept.	QA Dept.			
		Prepa	red	Checked	Approved	Checked	Approved
Rev. No.	Date		Page		Descriptio	ns	

1) Note

	Note						
General	 Should any de additional st customer and Inspection Co Luminance Inspection d Temperature Direction 	nditions : 500 Lux minimum listance : 300 mm (from the sample)					
Definition of Inspection item	Dot defect	Bright dot	Defect constantl yappears bright, even in display of all "Black" pixels. Count : Visible though 5% transparency o f filter. No count : Not visible though 5% trans -parency of filter. RGBRGBRGB RGBRGBRGB RGBRGBRGB				
		Black dot	Defect constantly appears black, even in "White" pixels, Size is based on bright dot.				
		Two dots join	Dot join defect is defined as two or more dots which always display a matching brightness, even when each of them is set to different brightness value. R G B R G B R G B R G B R G B R G B R G B R G B R G B As the above shows, two dot join is defined as defects of two adjoing dots				
	External inspection	Bubble,Scratches, Foreign particle (Polarizer, Cell, Backlight)	like 'R' and 'G'. Visible operating (all pixcels "Black" or "White") and non operating.				
	Others	CFL lead wires	Damaged CFL lead wires, functional failu re, appearance failure.				
	Definition of size	Definition of cir a $d = \frac{(a+b)}{2}$	Definition of linear size				

2) Standard

Classification	Inspect	ion item	Judgement standard				
Dot defect	Dot defect Bright dot Black dot		Acceptable number : 4 bright dots Bright dot spacing : 5 mm or more				
			Acceptable number : 5 black dots Black dot spacing : 5 mm or more				
		Bright dot	Acceptable number : 2				
	2 dots join	Black dot	Acceptable numb				
	3 or more dots join		Acceptable number : 0				
	Total dot def	-	Acceptable number : 5 Max				
	White dot, BI	ack dot					
	(Circle)		Size(mn	•	A	cceptable Number	
	, , ,		d <			(neglected)	
			0.2 < d			5	
			0.4 < d	0.5		3	
			0.5 < d			0	
External inspection	Polarizer(Scr	atches)	Width(mm)	Length(r	nm)	Acceptable Number	
			W 0.1	-		(neglected)	
				L 5.0		(neglected)	
			0.1 <w 0.3<="" td=""><td colspan="2">5.0<l< td=""><td>0</td></l<></td></w>	5.0 <l< td=""><td>0</td></l<>		0	
			0.3 < W	0.3 <w -<="" td=""><td colspan="2">0</td></w>		0	
	Polarizer (Bubble, Dent)		Size(mm)		Δ	Acceptable Number	
			d < 0.2			(neglected)	
			0.2 <d 0.3<="" td=""><td></td><td>5</td></d>			5	
			0.3 < d 0.5			3	
			0.5 <d< td=""><td colspan="2">0</td></d<>		0		
			0.0 (0			,	
	Foreign Parti shape)	cle(Circular	Size(mm)		Acceptable Number		
			d < 0.2		(neglected)		
			0.2 < d	0.4	5		
			0.4 < d 0.5		3		
			0.5 < d		0		
	Foreign Particle(Linear shape),Scratches		Width(mm)	Length(r	nm)	Acceptable Number	
			W 0.03	-	,	(neglected)	
			0.03 < W 0.1	L 2.0		(neglected)	
				2.0 <l 4.0<="" td=""><td>3</td></l>		3	
				4.0 < L		0	
			0.1 <w< td=""><td>-</td><td></td><td>(According to Circular shape)</td></w<>	-		(According to Circular shape)	