SPEC. NO.	TQ3C-8EAFO-E1DDP08-00				
DATE	July 18, 2006				

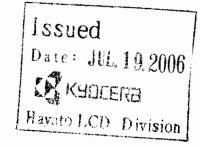
SPEC

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FOR		
LOK	•	

<u>TYPE: TCG057VGLAC-G00</u>

CONTENTS

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- 2. Construction and Outline
- 3. Mechanical Specifications
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KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by	:Engineering	Confirmed by :QA Dept.		
Issue Date	Prepared	Prepared Checked Appro		Checked	Approved
July 18, 2006	A Mishine	y Yanazaki	MFnitani	H. Stol	T. Minami

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

Date		Designed by:		Engineering D	ept.	Confirmed by: QA Dept.		
Da	Prepared		red	Checked	Approved	Checked	Approved	
Rev. No	ο.	Date		Page		Descriptio	ons	

1. Application

This data sheet defines the specification for a $(640 \times R.\,G.\,B) \times 480$ dot, amorphous silicon TFT transmissive color dot matrix type Liquid Crystal Display with LED backlight. [RoHS Compliant]

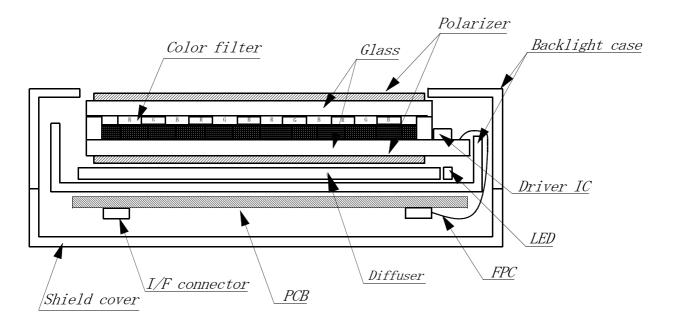
2. Construction and Outline

 $(640 \times R.G.B) \times 480$ dots, COG type LCD with LED backlight.

Backlight system : Side-edge type (LED).

Polarizer : Glare treatment.

Additional circuits: Timing controller, Power supply (3.3V input)



This drawing is showing conception only.

3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	144 (W) \times (104.8) (H) \times 13.0 (D)	mm
Effective viewing area	117.2 (W) × 88.4 (H)	mm
Dot number	(320×R.G.B) (W) × 240 (H)	Dots
Dot pitch	0.12 (W) × 0.36 (H)	mm
Display mode *1	Normally white	_
Mass	(220)	g

^{*1} Due to the characteristics of the LCD material, the color vary with environmental temperature.

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM		SYMBOL	Min.	Max.	UNIT
Power input voltage		VDD	0	4. 0	V
Input signal voltage	*1	Vin	-0.3	6. 0	V
Forward current	*2	IF	_	(27)	mA
Reversed voltage	*2	VR	_	(5)	V

*1 Input signals : CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q *2 For each : "AN1-CA1", "AN2-CA2", "AN3-CA3"

4-2. Environmental absolute maximum ratings

ITEM		SYMBOL	Min.	Max.	UNIT
Operating temperature	*1	Тор	-10	70	$^{\circ}\! \mathbb{C}$
Storage temperature	* 2	Tsto	-30	80	$^{\circ}\! \mathbb{C}$
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		-	* 5	* 5	_
Shock		_	*6	*6	_

- *1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- *2 Temp. = -30 °C < 48 h , Temp = 80 °C < 168 h Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock. LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard. (Please refers to 12. Precautions for use as detail).
- *3 Non-condensation.
- *4 Temp. \leq 40°C, 85%RH Max. Temp. > 40°C, Absolute Humidity shall be less than 85% RH at 40°C.

*5 Frequency $10\sim55~\mathrm{Hz}$ Converted to acceleration value : $(0.3\sim9~\mathrm{m/s^2})$ Interval $10-55-10~\mathrm{Hz}~1~\mathrm{minute}$

2 hours in each direction $\,$ X/Y/Z (6 hours as total) EIAJ ED-2531

*6 Acceleration: 490m/s^2 Pulse width: 11 ms

3 times in each direction : $\pm X/\pm Y/\pm Z$.

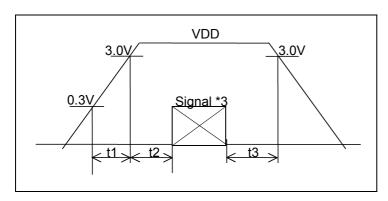
EIAJ ED-2531

5. Electrical Characteristics

 $VDD = +3.3V \pm 0.3V$, Temp. = $-10 \sim 70^{\circ}C$

ITEM		SYMBOL	MIN	TYP	MAX	UNIT
Power input voltage *1	VDD=3.3V	VDD	3. 0	3. 3	3. 6	V
Current consumption *2	۷۷۷–3. 3۷	IDD	_	210	270	mA
Permissive input ripple v	Vrp	-	_	100	mVp-p	
Input signal voltage (L	VIL	0	_	0. 3VDD	V	
Input signal voltage (H	igh) *3	VIH	0. 7VDD	_	VDD	V

*1 VDD-turn-on conditions



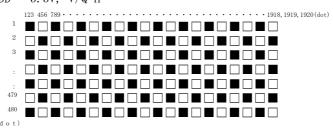
 $0 < t \ 1 \le 2 \ 0 \, ms$

 $0 < t \ 2 \le 5 \ 0 \, \text{ms}$

 $0 < t \ 3 \le 1 s$

*2 Power consumption Black & White pattern:

VDD = 3.3V, V/Q=H



*3 Input signals : CK, R0 \sim R5, G0 \sim G5, B0 \sim B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

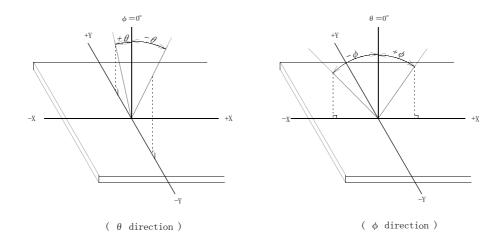
6. Optical Characteristics

Measuring points = ϕ 6.0mm , Temp. = 25°C

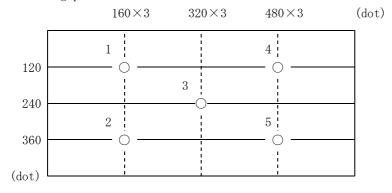
ITEM		SYMBOL	CONDITION		MIN	TYP	MAX	UNIT
Response	Rise	τr	θ =	$\theta = \phi = 0^{\circ}$		(10)	_	ms
time	Down	τd	θ =	$\phi = 0^{\circ}$	_	(25)	_	ms
		0		Upper	_	(80)	_	1
Vii		θ	CD > E	Lower	_	(70)	_	deg.
Viewing angle	range	,	CR≧ 5	Left	_	(80)	_	1
		φ		Right	_	(80)	_	deg.
Contrast rati	Contrast ratio		$\theta = \phi = 0^{\circ}$		(280)	(400)	_	_
Brightness		L	IF=(25mA)/1LED Line		(175)	(250)	_	$\mathrm{cd/m^2}$
	Red	X	0 – 1	0°	(TBD)	(TBD)	(TBD)	
		у	$\theta =$	$\theta = \phi = 0^{\circ}$		(TBD)	(TBD)	
	0.	X	0 -	0°	(TBD)	(TBD)	(TBD)	
Charamatiaita	Green	у	0 -	$\phi = 0^{\circ}$	(TBD)	(TBD)	(TBD)	_
Chromaticity coordinates	D1	X	0 -	0°	(TBD)	(TBD)	(TBD)	
	Blue	у	$\theta = \phi$	φ -0	(TBD)	(TBD)	(TBD)	
	White	X	0 -	1 -0°	(TBD)	(TBD)	(TBD)	
	White	<i>θ</i> =	$\phi = 0^{\circ}$	(TBD)	(TBD)	(TBD)		

6-1. Contrast ratio is defined as follows:

6-2. Definition of viewing angle



6-3. Measuring points



- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after LED is turned on. (Ambient Temp.=25°C)

7. Interface signals

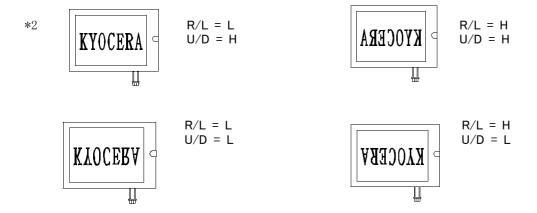
7-1. LCD

PIN NO.	SYMBOL	DESCRIPTION	I/0	Note
1	GND	GND	_	
2	CK	Clock signal for sampling each data signal	I	
3	Hsync	Horizontal synchronous signal (negative)	I	
4	Vsync	Vertical synchronous signal (negative)	I	
5	GND	GND	_	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	_	
13	GO	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	_	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	В5	BLUE data signal (MSB)	I	
26	GND	GND	_	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	VDD	3.3V power supply	_	
29	VDD	3.3V power supply	_	
30	R/L	Horizontal display mode select signal	I	*2
	/-	L : Normal , H : Left / Right reverse mode		
31	U/D	Vertical display mode select signal	I	*2
	/-	H : Normal , L : Up / Down reverse mode		
32	V/Q	H: Normal	I	
33	GND	GND	_	

LCD side connector : 08-6210-033-340-800+ (ELCO)

Recommended matching connector : FFC or FPC(P=0.5mm)

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2. Don't keep ENAB "High" during operation.



7-2. LED

PIN NO.	SYMBOL	DESCRIPTION
1	AN1	Anode1
2	AN2	Anode2
3	AN3	Anode3
4	CA1	Cathode1
5	CA2	Cathode2
6	CA3	Cathode3

LCD side connector : SHLP-06V-S-B (JST)
Recommended matching connector : SM06B-SHLS-TF (JST)

: SMO6B-SHLS-TF(LF)(SN) (JST) · · · (RoHS compliant)

8. Timing Characteristics of input signals

8-1. Timing characteristics

ITEM			MIN	TYP	MAX	UNIT	NOTE
	Frequency	1/Tc	_	25. 18	28. 33	MHz	V/Q=H
Clock	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	_	_	ns	
рата	Hold time	Tdh	10	_	_	ns	
Hanisantal arma	Cvcle	TH	30.0	31. 8	_	μs	V/Q=H
Horizontal sync. signal	Cycle	П	770	800	900	clock	v / W—П
	Pulse width	ТНр	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	V/Q=H
signal	Pulse width	TVp	2	_	34	line	
Horizontal displa	y period	THd		640		clock	
HsyncClock phase difference		ТНс	10	_	Tc-10	ns	
HsyncVsync. phase difference		TVh	0	_	ТН-ТНр	ns	
Vertical sync.sig	TVs		34		line	V/Q=H	
Vertical display	period	TVd		480		line	

^{*}In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position The horizontal display position is determined by ENAB signal.

ITI	SYMBOL	MIN	TYP	MAX	UNIT	NOTE	
Fa-1-1 : 1	Set up time	Tes 5		_	Tc-10	ns	
Enable signal Pulse width		Тер	2	640	TH-10	clock	
HsyncEnable signal phase difference		The	44	_	104	clock	V/Q=H

*When ENAB is fixed at "V/Q=H", the display starts from the data of C104(clock) as shown in 8-5.

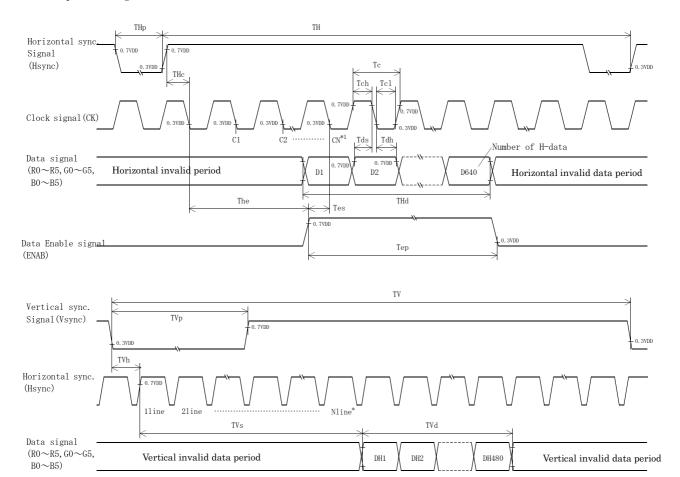
8-3. Vertical display position

The vertical display position (TVs) is fixed at 34th line (V/Q=H). Note) ENAB signal is independent of vertical display position.

8-4. Input Data Signals and Display position on the screen

D1,DH1	D2,DH1	D3,DH1		D640,DH1
D1,DH2	D2,DH2	D3,DH2		
	:	F		
		<u> </u>	R G B	
	•			
D1,DH480	D2,DH480	D3,DH480		

8-5. Input Timing Characteristics



- *1 When ENAB is fixed "V/Q=H" the display starts from the data of C104(Clock)
- *2 The vertical display position(TVs) is fixed at 34th line.

9. Backlight Characteristics

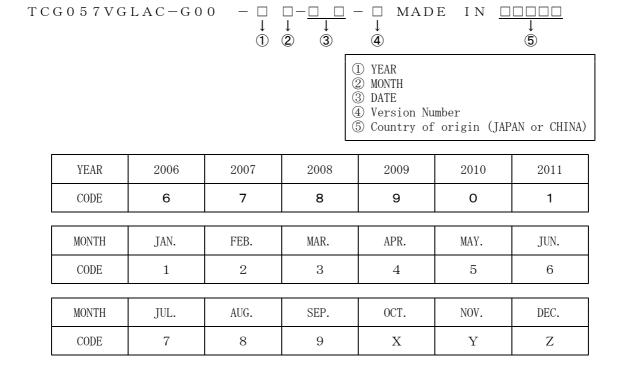
Temp. = 25° C

ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Forward current	*1	IF		(25)	_	mA	Ta = −10~70°C
			_	(24. 2)	(27. 0)	V	IF=25mA *1, Ta=−10°C
Forward voltage		VF	_	(23. 1)	(25. 9)	V	IF=25mA *1, Ta=25℃
			_	(22. 1)	(24. 9)	V	IF=25mA *1, Ta=70℃
Operating life	*2	Т	-	(50, 000) *3		V	IF=25mA *1

^{*1} For each "AN1-CA1", "AN2-CN2" and "AN3-CN3"

10. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.



^{*2} When brightness decrease 50% of initial brightness.

^{*3} Life Time is estimated data.

^{*} An input current below 8.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

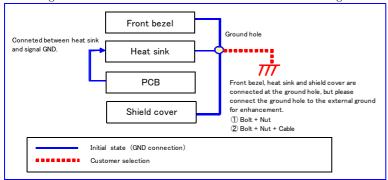
11-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

12. Precautions for use

12-1. Installation of the LCD

1. The LCD's bezel must be grounded. The heat sink and shield cover are connected at the ground hole. The ground hole is located on the right side of the LCD when viewed from the front. The ground hole must be connected to an external ground.



- 2. A transparent protection sheet shall be added to protect the LCD and its polarizers.
- 3. The LCD shall be installed so that there is no pressure on the LSI chips.
- 4. The LCD shall be installed flat, without twisting or bending.
- 5. The display window size should be the same as the effective viewing area.
- 6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 7. Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque:3.3 \pm -0.3kgf.cm Please set up'SPEED-LOW', 'SOFT START-SLOW' when using electric driver .

Recommendable screw JIS tapping screw two types nominal dia.3.0mm installing boss hole depth 3.5 \pm 0.5mm

Please be careful not to use high torque which may damage LCD module in installation.

- 8. A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.
- 9. Do not pull the LED lead wires and do not bend the root of the wires. Housing should be designed to protect LED lead wires from external stress.
- 10. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.

Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

12-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

12-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.

It may also change the characteristics of the liquid crystal.

This phenomenon may not recover. The LCD shall be operated within the temperature limits specified.

12-4. Storage

- 1. The LCD shall be stored within normal temperature and humidity. Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. The LCD should be packaged to prevent damage.

12-5. Handling

- 1. <u>DO not</u> store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
- 2. The front polarizer is easily scratched or damaged.

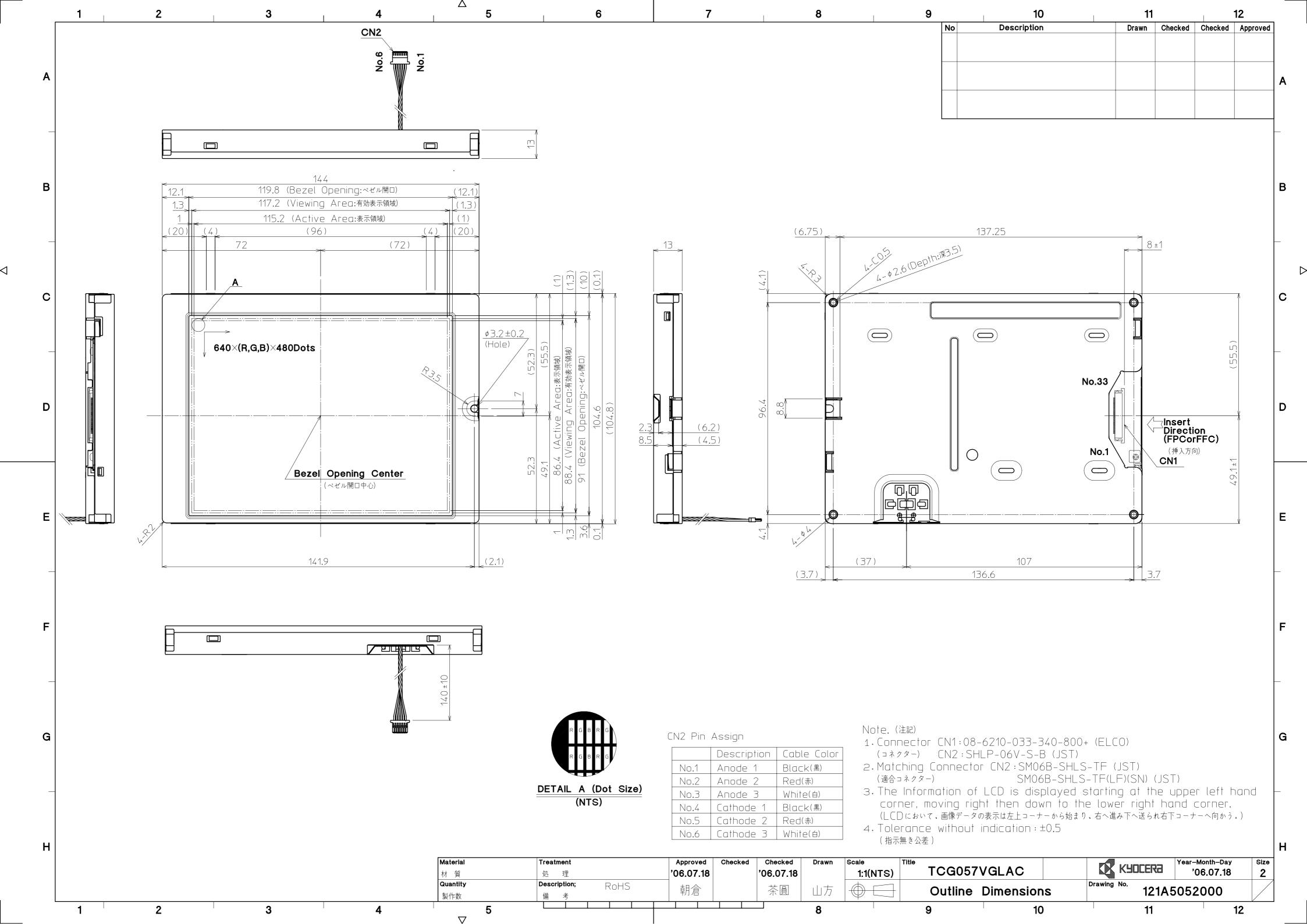
Prevent touching it with any hard material, and from being pushed or rubbed.

- 3. The LCD screen may be cleaned with a soft cloth or cotton pad.
 - Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
- 4. Water may cause damage or discoloration of the polarizer. Clean any condensation or moisture from any source immediately.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.
- 6. Do not disassemble LCD module because it will result in damage.
- 7. Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend to use screen saver etc. in cases where a solid-base image pattern must be used.
- 8. Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body, rinse it off right away with water and soap.

13. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	80°C	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	-30℃	240 h	Low Temp. Bubble: None Solid Crystallization of Liquid Crystal: None Display Quality: No defect Display Function: No defect Current Consumption: No defect
High Temp. Humidity Atmosphere	40℃ 90 %RH	240 h	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-30°C 0.5 h R.T. 0.5 h 80°C 0.5 h	10 cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	70°C	500 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect

- st Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- st The LCD is tested in circumstances in which there is no condensation.
- st The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.
- * The reliability test is not an out-going inspection.
- * The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.



SPEC. NO.	TQ3C-8EAF0-E2DDP09-00
DATE	July 18, 2006

FOR	•	
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KYOCERA INSPECTION STANDARD

TYPE: TCG057VGLAC-G00

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed	by :Engineer	Confirmed by :QA Dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
July 18, 2006	X. Mishin	J. Yamazahi	M. Fujitani	To Arch	T. minami

Revision Record

Date		Design	ed by:	Engineering D	ept.	Confirmed by: QA Dept.		
Da	аге		Prepa	red	Checked	Approved	Checked	Approved
Rev. No	ο.	Date		Page		Descriptio	ons	

1) Note

	Note						
General	shall be revi		defined within this inspection standard an additional standard shall be				
	Luminance	Inspection distance : 300 mm (from the sample) Temperature : 25 \pm 5 $^{\circ}\mathrm{C}$					
Definition of Inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool:5% Transparency neutral density filter. Count dot:If the dot is visible through the filter Don't count dot:If the dot is not visible through the filter. RGBRGBRGB RGBRGBRGB RGBRGBRGB				
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "white" data sent to the screen.				
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.				
			R G B R G B R G B R G B R G B R G B R G B				
	External inspection	Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixcels "Black" or "White") and non operating.				
		Appearance inspection	Does not satisfy the value at the spec.				
	Others	CFL wires	Damaged to the CFL wires, connector, pin, functional failure or appearance failure.				
	Definition of size	Definition of ci	Definition of linear size				

2) Standard

Classification		Inspecti	ion item	Judgement standard				
defect (in LCD	Dot defect	Bright dot de	fect	Acceptable number			t dots defects	
glass)		Black dot def	ect	Acceptable number : 5 black dots defects Black dot spacing : 5 mm or more				
		2 dots join	Bright dot defect	Acceptable numb	er : 2			
			Black dot defect	Acceptable numb	er : 3			
		3 or more dot	s join	Acceptable numb	er : 0			
		Total dot def	ects	Acceptable number	er : 5 l	Max		
	Others	White dot, Da	rk dot	Size(mm	1)	A	cceptable Number	
		(Circle)		d<0			(neglected)	
				0. 2 < d≦0			5	
				0.4 <d≦< td=""><td></td><td></td><td>3</td></d≦<>			3	
				0.5 <d< td=""><td></td><td></td><td>0</td></d<>			0	
				L				
External ins	pection	Polarizer(Scr	atches)	Width (mm)	Length (m	nm)	Acceptable Number	
(Defect o Polarize	er or			W≦0.1	_		(neglected)	
between -er and				0.1<₩≦0.3	L≦5.0		(neglected)	
glass)					5.0 <l< td=""><td></td><td>0</td></l<>		0	
				0.3 <w< td=""><td>_</td><td></td><td colspan="2">0</td></w<>	_		0	
		Polarizer Tou (Bubble, Dent	ch panel)	Size(mm)		A	cceptable Number	
				d<0.2		(neglected)		
				0.2 < d ≤ 0.3			5	
				$0.3 < d \le 0.5$			3	
				0.5 <d< td=""><td colspan="2">0</td></d<>		0		
		Foreign Parti	cle(Circular					
		shape)	•	Size(mm)		Acceptable Number		
				d<0.2		(neglected)		
				0. 2 < d≦0	0.4		5	
				0. 4 < d≦0	0.5		3	
				0.5 <d 0<="" td=""><td>0</td></d>			0	
		Foreign Parti	cle	W; 1+1- ()	I1 /	\	Assentable No. 1	
		(Linear shape Scratches	<i>)</i> ,	Width (mm)	Length (m	ım <i>)</i>	Acceptable Number	
				W≦0.03	- r < 0	2 0	(neglected)	
				$0.03 < W \le 0.1$	L≦2.0		(neglected)	
				$ 0.03 \setminus W \cong 0.1 $	2. 0 < L ≤ 4. 0 4. 0 < L		0	
				0.1 <w< td=""><td></td><td></td><td>(According to</td></w<>			(According to	
				U. 1 \ W			Circular shape)	