SPEC.NO.	TQ3C-8EAFO-E1DDM09-00
DATE	November 15, 2006

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TYPE: TCG062HV1AG-G00

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Issued
Date: Nov.28,2006
KYDDERA
Hayato LCD Division

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

This specification is subject to change. Consult Kyocera before ordering.

Original	Designed by	:Engineering	Confirmed by :QA Dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
November 15, 2006	J. Yonazohi	A. Mishino	4 Matsuno to	To Aut	T. Minami

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

Designed by:		Engineering D	ept.	Confirmed by: QA Dept.				
Da	аге		Prepared		Checked	Approved	Checked	Approved
Rev. No	ο.	Date		Page		Descriptio	ons	

1. Application

This data sheet defines the specification for a $(640 \times R.G.B) \times 240$ dot, amorphous silicon TFT transmissive color dot matrix type Liquid Crystal Display with CFL backlight.

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2. Construction and Outline

(640 x R.G.B) x 240 dots, COG type LCD with CFL backlight.

Backlight system : Side-edge type CFL (1 tube).

Inverter : Option

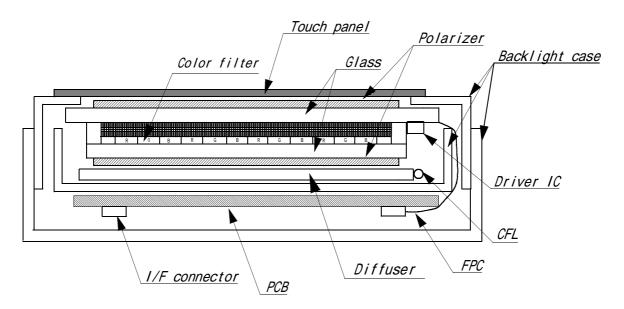
Recommended InverterCXA-L0612A-VJL (TDK)

or equivalent

Polarizer : Glare treatment

Additional circuits: Timing controller, Power supply (3.3V input)

Touch panel : Analog type. Non-Grare treatment.



This drawing is showing conception only.

3. Mechanical Specifications

3-1. Mechanical specification of LCD panel

ITEM	SPECIFICATION	UNIT
Outline dimensions	174.2 (W) × 73.4 (H) × 14.0 (D)	mm
Effective viewing area	149.8 (W) × 57.4 (H)	mm
Dot number	(640×R.G.B) (W) × 240 (H)	Dots
Dot pitch	0.077 (W) × 0.231 (H)	mm
Display mode *1	Normally white	-
Mass	(200)	g

^{*1} Due to the characteristics of the LCD material, the color vary with environmental temperature.

3-2. Mechanical Specifications of touch panel

ITEM	ITEM SPECIFICATION			
Input	Radius-0.8 stylus or Finger	1		
Actuation Force	0.5N ± 0.3N	-		
Transmittance	Тур.80	%		
Surface hardness	pencil hardness 2H or more according	-		

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	Min.	Max.	UNIT
Power input voltage	VDD	0	4.0	V
Input signal voltage *1	Vin	-0.3	6.0	V
Touch panel supply voltage	Vtp	0	6.0	V
Touch panel Input current	Itp	0	0.5	mA

^{*1} Input signals : CK, RO~R5, GO~G5, BO~B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

4-2. Environmental absolute maximum ratings

ITEM		SYMBOL	Min.	Max.	UNIT
Operating temperature	*1	Тор	-10	70	
Storage temperature	*2	Tsto	-30	80	
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

^{*1} Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25 , another temperature range should be confirmed.

*2 Temp. = -30 < 48 h , Temp = 80 < 168 h Store LCD panel at normal temperature/humidity.

Keep it free from vibration and shock.

LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard. (Please refers to 12. Precautions for use as detail).

*3 Non-condensation.

*4 Temp. 40 , 85%RH Max.

Temp. > 40 , Absolute Humidity shall be less than 85% RH at 40 .

*5

Frequency	10 ~ 55 Hz	Converted to acceleration value :
Vibration width	0.15 mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10 Hz	1 minute

2 hours in each direction $\ensuremath{\text{X/Y/Z}}$ (6 hours as total) EIAJ ED-2531

*6 Acceleration: 490m/s² Pulse width: 11 ms

3 times in each direction : $\pm X/\pm Y/\pm Z$.

EIAJ ED-2531

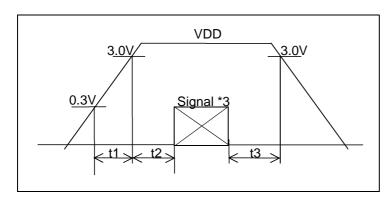
5. Electrical Characteristics

5-1. LCD

VDD =	+3.3V	±	0.3V	,	${\hbox{\rm Temp}}.$	=	-10 ~ 70
-------	-------	---	------	---	----------------------	---	----------

ITEM		SYMBOL	MIN	TYP	MAX	UNIT
Power input voltage *1	VDD 2 2V	VDD	3.0	3.3	3.6	V
Current consumption *2	VDD=3.3V Temp.=25	IDD	-	230	300	mA
Permissive input ripple v	VRP	-	-	100	mVp-p	
Input signal voltage (L	VIL	0	-	0.3VDD	V	
Input signal voltage (H	igh) *3	VIH	0.7VDD	-	5.5	V

*1 VDD-turn-on conditions



0 < t 1 20 ms

0 < t 2 5 0 ms

0 < t 3 1s

*2 Power consumption

Black & White pattern:

*3 Input signals : CK, RO~R5, GO~G5, BO~B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

5-2. Touch Panel

5-2-1. Terminal resistance

Between xL and xR : 200 \sim 1000 Between yU and yL : 200 \sim 1000

5-2-2. Linearity

±1.5% x : 1.5% or less

y : 1.5% or less

5-2-3. Insulation resistance 100M or more at DC25V

6 . Optical Characteristics

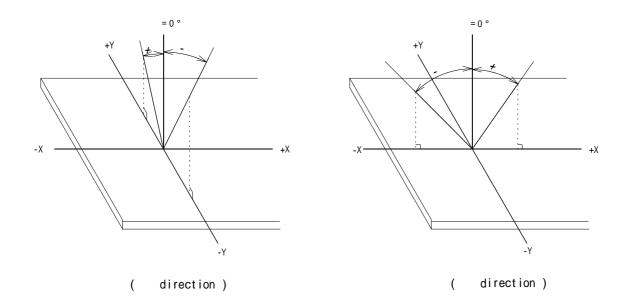
Measuring points = 6.0mm, Temp.=25

ITEM		SYMB0 L	CONDITION		MIN	TYP	MAX	UNIT
Response time	Rise	r	=	=0°	-	10	-	ms
time	Down	d	=	=0°	-	15	-	ms
				Upper	1	85	-	doa
Viewing angle	rongo		CR 5	Lower	ı	50	-	deg.
Viewing angle	range		CK 5	Left	1	75	-	doa
				Right	-	75	-	deg.
Contrast rati	0	CR	= =0°		300	500	-	-
Luminance(Bri	ghtness)	L	IL=5.0mArms		200	280	-	cd/m²
	Red	х	_	_0°	0.56	0.61	0.66	
		у	= =0°		0.29	0.34	0.39	
	Green	х	=	=0°	0.26	0.31	0.36	
Chromaticity	Green	у	=	=0	0.49	0.54	0.59	-
coordinates	Blue	х	=	O°	0.10	0.15	0.20	
	Diue	у	= 	=0	0.07	0.12	0.17	
	White	х	=	_0°	0.25	0.30	0.35	
	WIIILE	у	=	- U	0.27	0.32	0.37	

Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing angle of = = 0°.

6-1. Definition of Contrast (Transmissive Mode)

6-2. Definition of viewing angle



6-3. Measuring points

	160 × 3	320 x 3	480 × 3	(dot)
	1	1 1 1	4	
60		3		
120	! ! !			
100	2	! ! !	5	
180		 	<u></u>	
(dot)	<u>'</u>	<u> </u>	<u> </u>	

- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after CFL is turned on. (Ambient Temp.=25)
- 3) The inverter should meet the rating of the CFL ;
 - -Sine, symmetric waveform without spike in positive and negative.

7. Interface signals

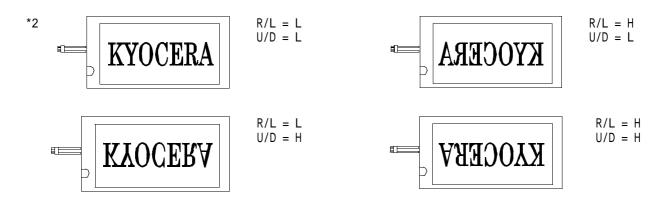
7-1. LCD

PIN NO.	SYMBOL	DESCRIPTION	1/0	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	Hsync	Horizontal synchronous signal (negative)	ı	
4	Vsync	Vertical synchronous signal (negative)	ı	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	GO	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	B0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	B3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	VDD	3.3V power supply	-	
29	VDD	3.3V power supply	-	
30	R/L	Horizontal display mode select signal	I	*2
		H : Normal , L : Left / Right reverse mode		
31	U/D	Vertical display mode select signal	I	*2
		L : Normal , H : Up / Down reverse mode		
32	V/Q	H : Normal (VGA)		
33	GND	GND	-	

LCD side connector : 08-6210-033-340-800+ (ELCO)

Recommended matching FFC or FPC : P=0.5mm

^{*1} The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2. Don't keep ENAB "High" during operation.



7-2 . C F L

PIN NO.	SYMBOL	DESCRIPTION				
1	Hot	Inverter output high voltage side				
2	NC					
3	Cold	Inverter output low voltage side				

LCD side connector : BHR-03VS-1 (JST)
Recommended matching connector : SM02(8.0)B-BHS-1 (JST)

: SM02(8.0)B-BHS-1 (JST) : SM02(8.0)B-BHS-1-TB(LF)(SN) (JST)···(RoHS Compliant)

* Please be careful NOT to connect inversely an inverter-output high voltage side to the CFL low voltage side. It may result in damage or electric chock.

7-3. Touch panel

PIN No.	SYMBOL	DESCRIPTION			
1 yU		y-Upper terminal			
2	xL	x-Left terminal			
3	yL	y-Lower terminal			
4	xR	x-Right terminal			

8 . Timing Characteristics of input signals

8-1. Timing characteristics

ITE	SYMBOL	MIN	TYP	MAX	UNIT	NOTE	
Clock	Frequency	1/Tc	-	25.18	28.33	MHz	V/Q=H
	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	-	-	ns	
Data	Hold time	Tdh	10	-	-	ns	
Horizontal sync.	Cycle	TH	30.0	31.8	-	μs	V//O II
signal		IΠ	770	800	900	clock	V/Q=H
	Pulse width	ТНр	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	V/Q=H
signal	Pulse width	TVp	2	-	34	line	
Horizontal displa	y period	THd	640			clock	
HsyncClock phas	HsyncClock phase difference		10	-	Tc-10	ns	
HsyncVsync. phase difference		TVh	0	-	TH-THp	ns	
Vertical sync.sig	TVs	34			line	V/Q=H	
Vertical display	period	TVd		240		line	

^{*}In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position The horizontal display position is determined by ENAB signal.

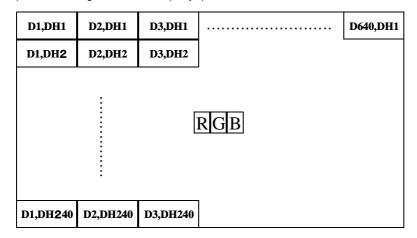
ITI	SYMBOL	MIN	TYP	MAX	UNIT	NOTE	
Enchlo oignal	Set up time	Tes	5	-	Tc-10	ns	
Enable signal	Pulse width	Тер	2	640	TH-10	clock	
HsyncEnable signal phase dif	The	44	-	TH-664	clock	V/Q=H	

^{*} When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.

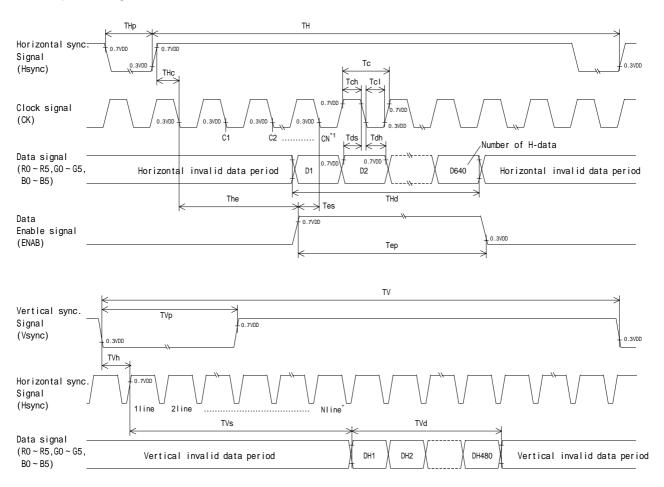
8-3. Vertical display position

The vertical display position (TVs) is fixed at 34th line (V/Q=H). Note) ENAB signal is independent of vertical display position.

8-4. Input Data Signals and Display position on the screen



8-5. Input Timing Characteristics



- *1 When ENAB is fixed "Low" the display starts from the data of C104(Clock).
- $^{\star}2$ The vertical display position(TVs) is fixed at 34^{th} line.

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting discharge Voltage	VS	-	-	1,077 Vrms	-10
discharge vortage *1	VS	-	-	645 Vrms	25
Discharging tube current *2	IL	3.0 mArms	5.0 mArms	6.0 mArms	-
Discharging tube voltage	VL	-	395 Vrms	-	-
Operating life *3 (IL=5.0 mArms)	Т	36,000 h	54,000 h	-	-
Operating frequency *4	F	40 kHz	-	100 kHz	-

- *1 The Non-load output voltage (VS) of the inverter should be 1.3 times the maximum VS at the low temperature to provide margin to assure that the CFL will start, because actual VS may increase due to leakage current from the CFL cables. (Reference value: 1,400 Vrms Min)
- *2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.
- *3 End of life is defined as when the illuminance or quantity of light has decreased to 50% of the initial value. Illuminance of light will drastically decrease when LCD is operated at lower temperature for long hours.
- *4 The driving frequency of the CFL may interfere with the horizontal synch signal ,leaving interference stripes on the display. So please evaluate LCD panels beforehand.

 To avoid interference stripes, we recommend to separate as far as possible the CFL frequency from the horizontal synchronous signal and its high harmonic frequency.
- * There may be cases where interface noise on LCD PCB, generated by high-voltage products such as inverters, may leave stripes on the display. Please be careful when designing a mold to take into consideration that the inverter shall be located as far as possible from PCB. Shield protection may be effective.
- * Prolonged storage in darkness and/or low temperature may slow the ignition and rise to full brightness of the CFL in an LCD Module. Please use an inverter designed to provide sufficient driving voltage for more than 1 second. Also a decreased Starting Discharge Voltage or shortened ignition time may not turn ON the CFL lamp.

1 0 . Design Guidance for Analog Touch-Panel(T/P)

10-1. Electrical

In customer's design, please remember the following considerations.

- 1. Do not use the current regulated circuit.
- 2. Keep the current limit with top and bottom layer. (See Sec, 4-1)
- 3. Analog T/P can not sense two point touching separately.
- 4. A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the T/P position data.
- 5. Analog T/P is also a "Capacitor" in an equivalent circuit. Design your sensing circuit and low-pass filter with considering this "Capacitor" value.
- 6. Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

10-2. Software

- 1. Do the "User Calibration".
- 2. "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3. When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

10-3. Mounting on display and housing bezel

- 1. Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- 2. Never expand the T/P top layer (PET-film) like a balloon by internal air pressure. The life of the T/P will be extremely short.
- 3. If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur.

11. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

TCG062HV1AG-G00 - - ____ - MADE IN _____

YEAR MONTH DATE Version Number Country fo origin(Japan or China)

YEAR	2006	2007	2008	2009	2010	2011
CODE	6	7	8	9	0	1
MONTH	JAN.	FEB.	MAR.	APR.	MAY.	JUN.
CODE	1	2	3	4	5	6
MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	Х	Υ	Z

12. Warranty

12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

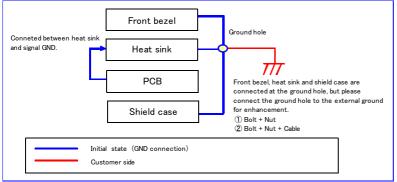
12-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

13. Precautions for use

13-1. Installation of the LCD

1. LCD hole(left side) are not connected with GND, but the LCD is structured to have GND connection available to protect against noise. We recommend to connect customer's frame GND to LCD frame in order to stabilize the display performance.



- 2. The LCD shall be installed so that there is no pressure on the LSI chips.
- 3. The LCD shall be installed flat, without twisting or bending.
- 4. The display window size should be the same as the effective viewing area.
- 5. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 6. Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque:0.265 ± 0.025N.m

Please set up'SPEED-LOW', 'SOFT START-SLOW' when using electric driver .

Recommendable screw P-TITE screw two types nominal dia.3.0mm installing boss hole depth 4.2 + /-0.5 mm

Please be careful not to use high torque which may damage LCD module in installation.

- 8. Do not pull the CFL lead wires and do not bend the root of the wires.
- Housing should be designed to protect CFL lead wires from external stress.
- 9. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.

 Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

13-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

13-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.

It may also change the characteristics of the liquid crystal.

This phenomenon may not recover. The LCD shall be operated within the temperature limits specified.

13-4. Storage

- 1. The LCD shall be stored within normal temperature and humidity.

 Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. Always store the LCD so that it is free from external pressure onto it.

13-5. Handling

- 1. <u>DO NOT</u> store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
- 2. The front polarizer is easily scratched or damaged.

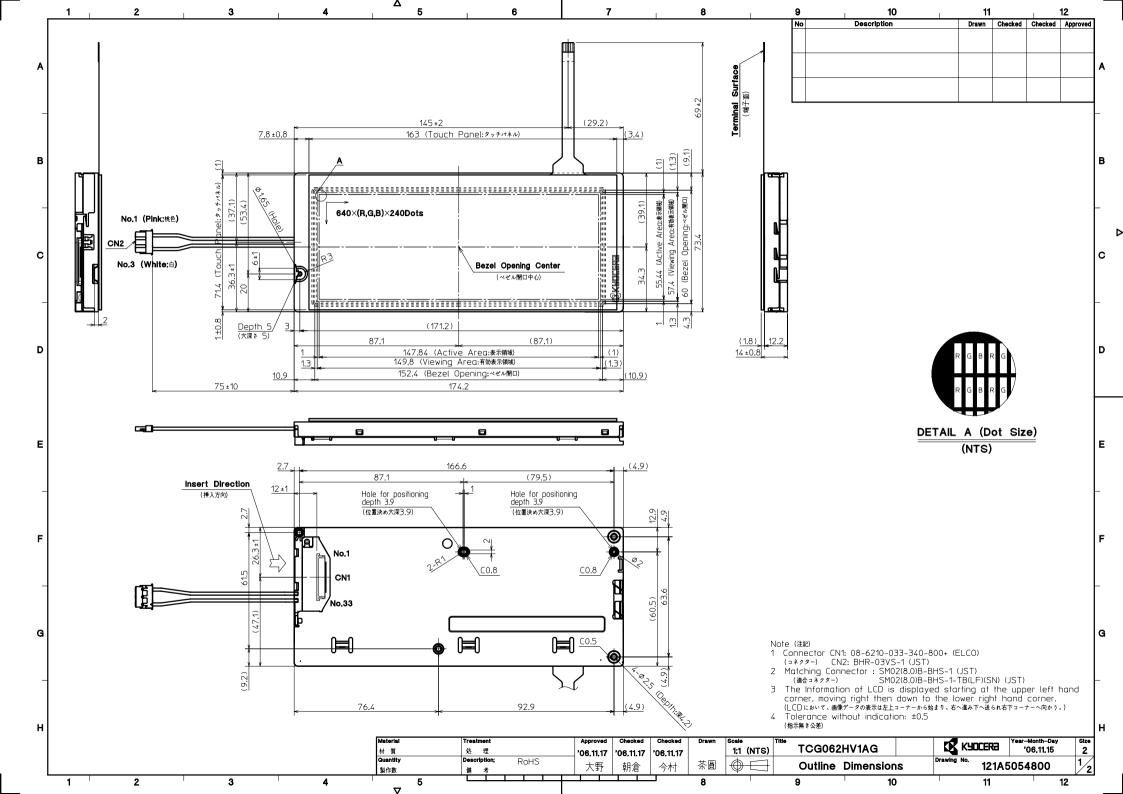
 Prevent touching it with any hard material, and from being pushed or rubbed.
- 3. The LCD screen may be cleaned with a soft cloth or cotton pad.

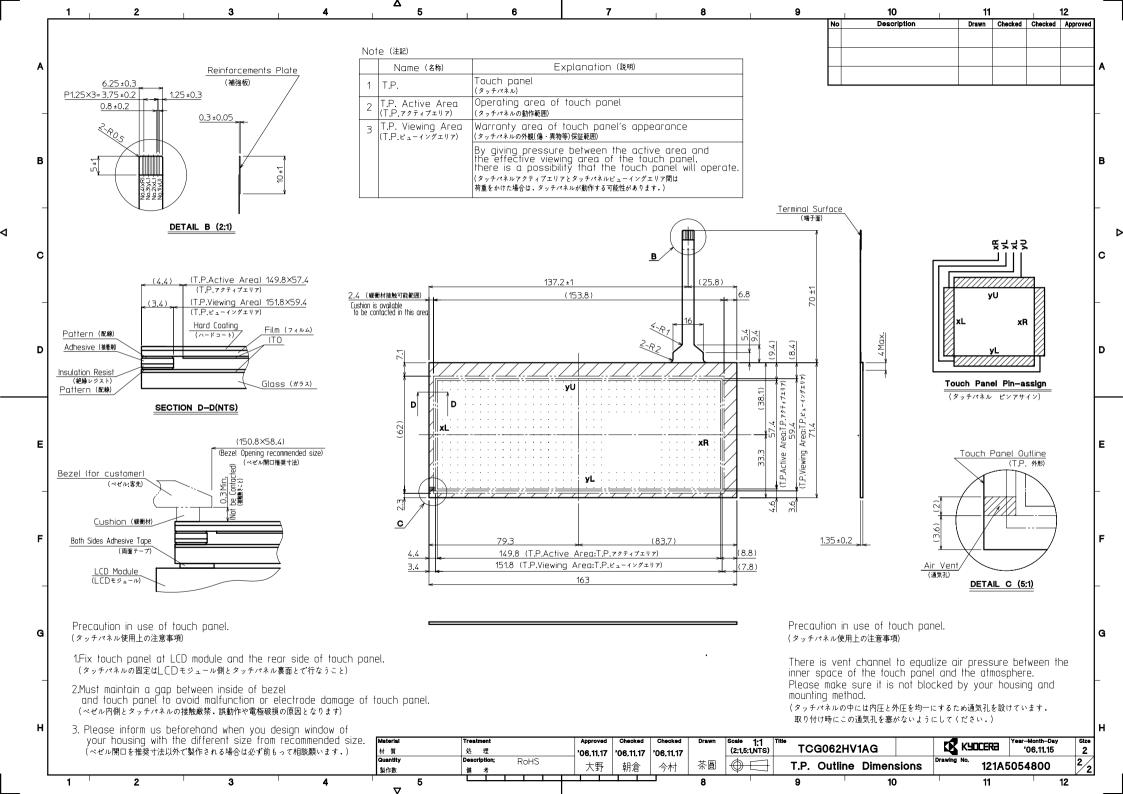
 Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
- 4. Water may cause damage or discoloration of the polarizer.
 Clean any condensation or moisture from any source immediately.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.
- 6. Do not disassemble LCD module because it will result in damage.
- 7. Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend to use screen saver etc. in cases where a solid-base image pattern must be used.
- 8. Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body, rinse it off right away with water and soap.

14 . Reliability Data / Environmental Test

TEST ITEM			RESULT
			Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	-30	240 h	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	midity 90 %RH		Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-30 0.5 h R.T. 0.5 h 80 0.5 h	10 cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation			Display Quality : No defect Display Function : No defect Current Consumption : No defect
Point Activation life	Polyacetal stylus (R0.8) Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance : Insulation resistance Linearity : Actuation Force

- * Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- * The LCD is tested in circumstances in which there is no condensation.
- * The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.
- * The reliability test is not an out-going inspection.
- * The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.





SPEC.NO.	TQ3C-8EAF0-E2DDM09-00				
DATE	November 15, 2006				

FOR:

KYOCERA INSPECTION STANDARD

<u>TYPE</u>: TCG062HV1AG-G00

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed	by :Engineer	Confirmed by :QA Dept.		
Issue Date	Issue Date Prepared		Approved	Checked Approved	
November 15, 2006	y. Yonazohi	A. Mishino	4. Matsunoto	To , Suf	T. Minami

Revision Record

Date		Designed by:		Engineering D	ept.	Confirmed by: QA Dept.		
Da	Prepared		red	Checked	Approved	Checked	Approved	
Rev. No	ο.	Date		Page		Descriptio	ons	

1) Note

	Note						
General	1. Customer identified anomalies not defined within this inspection shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent.						
	2. Inspection Conditions Luminance : 500 Lux minimum Inspection distance : 300 mm (from the sample) Temperature : $25 \pm 5 \degree C$ Direction : directly above						
Definition of Inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool:5% Transparency neutral density filter. Count dot:If the dot is visible through the filter Don't count dot:If the dot is not visible through the filter. RGBRGBRGB RGBRGBRGB RGBRGBRGB				
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "white" data sent to the screen.				
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.				
			RGBRGBRGB RGBRGBRGB RGBRGBRGB				
	External inspection	Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixcels "Black" or "White") and non operating.				
		Appearance inspection	Does not satisfy the value at the spec.				
	Others	CFL wires Damaged to the CFL wires, connectional failure or appearance					
	Definition of size	Definition of ci	Definition of linear size				

2) Standard

Classification Inspection item		Judgement standard						
defect (in LCD	Dot defect	Bright dot defect		Acceptable number : 4 bright dots defects Bright dot spacing : 5 mm or more				
glass)	Black dot defect		Acceptable number					
		2 dots join	Bright dot defect	Acceptable numb				
			Black dot defect	Acceptable numb				
Others		3 or more dots join		Acceptable number : 0				
		Total dot defects		Acceptable number : 5 Max				
		White dot, Dark dot (Circle)		Size (mm) Acceptable Numbe				
				d<		(neglected)		
				0.2 <d≦< td=""><td></td><td colspan="3">5</td></d≦<>		5		
				0.4 <d≦< td=""><td></td><td colspan="3">3</td></d≦<>		3		
						0		
		- 1 · 1 · 1						
	pection	Polarizer (Scratches)		Width (mm)	Length(n	nm)	Acceptable Number	
(Defect o Polarize	er or			W≦0.1	-		(neglected)	
between -er and	Polariz LCD			0.1/W<0.2	L≦5.0		(neglected)	
glass)				$0.1 < W \le 0.3$	5.0 <l< td=""><td colspan="2">0</td></l<>		0	
				0.3 <w< td=""><td colspan="2">-</td><td colspan="2">0</td></w<>	-		0	
		Polarizer Touch panel (Bubble, Dent)						
				Size(mm)		Acceptable Number		
				d<0.2		(neglected)		
				0. 2 < d ≤ 0. 3		5		
				$0.3 < d \le 0.5$ $0.5 < d$		3		
						0		
		Foreign Particle(Circular		Size(mm)		Acceptable Number		
		shape)		d<0.2		(neglected)		
				$0.2 < d \le 0.4$		(negrected) 5		
				$0.4 < d \le 0.5$		3		
				0. 1 < d = 0. 0		0		
		Foreign Particle (Linear shape), Scratches		Width(mm) Length		mm) Acceptable Number		
				W≦0.03			(neglected)	
				0.03<₩≦0.1	L≦2.0		(neglected)	
					2.0 <l≦4.0< td=""><td colspan="2">3</td></l≦4.0<>		3	
					4.0 <l< td=""><td colspan="2">0</td></l<>		0	
				0.1 <w -<="" td=""><td colspan="2">(According to Circular shape)</td></w>		(According to Circular shape)		

Inspection item			lud	nement et	andard				
mopeotion item	Judgement standard								
	(D = Average Diameter = (major axis + minor axis) / 2 Width(mm) Length(mm) Acceptable No.								
	Caratab		/> 0.08	4 L 6 L		within	30mm 20mm		
Scratch,	Scratch		/ > 0.05 / > 0.03	6 L 10 L		within within	20mm 20mm		
Foreige particle		0.03 W		20 L	2000	neglected			
(Touch Screen portion)	Foreige		/> 0.05	5 L	2pcs	within	30mm		
<u> </u>	(line 0.05 W neglected neglect						I		
	like)								
	Foreige				2pcs	2pcs within 30mm			
	(circle	0.2 D			neglected				
	TIKe)	like)							
Close ereck	Above are applied to the visible area. Unless there are foreign particles and damage affected seriously to the electrical performance in the visible area, we approve of this product.								
Glass crack (Touch Screen portion)		Corner crack			Cracks in other area				
				/	than in corner				
	Judgeme					+ × × 1/1/11			
	nt stan								
	dard								
		Х	Y	Z	Х	Y	Z		
		3	3	<t< td=""><td>5</td><td>1.5</td><td><t< td=""></t<></td></t<>	5	1.5	<t< td=""></t<>		
		2pcs/panel			2pcs/side				
		Progressive crack							
	Judgeme nt stan dard								
		Opcs(NG even 1pcs)							
	Above are applied to the visible area. Unless there are foreign particles and damage affected seriously to the electrical performance in the visible area, we approve of this product.								